Vishay Siliconix

## 50 A VRPower® Integrated Power Stage

#### **DESCRIPTION**

The SiC654 is a high frequency integrated power stage optimized for synchronous buck applications to offer high current, high efficiency, and high power density performance with very low shutdown current. Packaged in Vishay's proprietary 5 mm x 5 mm MLP package, SiC654 enables voltage regulator designs to deliver up to 50 A continuous current per phase.

The internal power MOSFETs utilize Vishay's latest TrenchFET® technology that delivers industry benchmark performance to significantly reduce switching and conduction losses.

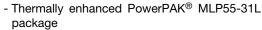
The SiC654 incorporates an advanced MOSFET gate driver IC that features high current driving capability, adaptive dead-time control, an integrated bootstrap switch, and user selectable zero current detection to improve light load efficiency. The driver is also compatible with a wide range of PWM controllers, supports tri-state PWM, and 5 V / 3.3 V PWM logic.

The device also supports PS4 mode to reduce power consumption when the system is in standby state.

The SiC654 offers operating temperature monitoring, protection features, and warning flags that improve system monitoring and reliability.

#### **FEATURES**

Highly efficient





- Vishay's latest TrenchFET technology and low side MOSFET with integrated Schottky diode

- Integrated, low impedance, bootstrap switch
- Power MOSFETs optimized for 19 V input stage
- Supports PS4 mode light load requirement with low shutdown supply current (5 V, 3 μA)
- Zero current detection for improved light load efficiency
- · Highly versatile
  - 5 V and 3.3 V PWM logic with tri-state and hold-off timer
  - 5 V DSBL#, ZCD\_EN# logic with PS4 state support
  - High frequency operation up to 2 MHz
- Robust and reliable
  - Delivers in excess of 50 A continuous current, 70 A, peak (10 ms) and 100 A, peak (10 µs)
- Over current protection
- Over temperature flag
- Over temperature protection
- Undervoltage lockout protection
- High side MOSFET short detection
- · Effective monitoring and reporting
- Accurate temperature reporting
- Warnings and faults reporting flag
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### APPLICATIONS

- Multi-phase VRDs for computing, graphics card and memory
- Intel core processor power delivery
- V<sub>CORE</sub>, V<sub>GRAPHICS</sub>, V<sub>SYSTEM AGENT</sub>
- Up to 24 V rail input DC/DC VR modules

### TYPICAL APPLICATION DIAGRAM

S19-0260-Rev. A, 18-Mar-2019

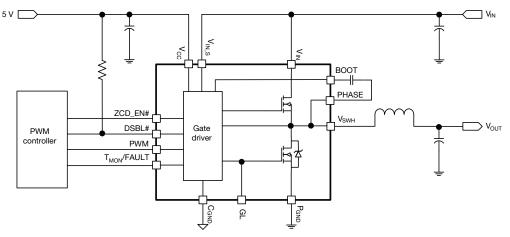


Fig. 1 - Typical Application Diagram

### SiC654 Datasheet in Brief

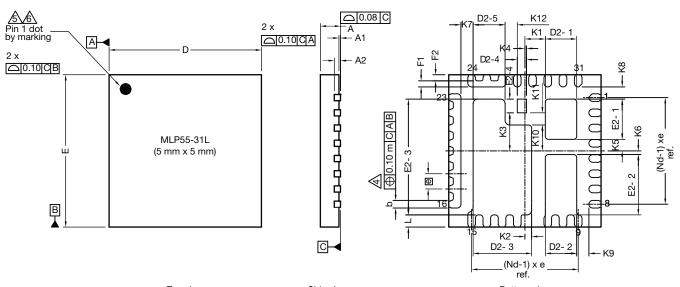


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"For more details, please contact VRPower@vishay.com"

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# PowerPAK® MLP55-31L Case Outline



Top view	Side view	Bottom view
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DIM	MILLIMETERS			INCHES				
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
A (8)	0.70	0.75	0.80	0.027	0.029	0.031		
A1	0.00	-	0.05	0.000	-	0.002		
A2		0.20 ref.			0.008 ref.			
b <sup>(4)</sup>	0.20	0.25	0.30	0.008	0.010	0.012		
D	4.90	5.00	5.10	0.193	0.196	0.200		
е		0.50 BSC		0.019 BSC				
E	4.90	5.00	5.10	0.193	0.196	0.200		
	0.35	0.40	0.45	0.013	0.015	0.017		
N <sup>(3)</sup>	32			32				
Nd <sup>(3)</sup>	8			8				
Ne <sup>(3)</sup>	8			8				
D2-1	0.98	1.03	1.08	0.039	0.041	0.043		
D2-2	0.98	1.03	1.08	0.039	0.041	0.043		
D2-3	1.87	1.92	1.97	0.074	0.076	0.078		
D2-4		0.30 BSC			0.012 BSC			
D2-5	1.00	1.05	1.10	0.039	0.041	0.043		
E2-1	1.27	1.32	1.37	0.050	0.052	0.054		
E2-2	1.93	1.98	2.03	0.076	0.078	0.080		
E2-3	3.75	3.80	3.82	0.148	0.150	0.152		
E2-4	0.45 BSC			0.018 BSC				
F1	0.20 BSC			0.008 BSC				
F2	0.20 BSC		0.008 BSC					
K1	0.67 BSC		0.026 BSC					
K2	0.22 BSC		0.008 BSC					
K3	1.25 BSC			0.049 BSC				
K4	0.05 BSC			0.002 BSC				
K5		0.38 BSC			0.015 BSC			
K6		0.12 BSC		0.005 BSC				

Revision: 24-Oct-16 Document Number: 64909



# **Package Information**

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### Vishay Siliconix

DIM	MILLIMETERS			INCHES			
DIM.	MIN.	MIN. NOM. MAX.	MIN.	NOM.	MAX.		
K7	0.40 BSC			0.016 BSC			
K8	0.40 BSC			0.016 BSC			
K9	0.40 BSC			0.016 BSC			
K10		0.85 BSC			0.033 BSC		
K11		0.40 BSC			0.016 BSC		
K12	0.40 BSC			0.016 BSC			

ECN: T16-0644-Rev. E, 24-Oct-16

DWG: 6025

#### **Notes**

- 1. Use millimeters as the primary measurement
- 2. Dimensioning and tolerances conform to ASME Y14.5M. 1994
- 3. N is the number of terminals,

Nd is the number of terminals in X-direction, and

Ne is the number of terminals in Y-direction

📐 The pin #1 identifier must be existed on the top surface of the package by using indentation mark or other feature of package body

Exact shape and size of this feature is optional

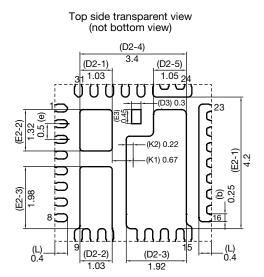
7. Package warpage max. 0.08 mm

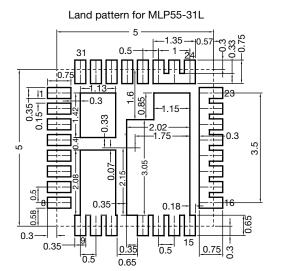
Applied only for terminals



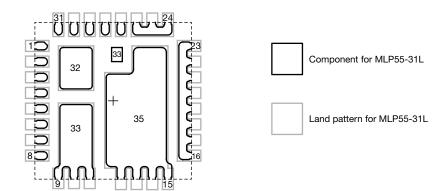


# Recommended Land Pattern PowerPAK® MLP55-31L





All dimensions in millimeters



Revision: 18-Oct-2019 1 Document Number: 66944



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