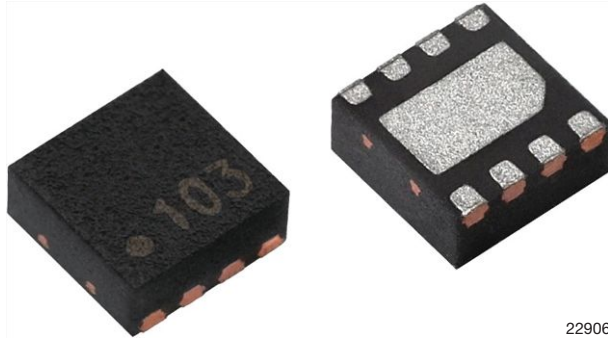


# Preamplifier Circuit for IR Remote Control

(valid July 2016 onwards)



22906

## DESIGN SUPPORT TOOLS

[click logo to get started](#)



## MECHANICAL DATA

### Pinning:

1, 4, 5 = N.C., 2 =  $V_S$ , 3 = OUT, 6, 8 = GND, 7 = IN

## FEATURES

- Narrow bandpassfilter for all common carrier frequencies
- High immunity against DC light
- Intelligent AGC to suppress disturbance from fluorescent lamps and CRTs
- Low power consumption
- Wide supply voltage range
- High immunity against ripple on the supply voltage
- Output active low
- IC manufactured in CMOS technology
- Small QFN package with 2 mm width
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

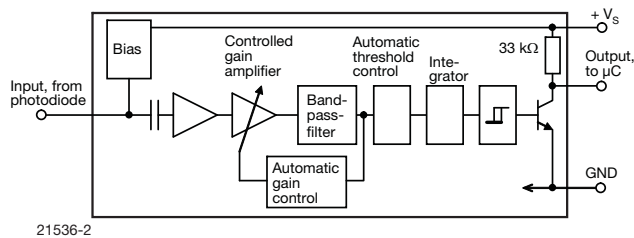
**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
[5-2008]

## DESCRIPTION

The VSOP383.. is designed for use in an IR receiver application together with a photo pin diode. It is a sophisticated receiver concept that is very sensitive to data signals and compatible with the most common data formats for IR remote control. On the other hand it is immune to DC current caused by DC light sources such as tungsten bulbs. The disturbance signal of fluorescent lamps is suppressed; there are no unwanted pulses at the output.

PARTS TABLE	
<b>AGC</b>	<b>NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)</b>
<b>Carrier frequency</b>	VSOP38336 <sup>(1)</sup>
	VSOP38338 <sup>(2)(3)(4)(5)</sup>
<b>Package</b>	VSOP
<b>Pinning</b>	1, 4, 5 = N.C., 2 = $V_S$ , 3 = OUT, 6, 8 = GND, 7 = IN
<b>Dimensions (mm)</b>	2.0 W x 2.0 H x 0.76 D
<b>Mounting</b>	SMD
<b>Application</b>	Remote control
<b>Best remote control code</b>	<sup>(1)</sup> MCIR <sup>(2)</sup> Mitsubishi <sup>(3)</sup> RECS-80 Code <sup>(4)</sup> r-map <sup>(5)</sup> XMP-1, XMP-2

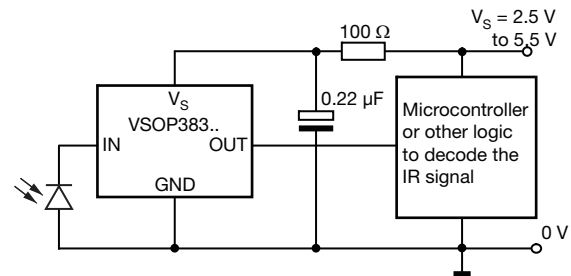
## BLOCK DIAGRAM (simplified)



21536-2

Vishay recommends using a photodiode with at least 2.3 mm<sup>2</sup> area. The connection between the photodiode and pin 7 should be kept as short as possible and carefully shielded to prevent noise coupling.

## APPLICATION CIRCUIT



The RC filter is optional to improve the EOS robustness and the immunity to supply voltage ripple. We recommend to keep the distance between the photodiode and the input of the VSOP383.. as short as possible.

21537-4

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage	Pin 2	$V_S$	-0.3 to +6	V
Supply current	Pin 2	$I_S$	3	mA
Output voltage	Pin 3	$V_O$	-0.3 to ( $V_S + 0.3$ )	V
Output sink current	Pin 3	$I_O$	5	mA
Power dissipation	$T_{amb} \leq 85\text{ }^{\circ}\text{C}$	$P_{tot}$	10	mW
Operating temperature range		$T_{amb}$	-25 to +85	$^{\circ}\text{C}$
Storage temperature range		$T_{stg}$	-25 to +85	$^{\circ}\text{C}$
ESD stress, HBM	Pin 2, pin 3, MIL-STD-883C	$V_{ESD}$	2000	V
	Pin 7, MIL-STD-883C	$V_{ESD}$	500	V
ESD stress, MM	Pin 2, pin 3, MIL-STD-883C	$V_{ESD}$	200	V
	Pin 7, MIL-STD-883C	$V_{ESD}$	100	V

**Note**

- Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = -30\text{ }^{\circ}\text{C}$ to $+85\text{ }^{\circ}\text{C}$ )						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		$V_S$	2.5	-	5.5	V
Supply current (pin 2)	$I_{IN} = 0, V_S = 5\text{ V}$	$I_S$	0.27	0.35	0.45	mA
Output voltage low (pin 3)	$I_{OL} = 2\text{ mA}$	$V_{OL}$	-	-	100	mV
Output voltage high (pin 3)	$I_{OL} = 0$	$V_{OH}$	$V_S - 0.25$	-	-	V
Internal pull up resistor (pin 2, pin 3)		$R_{PU}$	-	33	-	k $\Omega$
Max. input DC current	$V_{IN} > 0$	$I_{IN-DCmax.}$	400	-	-	$\mu\text{A}$
Min. signal detection current	$I_{IN-DC} = 0, f_C = f_{BPF}$	$I_{IN-min.}$	-	400	800	pA
	$I_{IN-DC} = 100\text{ }\mu\text{A}, f_C = f_{BPF}$	$I_{IN-min.}$	-	5	10	nA
Output pulse width	$I_{IN-DC} = 0, f_C = f_{BPF},$ $I_{IN} = 0.8\text{ nA to } 50\text{ }\mu\text{A},$ testsignal see fig. 1, BER $\leq 2\%$	$t_{po}$	$t_{pi} - 6/f_0$	$t_{pi}$	$t_{pi} + 6/f_0$	$\mu\text{s}$
Accuracy of bandpass center frequency	$T_{amb} = +25\text{ }^{\circ}\text{C}$	$f_{BPF}$	$f_0 - 4\%$	$f_0$	$f_0 + 4\%$	kHz
Bandwidth of bandpassfilter	-3 dB, $f_0 = 38\text{ kHz}$	B	-	3.8	-	kHz

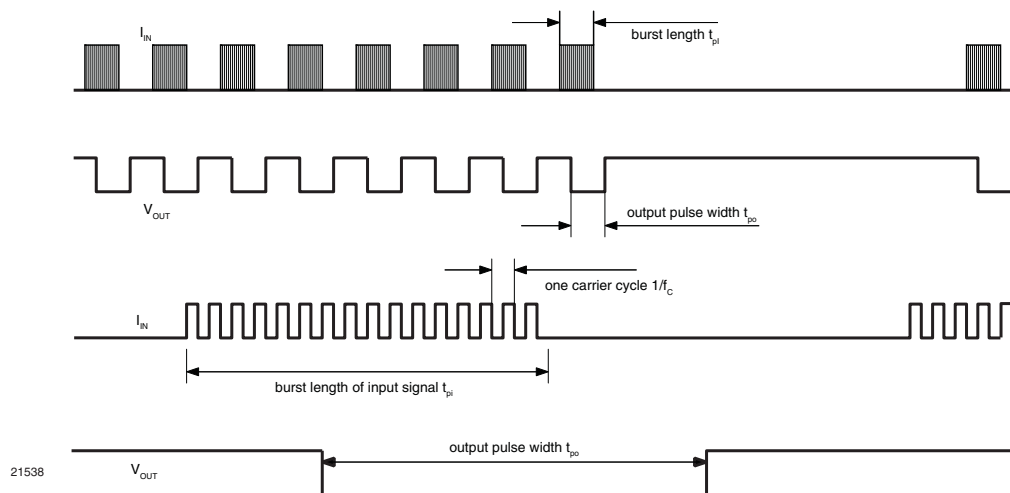


Fig. 1 - Testsignal

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ ,  $V_S = 3.3\text{ V}$ , unless otherwise specified)

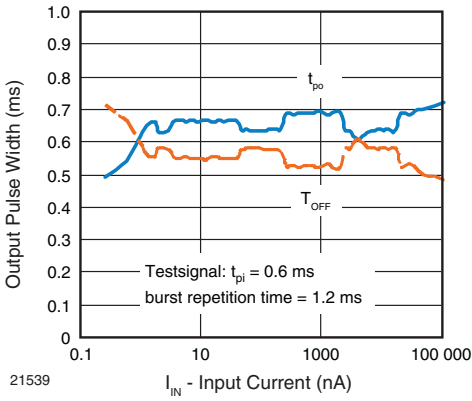


Fig. 2 - Output Pulse Diagram

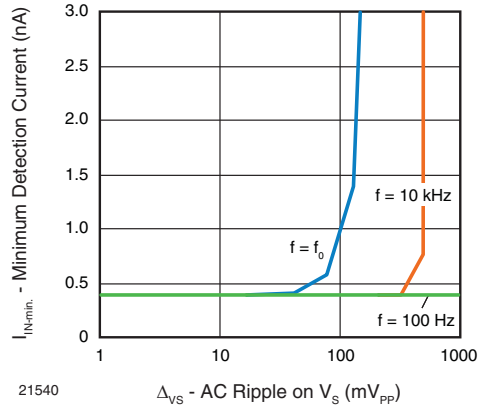


Fig. 5 - Suppression of Ripple on Supply Voltage

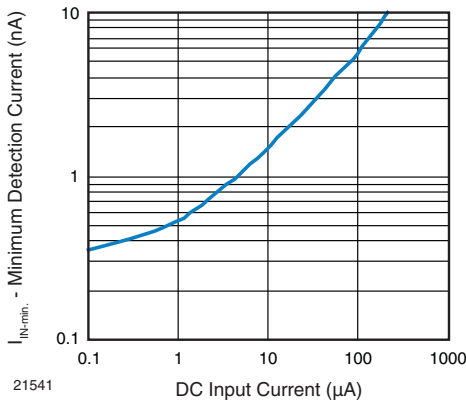


Fig. 3 - Sensitivity vs. DC Input Current

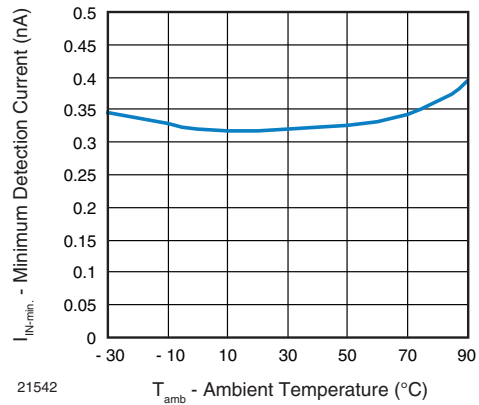


Fig. 6 - Sensitivity vs. Temperature

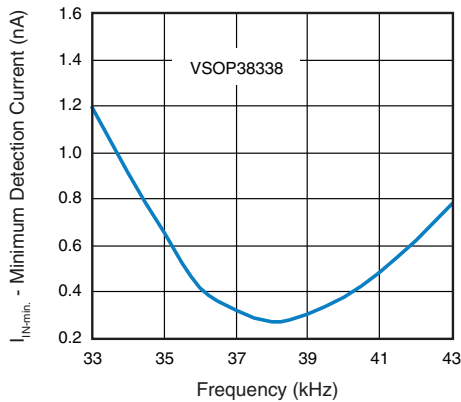


Fig. 4 - Bandpassfilter Characteristic

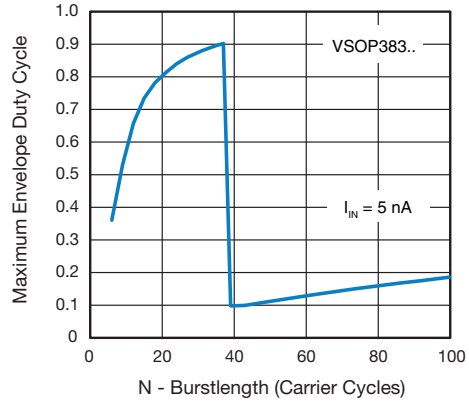
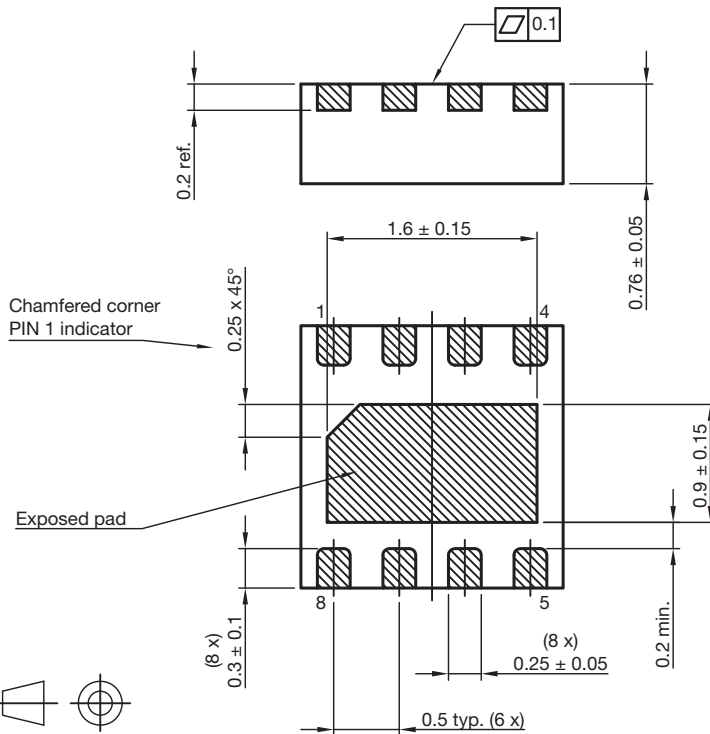
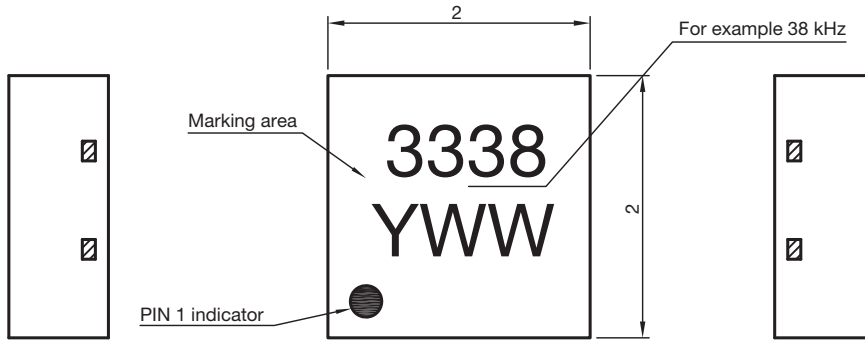


Fig. 7 - Maximum Envelope Duty Cycle

**PACKAGE DIMENSIONS** in millimeters

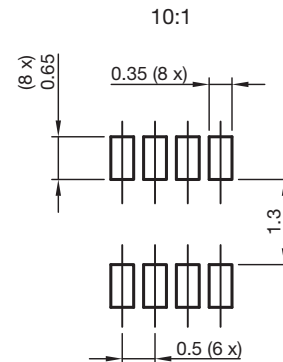


1. Coplanarity (0.1 mm) applies to the exposed pad as well as the exposed terminals.
2. Package dimension does not include mold flash, protrusions, burrs or metal smearing.

Pinning:

1. n.c.
2.  $V_S$
3. Out
4. n.c.
5. n.c.
6. GND
7. IN
8. GND

Proposed hole layout from component side (for reference only)



technical drawings according to DIN specifications

Not indicated tolerances  $\pm 0.1$

Drawing-No.: 6.550-5314.01-4  
Issue: 3; 07.12.15



### ASSEMBLY INSTRUCTIONS

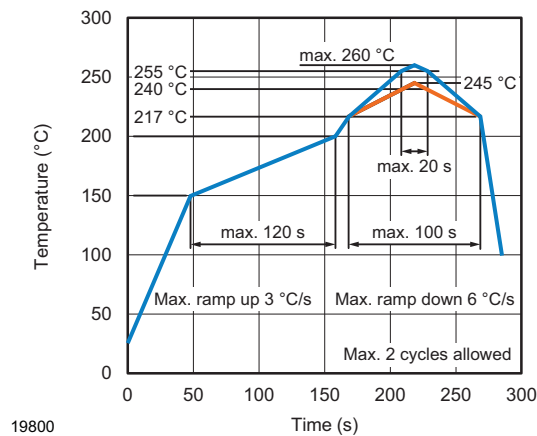
#### Reflow Soldering

- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

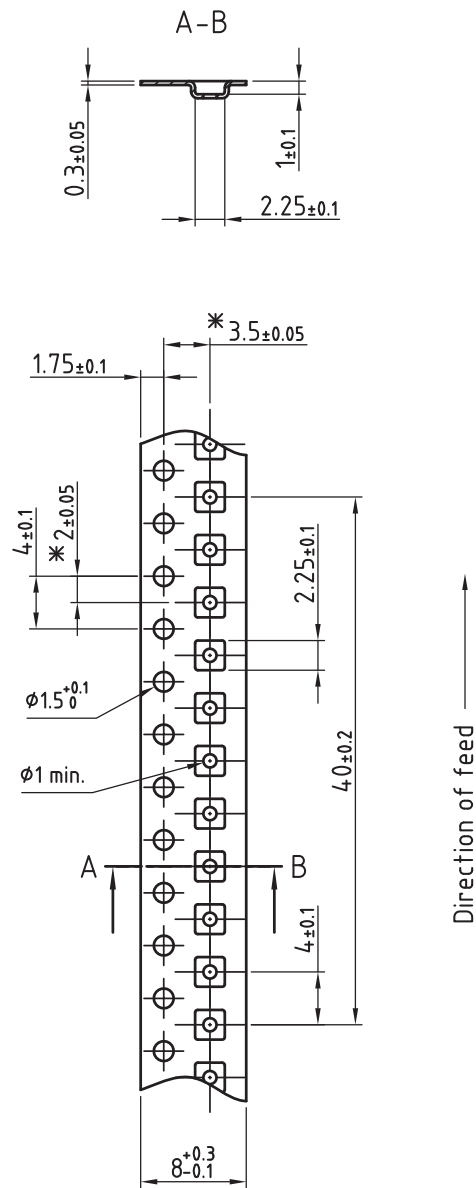
#### Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off.

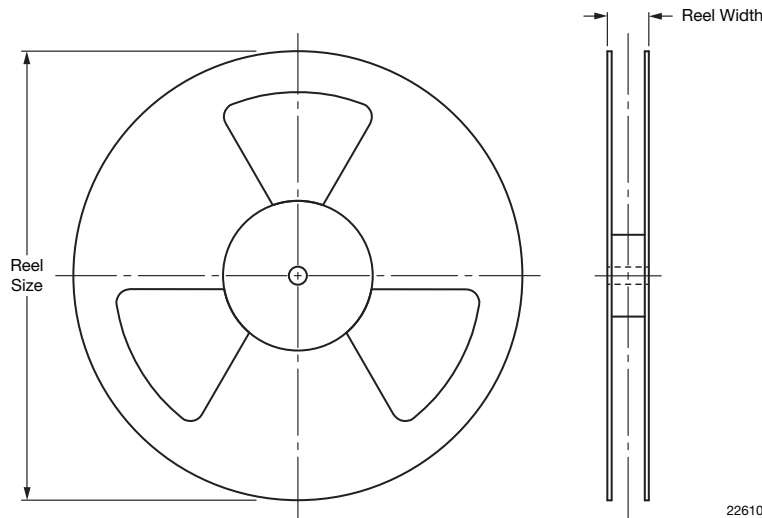
### VISHAY LEAD (PB)-FREE REFLOW SOLDER PROFILE



**TAPING VERSION VSOP DIMENSIONS** in millimeters



\* Measured from centerline of sprocket hole to centerline of pocket

**REEL DIMENSIONS** in millimeters


REEL		TRAILER LENGTH (mm)	LEADER LENGTH (mm)	QUANTITY PER REEL
REEL SIZE (inch)	REEL WIDTH (mm)			
7	8.4	160	400	3000

**LABEL**
**Standard bar code labels for finished goods**

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled

with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

<b>VISHAY SEMICONDUCTOR GMBH STANDARD BAR CODE PRODUCT LABEL (finished goods)</b>		
PLAIN WRITTING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
LONG BAR CODE TOP	TYPE	LENGTH
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
SHORT BAR CODE BOTTOM	TYPE	LENGTH
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17



**ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

**VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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