VS-UFB280FA40

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 280 A



www.vishay.com

PRIMARY CHARACTERISTICS					
V _R	400 V				
$I_{F(AV)}$ per module at $T_C = 90 \ ^{\circ}C$	280 A				
t _{rr}	40 ns				
Туре	Modules - diode FRED Pt®				
Package	SOT-227				

FEATURES

- Two fully independent diodes
- · Fully insulated package
- Ultrafast, soft reverse recovery, with high operation junction temperature (T_1 max. = 175 °C)
- Low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB280FA40 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		400	V		
Continuous forward current per diode	I _F ⁽¹⁾	$T_{\rm C} = 90 \ ^{\circ}{\rm C}$	170	А		
Single pulse forward current per diode	I _{FSM}	$T_{C} = 25 \ ^{\circ}C$	1300	A		
Maximum power dissipation per module	PD	T _C = 90 °C	395	W		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

Note

⁽¹⁾ Maximum continuous forward current must be limited to 100 A to do not exceed the maximum temperature of power terminals.





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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	400	-	-	
Forward voltage	V _{FM}	I _F = 100 A	-	1.06	1.24	V
		I _F = 100 A, T _J = 175 °C	-	0.85	0.95	
Reverse leakage current	I _{RM}	$V_{R} = V_{R}$ rated	-	1.3	50	μA
		$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.36	4	mA
Junction capacitance	CT	V _R = 400 V	-	100	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		40	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	93	-	ns
		T _J = 125 °C	I _F = 150 A dI _F /dt = 200 A/μs V _R = 200 V	-	172	-	
Peak recovery current	1	T _J = 25 °C		-	10.5	-	A
	IRRM	T _J = 125 °C		-	20.2	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	490	-	nC
		T _J = 125 °C		-	1740	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	D		-	-	0.43	
Junction to case, both leg conducting	R _{thJC}		-	-	0.215	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style					SOT-227	•

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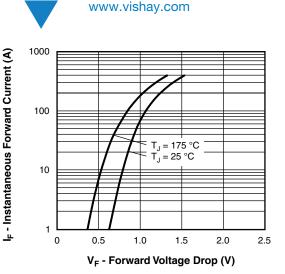
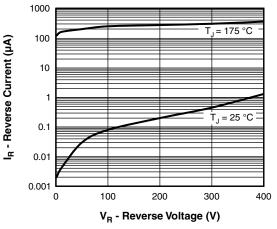
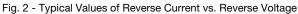


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)





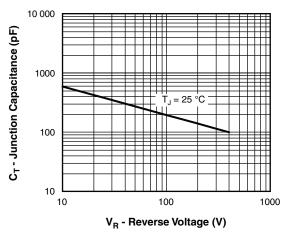


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

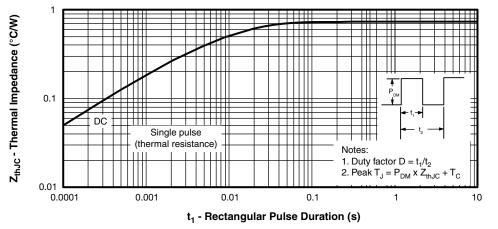
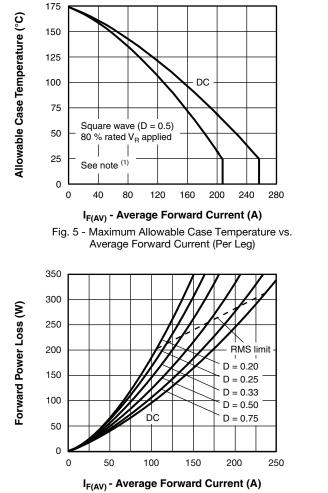


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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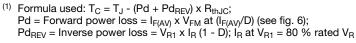


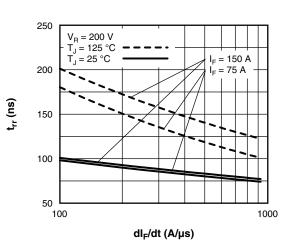
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Fig. 6 - Forward Power Loss Characteristics (Per Leg)

Note

SHAY







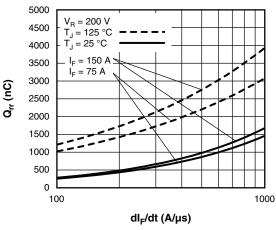


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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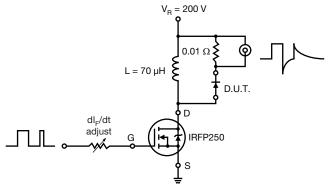
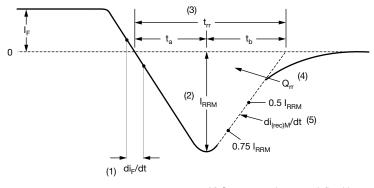


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) $di_{(rec)M}/dt$ peak rate of change of current during t_b portion of t_{rr}

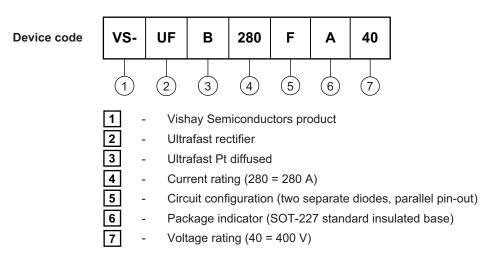
 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

Fig. 10 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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