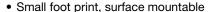


High Performance Schottky Rectifier, 1.0 A



| PRODUCT SUMMARY | | | |
|----------------------------------|------------------|--|--|
| Package | SMB | | |
| I _{F(AV)} | 1.0 A | | |
| V _R | 40 V | | |
| V _F at I _F | 0.53 V | | |
| I _{RM} max. | 4.0 mA at 125 °C | | |
| T _J max. | 125 °C | | |
| Diode variation | Single die | | |
| E _{AS} | 3.0 mJ | | |

FEATURES



Low forward voltage drop

• High frequency operation

ROHS
COMPLIANT
HALOGEN
FREE

Guard ring for enhanced ruggedness and long term reliability

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-MBRS140-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | |
|-----------------------------------|---|-------------|-------|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | |
| I _{F(AV)} | Rectangular waveform | 1.0 | Α | |
| V _{RRM} | | 40 | V | |
| I _{FSM} | t _p = 5 μs sine | 380 | Α | |
| V _F | 1.0 A _{pk} , T _J = 125 °C | 0.53 | V | |
| TJ | Range | -55 to +150 | °C | |

| VOLTAGE RATINGS | | | | |
|--------------------------------------|-----------|---------------|-------|--|
| PARAMETER | SYMBOL | VS-MBRS140-M3 | UNITS | |
| Maximum DC reverse voltage | V_{R} | 40 | V | |
| Maximum working peak reverse voltage | V_{RWM} | 40 | V | |

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|---------------------------------|--------------------------------|---|---------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum average forward current | I _{F(AV)} | 50 % duty cycle at T _L = 119 °C, rectangular waveform | | 1.0 | |
| Maximum peak one cycle | 1 | 5 μs sine or 3 μs rect. pulse | Following any rated | 380 | А |
| non-repetitive surge current | 10 ms sine or 6 ms rect. pulse | rated V _{RRM} applied | 40 | | |
| Non-repetitive avalanche energy | E _{AS} | $T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$ 3.0 r | | mJ | |
| Repetitive avalanche current | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical | | Α | |



| ELECTRICAL SPECIFICATIONS | | | | | | |
|---------------------------------|--------------------------------|--|---------------------------------------|------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | TYP. | MAX. | UNITS |
| | V _{FM} ⁽¹⁾ | 1 A | - T _J = 25 °C | 0.52 | 0.6 | · V |
| Maximum forward valtage drop | | 2 A | | 0.70 | 0.77 | |
| Maximum forward voltage drop | | 1 A | T _J = 125 °C | 0.48 | 0.53 | |
| | | 2 A | | 0.63 | 0.71 | |
| Maximum reverse leakage current | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | - | 0.1 | mA |
| | | T _J = 125 °C | | - | 4.0 | IIIA |
| Maximum junction capacitance | C _T | $V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C | | - | 80 | pF |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body | | - | 2.0 | nΗ |
| Maximum voltage rate of change | dV/dt | Rated V _R | | - | 10 000 | V/µs |

Note

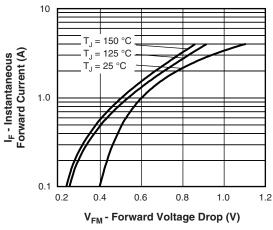
 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

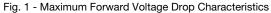
| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|---|--|--------------------------------------|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | -55 to +150 | °C |
| Maximum thermal resistance, junction to lead | R _{thJL} ⁽²⁾ | DC operation See fig. 4 | 36 | °C/W |
| Maximum thermal resistance, junction to ambient | R _{thJA} | DC operation | 80 | C/VV |
| Approximate weight | | | 0.10 | g |
| Approximate weight | | | 0.003 | OZ. |
| Marking device | | Case style SMB (similar to DO-214AA) | 1 | 4 |

Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB





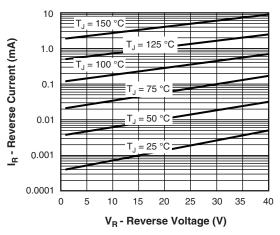


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

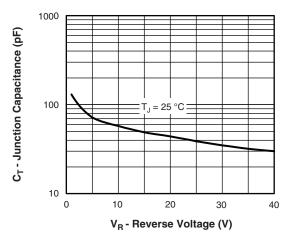


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

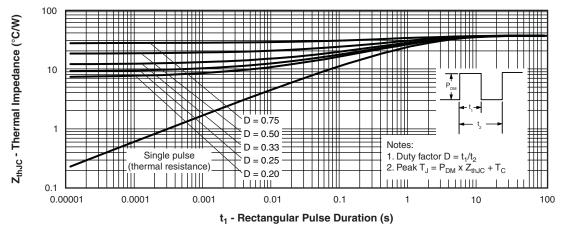


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Vishay Semiconductors

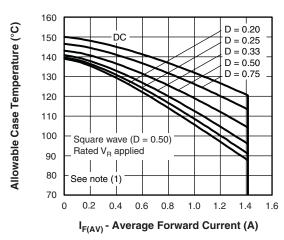


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

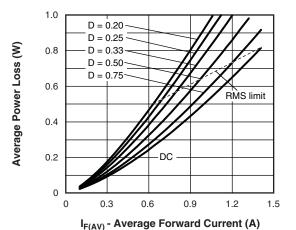


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

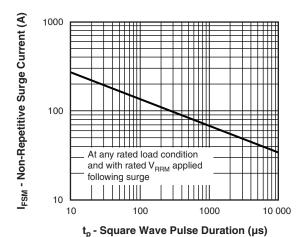


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

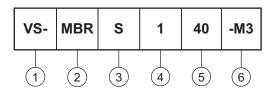
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors products
- 2 Schottky MBR series
- 3 S = SMB
- 4 Current rating (1 = 1 A)
- 5 Voltage rating (40 = 40 V)
- 6 -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

| ORDERING INFORMATION (Example) | | | | | |
|--------------------------------|---|------|------------------------------------|--|--|
| PREFERRED P/N | D P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION | | | | |
| VS-MBRS140-M3/5BT | 5BT | 3200 | 13" diameter plastic tape and reel | | |

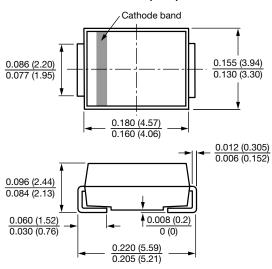
| LINKS TO RELATED DOCUMENTS | | | | |
|-------------------------------------|--------------------------|--|--|--|
| Dimensions www.vishay.com/doc?95401 | | | | |
| Part marking information | www.vishay.com/doc?95403 | | | |
| Packaging information | www.vishay.com/doc?95404 | | | |
| SPICE model | www.vishay.com/doc?95299 | | | |



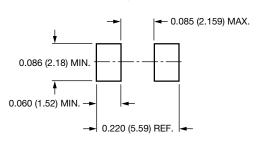
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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Vishay

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