VS-GA200SA60UP

Vishay Semiconductors

Insulated Gate Bipolar Transistor (Ultrafast Speed IGBT), 100 A



www.vishay.com

PRIMARY CHARACTERISTICS					
V _{CES} 600 V					
V _{CE(on)} (typical)	1.92 V				
V _{GE} 15 V					
I _C 100 A					
Speed	8 kHz to 30 kHz				
Package SOT-227					
Circuit configuration	Single switch no diode				

FEATURES

• Ultrafast: optimized for minimum saturation voltage and speed up to 30 kHz in hard switching, > 200 kHz in resonant mode



- ROHS COMPLIANT
- Very low conduction and switching losses
- Fully isolate package (2500 V_{AC/RMS})
- Very low internal inductance (\leq 5 nH typical)
- Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Lower overall losses available at frequencies = 20 kHz
- Easy to assemble and parallel
- Direct mounting to heatsink
- Lower EMI, requires less snubbing
- · Plug-in compatible with other SOT-227 packages

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	MBOL TEST CONDITIONS		UNITS	
Collector to emitter breakdown voltage	V _{CES}		600	V	
Continuous collector current	1	T _C = 25 °C	200		
	I _C	T _C = 100 °C	100		
Pulsed collector current	I _{CM}		400	А	
Clamped inductive load current	I _{LM}	V_{CC} = 80 % (V _{CES}), V _{GE} = 20 V, L = 10 μH, R _g = 2.0 Ω, see fig. 13a	400		
Gate to emitter voltage	V _{GE}		± 20	V	
Reverse voltage avalanche energy	E _{ARV}	Repetitive rating; pulse width limited by maximum junction temperature	160	mJ	
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V	
Maximum power dissipation	PD	T _C = 25 °C	500	14/	
		T _C = 100 °C	200	W	
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C	
Mounting torque		6-32 or M3 screw	1.3 (12)	Nm (lbf.in	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-55	-	150	
Thermal resistance, junction to case	R _{thJC}		-	-	0.25	°C/W
Thermal resistance case to heatsink	R _{thCS}	Flat, greased, surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			

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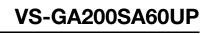
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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	V _{GE} = 0 V, I _C = 250 μA		600	-	-	
Emitter to collector breakdown voltage	V _{(BR)ECS}	V_{GE} = 0 V, I_C = 1.0 A Pulse width \leq 80 $\mu s;$ duty factor \leq 0.1 %		18	-	-	V
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES} / \Delta T_J$	$V_{GE} = 0 V, I_{C} = 10 mA$		-	0.38	-	V/°C
Collector to emitter saturation voltage	V _{CE(on)}	I _C = 100 A	V _{GE} = 15 V See fig. 2, 5	-	1.60	1.9	v
		I _C = 200 A		-	1.92	-	
		I _C = 100 A, T _J = 150 °C		-	1.54	-	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 250 \ \mu A$		3.0	-	6.0	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	$V_{CE} = V_{GE}$, $I_C = 2.0$ mA		-	-11	-	mV/°C
Forward transconductance	g fe	V_{CE} = 100 V, I _C = 100 A Pulse width 5.0 µs, single shot		79	-	-	S
Zero gate voltage collector current	I _{CES}	$V_{GE} = 0 V, V_{CE} = 600 V$		-	-	1.0	mA
		$V_{GE} = 0 V, V_{CE} = 600 V, T$	J = 150 °C	-	-	10	
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 20 \text{ V}$		-	-	± 250	nA

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 100 A	-	770	1200	
Gate-emitter charge (turn-on)	Q _{ge}	V _{CC} = 400 V	-	100	150	nC
Gate-collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V; See fig. 8	-	260	380	
Turn-on delay time	t _{d(on)}	T _{.1} = 25 °C	-	54	-	ns
Rise time	t _r	$I_{\rm C} = 100 {\rm A}$	-	79	-	
Turn-off delay time	t _{d(off)}	$V_{\rm CC} = 480 \text{ V}$	-	130	200	
Fall time	t _f	V _{GE} = 15 V	-	300	450	
Turn-on switching loss	E _{on}	$\begin{aligned} R_g &= 2.0 \ \Omega \\ \text{Energy losses include "tail"} \\ \text{See fig. 9, 10, 14} \\ \hline T_J &= 150 \ ^\circ\text{C} \\ I_C &= 100 \ \text{A}, \ V_{CC} &= 480 \ \text{V} \\ \hline V_{GE} &= 15 \ \text{V}, \ R_g &= 2.0 \ \Omega \\ \text{Energy losses include "tail"} \end{aligned}$	-	0.98	-	
Turn-off switching loss	E _{off}		-	3.48	-	mJ
Total switching loss	E _{ts}		-	4.46	7.6	
Turn-on delay time	t _{d(on)}		-	56	-	
Rise time	tr		-	75	-	1
Turn-off delay time	t _{d(off)}		-	160	-	ns
Fall time	t _f		-	460	-	
Total switching loss	E _{ts}	See fig. 10, 11, 14	-	7.24	-	mJ
Internal emitter inductance	LE	Measured 5 mm from package	-	5.0	-	nH
Input capacitance	C _{ies}	V _{GE} = 0 V	-	16 500	-	
Output capacitance	C _{oes}	$V_{\rm CC} = 30 \text{ V}$	-	1000	-	pF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz; See fig. 7	-	200	-	1





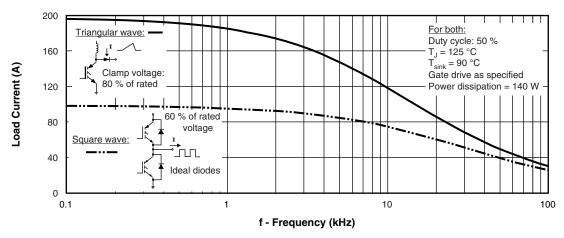


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of Fundamental)

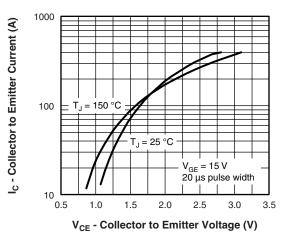


Fig. 2 - Typical Output Characteristics

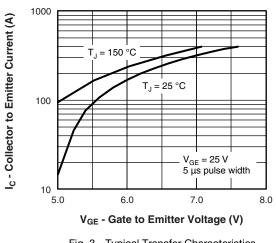


Fig. 3 - Typical Transfer Characteristics

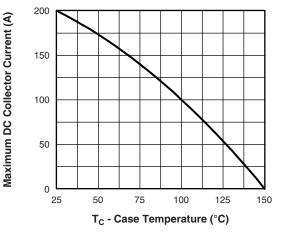
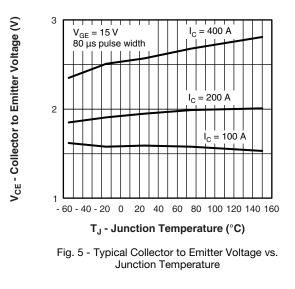


Fig. 4 - Maximum Collector Current vs. Case Temperature



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VS-GA200SA60UP www.vishay.com **Vishay Semiconductors** 1 Z_{thJC} - Thermal Response Π D = 0.50 0.1 ł D = 0.20D = 0.10D = 0.05 0 02 D 0.01 D = 0.01 Notes: Single pulse 1. Duty factor $D = t_1/t_2$ (thermal resistance) 2. Peak T_J = P_{DM} x Z_{thJC} 0.001 0.00001 0.0001 0.001 0.01 0.1 1 t₁ - Rectangular Pulse Duration (s)

Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction to Case

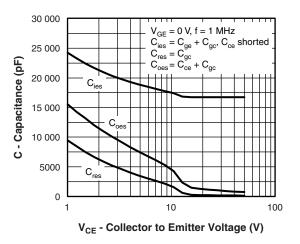


Fig. 7 - Typical Capacitance vs. Collector to Emitter Voltage

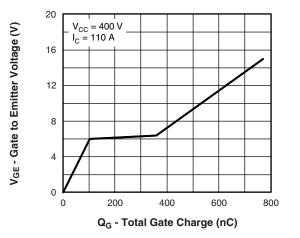


Fig. 8 - Typical Gate Charge vs. Gate to Emitter Voltage

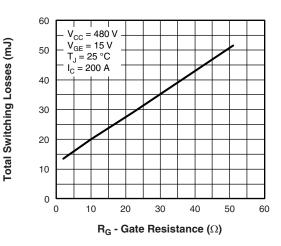


Fig. 9 - Typical Switching Losses vs. Gate Resistance

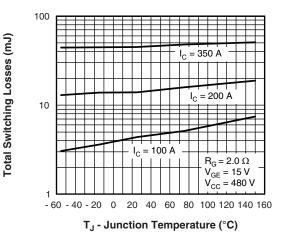


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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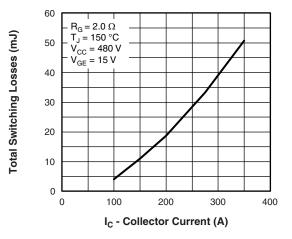


Fig. 11 - Typical Switching Losses vs. Collector Current

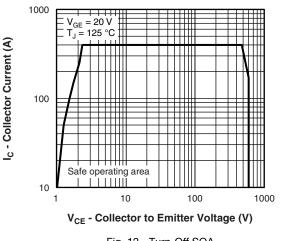
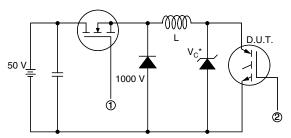


Fig. 12 - Turn-Off SOA



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* Driver same type as D.U.T.; V_{C} = 80 % of V_{CE} (max)

Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain rated I_{rt}

Fig. 13a - Clamped Inductive Load Test Circuit

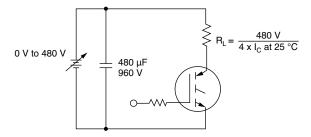
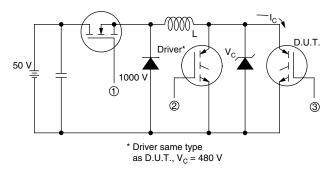


Fig. 13b - Pulsed Collector Current Test Circuit





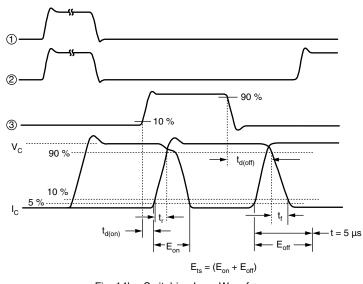
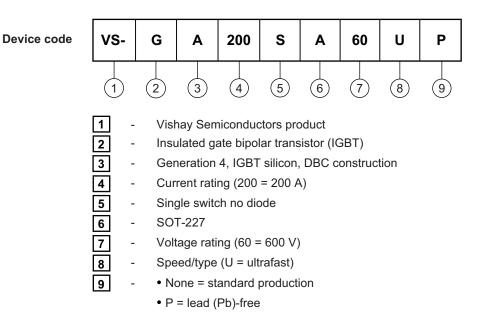


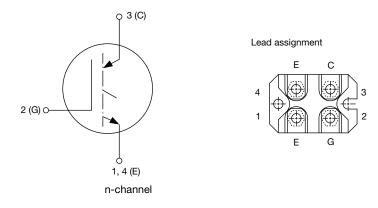
Fig. 14b - Switching Loss Waveforms



ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95425				
Packaging information www.vishay.com/doc?95423				



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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