# VS-GA200SA60UP

**Vishay Semiconductors** 

# Insulated Gate Bipolar Transistor (Ultrafast Speed IGBT), 100 A



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PRIMARY CHARACTERISTICS					
V <sub>CES</sub> 600 V					
V <sub>CE(on)</sub> (typical)	1.92 V				
V <sub>GE</sub> 15 V					
I <sub>C</sub> 100 A					
Speed	8 kHz to 30 kHz				
Package SOT-227					
Circuit configuration	Single switch no diode				

### FEATURES

• Ultrafast: optimized for minimum saturation voltage and speed up to 30 kHz in hard switching, > 200 kHz in resonant mode



- ROHS COMPLIANT
- Very low conduction and switching losses
- Fully isolate package (2500 V<sub>AC/RMS</sub>)
- Very low internal inductance ( $\leq$  5 nH typical)
- Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Lower overall losses available at frequencies = 20 kHz
- Easy to assemble and parallel
- Direct mounting to heatsink
- Lower EMI, requires less snubbing
- · Plug-in compatible with other SOT-227 packages

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	MBOL TEST CONDITIONS		UNITS	
Collector to emitter breakdown voltage	V <sub>CES</sub>		600	V	
Continuous collector current	1	T <sub>C</sub> = 25 °C	200		
	I <sub>C</sub>	T <sub>C</sub> = 100 °C	100		
Pulsed collector current	I <sub>CM</sub>		400	А	
Clamped inductive load current	I <sub>LM</sub>	$V_{CC}$ = 80 % (V <sub>CES</sub> ), V <sub>GE</sub> = 20 V, L = 10 μH, R <sub>g</sub> = 2.0 Ω, see fig. 13a	400		
Gate to emitter voltage	V <sub>GE</sub>		± 20	V	
Reverse voltage avalanche energy	E <sub>ARV</sub>	Repetitive rating; pulse width limited by maximum junction temperature	160	mJ	
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V	
Maximum power dissipation	PD	T <sub>C</sub> = 25 °C	500	14/	
		T <sub>C</sub> = 100 °C	200	W	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Mounting torque		6-32 or M3 screw	1.3 (12)	Nm (lbf.in	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	150	
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.25	°C/W
Thermal resistance case to heatsink	R <sub>thCS</sub>	Flat, greased, surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			

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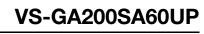
Document Number: 94364

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<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 μA		600	-	-	
Emitter to collector breakdown voltage	V <sub>(BR)ECS</sub>	$V_{GE}$ = 0 V, $I_C$ = 1.0 A Pulse width $\leq$ 80 $\mu s;$ duty factor $\leq$ 0.1 %		18	-	-	V
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES} / \Delta T_J$	$V_{GE} = 0 V, I_{C} = 10 mA$		-	0.38	-	V/°C
Collector to emitter saturation voltage	V <sub>CE(on)</sub>	I <sub>C</sub> = 100 A	V <sub>GE</sub> = 15 V See fig. 2, 5	-	1.60	1.9	v
		I <sub>C</sub> = 200 A		-	1.92	-	
		I <sub>C</sub> = 100 A, T <sub>J</sub> = 150 °C		-	1.54	-	
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 250 \ \mu A$		3.0	-	6.0	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	$V_{CE} = V_{GE}$ , $I_C = 2.0$ mA		-	-11	-	mV/°C
Forward transconductance	<b>g</b> fe	$V_{CE}$ = 100 V, I <sub>C</sub> = 100 A Pulse width 5.0 µs, single shot		79	-	-	S
Zero gate voltage collector current	I <sub>CES</sub>	$V_{GE} = 0 V, V_{CE} = 600 V$		-	-	1.0	mA
		$V_{GE} = 0 V, V_{CE} = 600 V, T$	J = 150 °C	-	-	10	
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = \pm 20 \text{ V}$		-	-	± 250	nA

SWITCHING CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I <sub>C</sub> = 100 A	-	770	1200	
Gate-emitter charge (turn-on)	Q <sub>ge</sub>	V <sub>CC</sub> = 400 V	-	100	150	nC
Gate-collector charge (turn-on)	Q <sub>gc</sub>	V <sub>GE</sub> = 15 V; See fig. 8	-	260	380	
Turn-on delay time	t <sub>d(on)</sub>	T <sub>.1</sub> = 25 °C	-	54	-	ns
Rise time	t <sub>r</sub>	$I_{\rm C} = 100  {\rm A}$	-	79	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{\rm CC} = 480 \text{ V}$	-	130	200	
Fall time	t <sub>f</sub>	V <sub>GE</sub> = 15 V	-	300	450	
Turn-on switching loss	E <sub>on</sub>	$\begin{aligned} R_g &= 2.0 \ \Omega \\ \text{Energy losses include "tail"} \\ \text{See fig. 9, 10, 14} \\ \hline T_J &= 150 \ ^\circ\text{C} \\ I_C &= 100 \ \text{A}, \ V_{CC} &= 480 \ \text{V} \\ \hline V_{GE} &= 15 \ \text{V}, \ R_g &= 2.0 \ \Omega \\ \text{Energy losses include "tail"} \end{aligned}$	-	0.98	-	
Turn-off switching loss	E <sub>off</sub>		-	3.48	-	mJ
Total switching loss	E <sub>ts</sub>		-	4.46	7.6	
Turn-on delay time	t <sub>d(on)</sub>		-	56	-	
Rise time	tr		-	75	-	1
Turn-off delay time	t <sub>d(off)</sub>		-	160	-	ns
Fall time	t <sub>f</sub>		-	460	-	
Total switching loss	E <sub>ts</sub>	See fig. 10, 11, 14	-	7.24	-	mJ
Internal emitter inductance	LE	Measured 5 mm from package	-	5.0	-	nH
Input capacitance	C <sub>ies</sub>	V <sub>GE</sub> = 0 V	-	16 500	-	
Output capacitance	C <sub>oes</sub>	$V_{\rm CC} = 30 \text{ V}$	-	1000	-	pF
Reverse transfer capacitance	C <sub>res</sub>	f = 1.0 MHz; See fig. 7	-	200	-	1





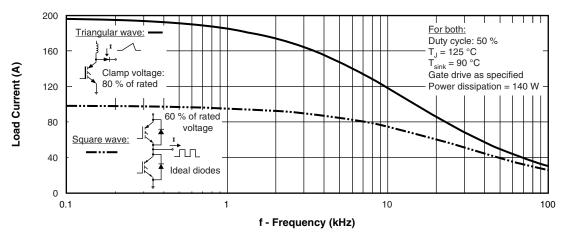


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I<sub>RMS</sub> of Fundamental)

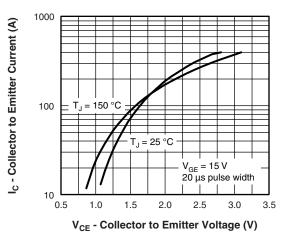


Fig. 2 - Typical Output Characteristics

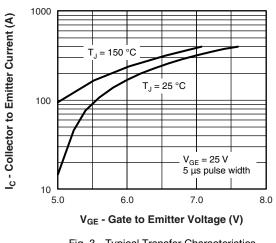


Fig. 3 - Typical Transfer Characteristics

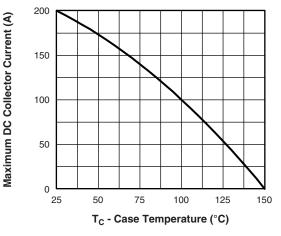
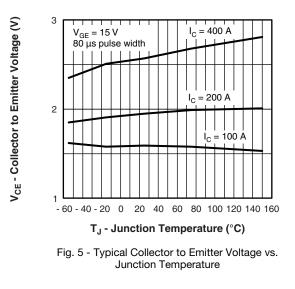


Fig. 4 - Maximum Collector Current vs. Case Temperature



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#### VS-GA200SA60UP www.vishay.com **Vishay Semiconductors** 1 Z<sub>thJC</sub> - Thermal Response Π D = 0.50 0.1 ł D = 0.20D = 0.10D = 0.05 0 02 D 0.01 D = 0.01 Notes: Single pulse 1. Duty factor $D = t_1/t_2$ (thermal resistance) 2. Peak T<sub>J</sub> = P<sub>DM</sub> x Z<sub>thJC</sub> 0.001 0.00001 0.0001 0.001 0.01 0.1 1 t<sub>1</sub> - Rectangular Pulse Duration (s)

Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction to Case

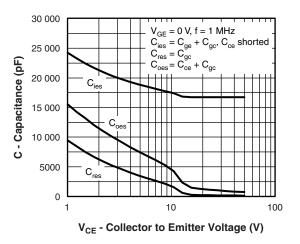


Fig. 7 - Typical Capacitance vs. Collector to Emitter Voltage

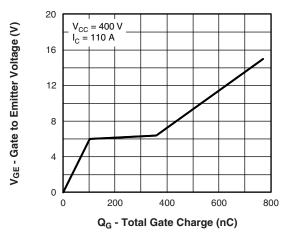


Fig. 8 - Typical Gate Charge vs. Gate to Emitter Voltage

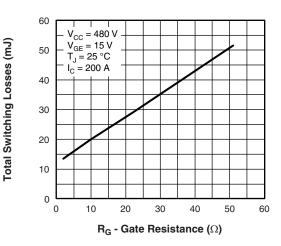


Fig. 9 - Typical Switching Losses vs. Gate Resistance

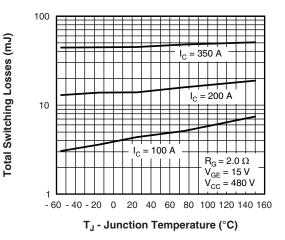


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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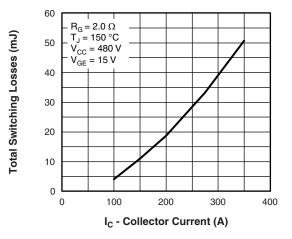


Fig. 11 - Typical Switching Losses vs. Collector Current

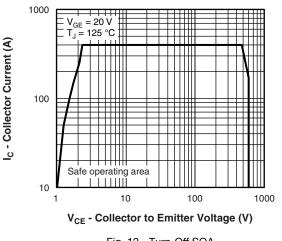
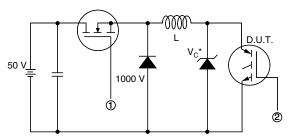


Fig. 12 - Turn-Off SOA



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\* Driver same type as D.U.T.;  $V_{C}$  = 80 % of  $V_{CE}$  (max)

Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain rated I<sub>rt</sub>

Fig. 13a - Clamped Inductive Load Test Circuit

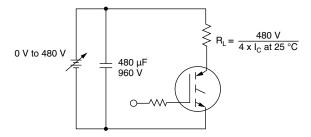
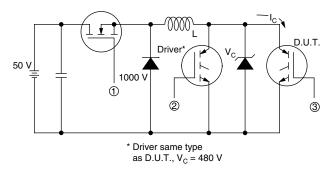
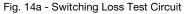


Fig. 13b - Pulsed Collector Current Test Circuit





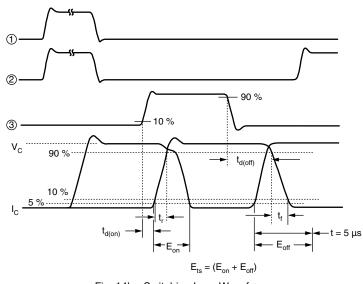
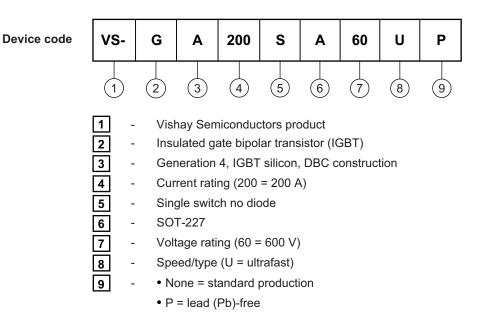


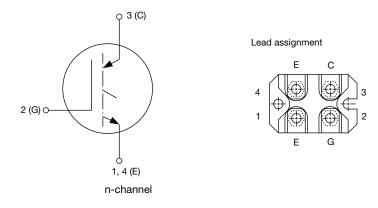
Fig. 14b - Switching Loss Waveforms



### **ORDERING INFORMATION TABLE**



### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95425				
Packaging information www.vishay.com/doc?95423				



SOT-227 Generation 2

### **DIMENSIONS** in millimeters (inches)



#### Note

• Controlling dimension: millimeter



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