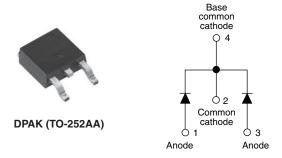


RoHS

COMPLIANT HALOGEN

FREE

High Performance Schottky Rectifier, 2 x 3.5 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} 2 x 3.5 A					
V _R	60 V				
V _F at I _F	See Electrical table				
I _{RM}	30 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	6 mJ				
Package DPAK (TO-252AA					
Circuit configuration Common cathode					

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- · Center tap configuration
- · Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-6CWQ06FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	7	Α			
V_{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	490	Α			
V _F	3 A _{pk} , T _J = 25 °C (per leg)	0.61	V			
T _J	Range	-40 to +150	°C			

VOLTAGE RATINGS					
PARAMETER SYMBOL VS-6CWQ06FNHM3 UNITS					
Maximum DC reverse voltage	V_R	60	V		
Maximum working peak reverse voltage	V_{RWM}	00	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward per leg		50 % duty cycle at T _C = 133 °C, rectangular waveform		50 0/ distributed T		3.5	
current, see fig. 5 per device	I _{F(AV)}			7			
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	490	Α		
surge current, see fig. 7		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	70			
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25$ °C, $I_{AS} = 1$ A, L = 12 mH		6	mJ				
L Donotituo avalancho current per lea		, ,	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		Α		



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		3 A	T _{.1} = 25 °C	0.61	V	
Maximum forward voltage drop per	V (1)	6 A	- IJ = 25 C	0.76		
leg, see fig. 1	V _{FM} ⁽¹⁾	3 A	T _{.1} = 125 °C	0.53		
		6 A	I _J = 125 C	0.65		
Maximum reverse leakage current	ı (1)	T _J = 25 °C		2	^	
per leg, see fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V_R = Rated V_R	30	- mA	
Threshold voltage	V _{F(TO)}	T - T movimum		0.38	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		34.31	mΩ	
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal rai	145	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5	mm from package body	5.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _B			

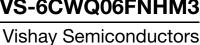
Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

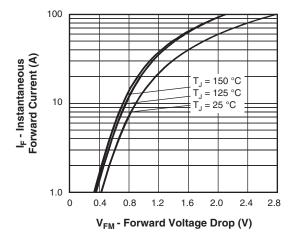
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage ten	nperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance,	per leg	В	DC operation	4.70	°C/W	
junction to case	per device	R_{thJC}	See fig. 4	2.35	C/VV	
Approximate weight				0.3	g	
Approximate weight				0.01	OZ.	
Marking device			Case style DPAK	6CWQ	06FNH	

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{th,JA}}$$
 thermal runaway condition for a diode on its own heatsink









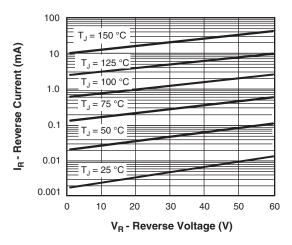


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

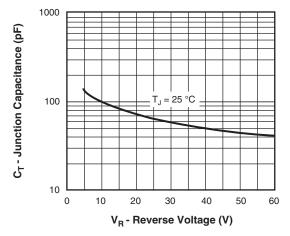


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

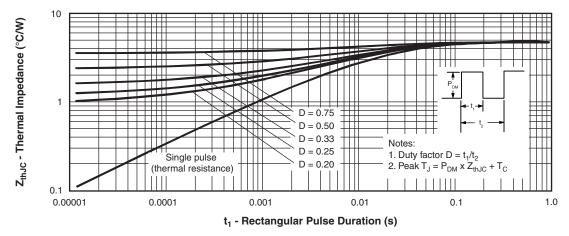


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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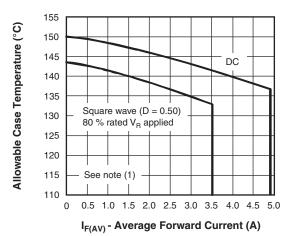


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

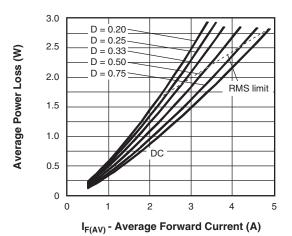


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

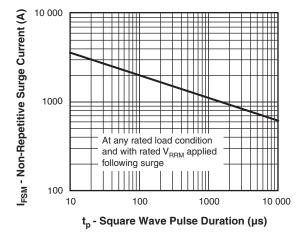


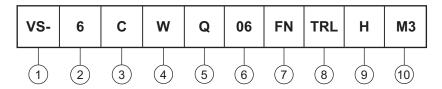
Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80$ % rated V_R

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (7 A)

Center tap configuration

4 - Package identifier:

W = DPAK

5 - Schottky "Q" series

6 - Voltage rating (06 = 60 V)

7 - FN = TO-252AA

8 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

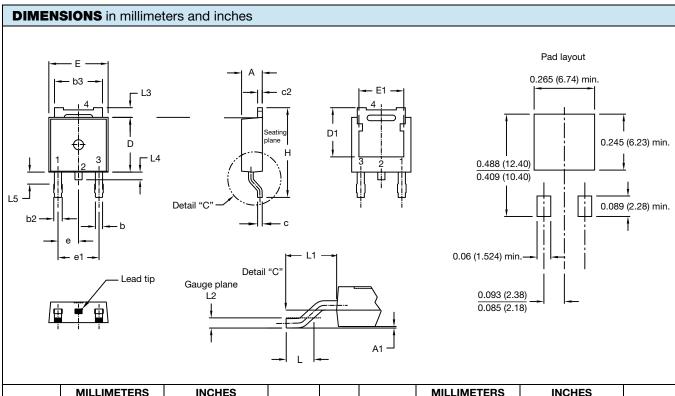
M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-6CWQ06FNHM3	75	3000	Antistatic plastic tube			
VS-6CWQ06FNTRHM3	2000	2000	13" diameter reel			
VS-6CWQ06FNTRRHM3	3000	3000	13" diameter reel			
VS-6CWQ06FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95519			
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			
SPICE model	www.vishay.com/doc?96651			



DPAK (TO-252AA)



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	ı	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIN	IETERS	INC	INCHES	
	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090) BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108	REF.	
L2	0.51	BSC	0.020) BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
	•		•		•

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA



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Vishay

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