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Schottky Rectifier, 5.5 A



D-PAK	(TO-252AA)

Bas	e
catho	ode
	4, 2
∫ ● ⊃ 1	3
node	Anode

PRODUCT SUMMARY		
Package	D-PAK (TO-252AA)	
I _{F(AV)}	5.5 A	
V _R	60 V	
V _F at I _F	See Electrical table	
I _{RM}	35 mA at 125 °C	
T _J max.	150 °C	
Diode variation	Single die	
E _{AS}	7 mJ	

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-50WQ06FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	5.5	А	
V _{RRM}		60	V	
I _{FSM}	t _p = 5 μs sine	320	А	
V _F	5 A _{pk} , T _J = 125 °C	0.54	V	
TJ	Range	- 40 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-50WQ06FNHM3	UNITS	
Maximum DC reverse voltage	V _R	60	V	
Maximum working peak reverse voltage	V _{RWM}	60	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C	, rectangular waveform	5.5	
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	А
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	105	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.2 A, L = 10 mH		7	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.8	А

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HALOGEN

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop		5 A	T,I = 25 °C	0.57	V
	V (1)	10 A	$-1_{j}=25$ C	0.74	
See fig. 1	V _{FM} ⁽¹⁾	5 A	T 105 %C	0.54	
		10 A	T _J = 125 °C	0.68	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	mA
See fig. 2	IRM (1)	T _J = 125 °C		35	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.35	V
Forward slope resistance	r _t			25.5	mΩ
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		360	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

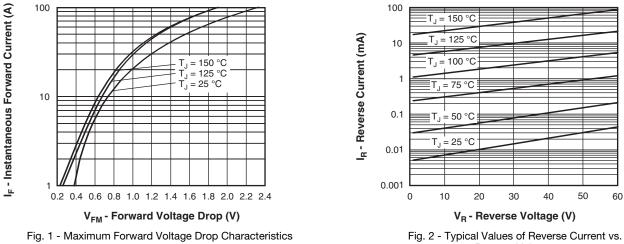
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W
Approvimate weight			0.3	g
Approximate weight			0.01	oz.
Marking device		Case style D-PAK	50WQ	06FNH

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

VS-50WQ06FNHM3

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Reverse Voltage

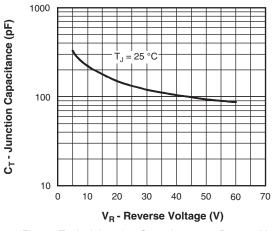


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

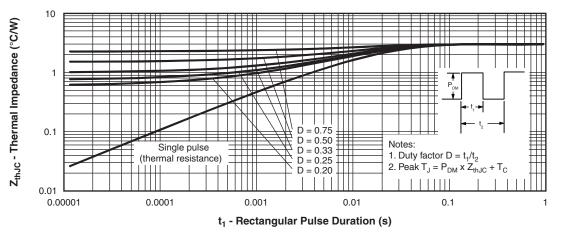
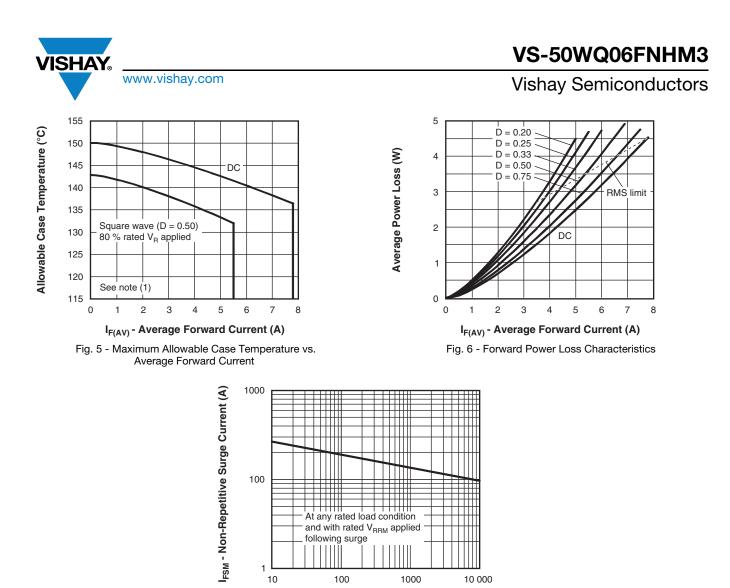


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-50WQ06FNHM3	75	3000	Antistatic plastic tube	
VS-50WQ06FNTRHM3	2000	2000	13" diameter reel	
VS-50WQ06FNTRRHM3	3000	3000	13" diameter reel	
VS-50WQ06FNTRLHM3	3000	3000	13" diameter reel	

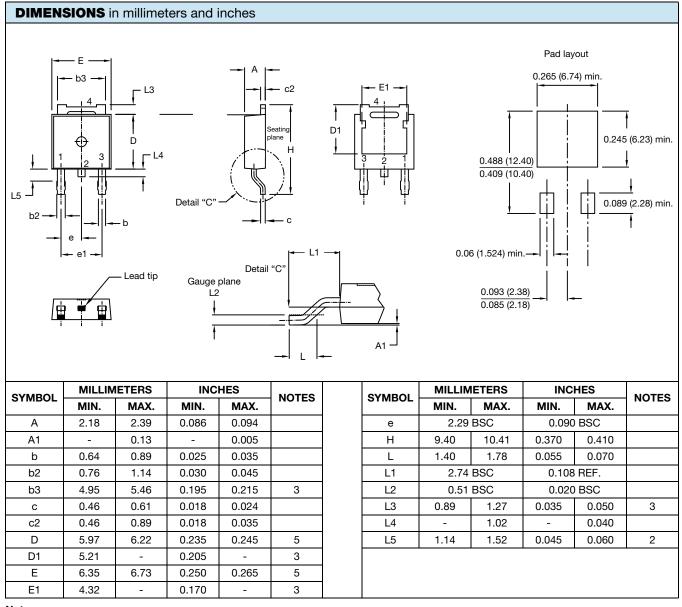
LINKS TO RELATED DOCUMENTS		
Dimensions www.vishay.com/doc?95519		
Part marking information	www.vishay.com/doc?95518	
Packaging information	www.vishay.com/doc?95033	

Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA



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