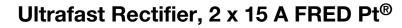
## **Vishay Semiconductors**





www.vishay.com

## **DESIGN SUPPORT TOOLS AVAILABLE**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 15 A				
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	0.9 V				
t <sub>rr</sub>	55 ns				
T <sub>J</sub> max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Common cathode				

#### **FEATURES**

- Ultrafast recovery time, reduced Qrr, and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM, snubber operation
- · Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage		V <sub>RRM</sub>		600	V		
Average rectified forward current	per device		T <sub>solder pad</sub> = 143 °C	30	A		
	per diode	IF(AV)		15			
Non repetitive peak aurea aureant	per device	I=	T <sub>J</sub> = 25 °C, 6 ms square pulse	300			
Non-repetitive peak surge current	per diode	IFSM		160			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.03	1.25	V
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.9	1.1	
Deverse lectrone everent per diade		V <sub>R</sub> = V <sub>R</sub> rated	-	-	15	
Reverse leakage current, per diode	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	70	300	μA
Junction capacitance, per diode	CT	V <sub>R</sub> = 600 V	-	13	-	pF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A}$	Vµs, V <sub>R</sub> = 30 V	-	55	-	
Reverse recovery time	+	$I_{\rm F} = 0.5 \text{ A}, I_{\rm R} = 1 \text{ A}, I_{\rm rr}$	-	-	65		
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	96	-	ns
		T <sub>J</sub> = 125 °C		-	150	-	
Deals receiver a surrent		T <sub>J</sub> = 25 °C	$I_F = 15 \text{ A},$	-	18	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 500 A/µs, V <sub>B</sub> = 400 V	-	26	-	A
Devene version allower	0	T <sub>J</sub> = 25 °C	1	-	1.0	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	2.0	-	μC

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	+175	°C	
Thermal resistance, per diode junction to solder pad	R <sub>thJ-Sp</sub>		-	1.2	1.7	°C/W	
Approximate weight				0.55		g	
				0.02		oz.	
Marking device		Case style SMPD (TO-263AC)		30CI	DU06		

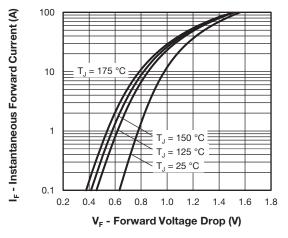


Fig. 1 - Typical Forward Voltage Drop Characteristics

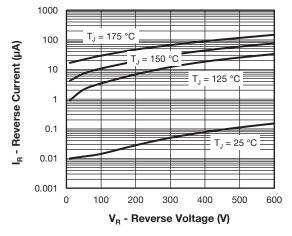


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

# VS-30CDU06-M3

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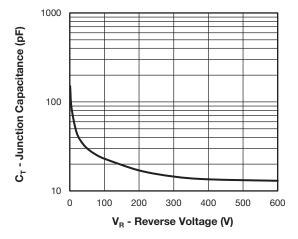


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

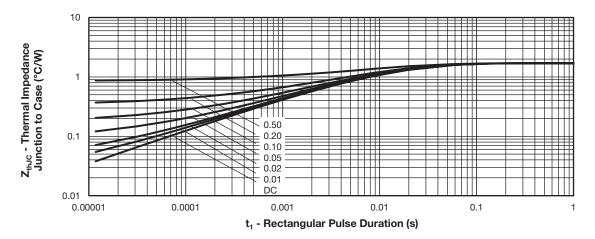
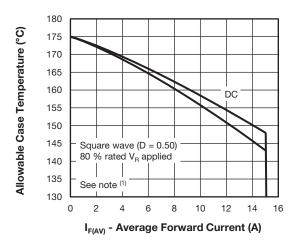
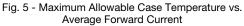


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics





#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

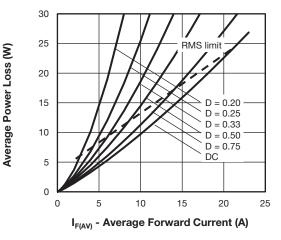


Fig. 6 - Forward Power Loss Characteristics

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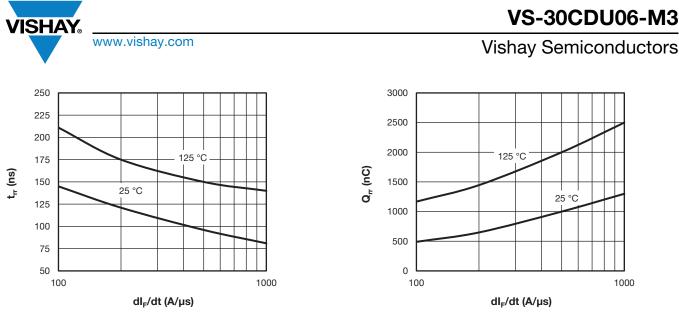


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

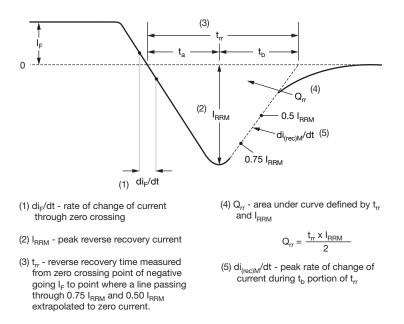


Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

Device code	VS-	30	С	D	U	06	-M3
	1	2	3	4	5	6	7
	1	- Visl	nay Sen	nicondu	ctors pr	oduct	
	2	- Cur	rent rati	ng (30 A	4)		
	3	- Circ	cuit cont	figuratio	n:		
		C =	commo	on catho	de		
	4	- D =	SMPD	package	е		
	5	- Pro	cess typ	be,			
		U =	ultrafas	st recove	ery		
	6	- Volt	tage coo	de (06 =	600 V)		
	7	M3	3 = halo	gen-free	e, RoHS	-compli	iant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-30CDU06-M3/I	2000	2000	13" diameter plastic tape and reel				

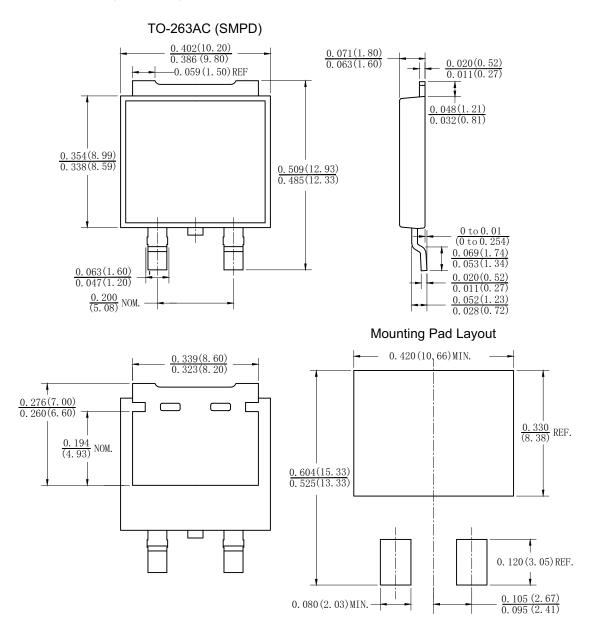
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95604					
Part marking information	www.vishay.com/doc?95566				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96576				





TO-263AC (SMPD)

### **DIMENSIONS** in inches (millimeters)





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