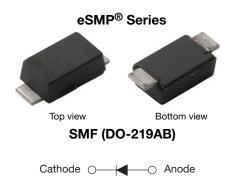
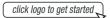


Hyperfast Rectifier, 2 A FRED Pt®



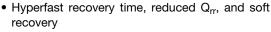
DESIGN SUPPORT TOOLS





PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 A			
V_{R}	200 V			
V _F at I _F (typ. 125 °C)	0.75 V			
t _{rr}	25 ns			
T _J max.	175 °C			
Package	SMF (DO-219AB)			
Circuit configuration	Single			

FEATURES





RoHS

COMPLIANT **HALOGEN**

FREE

- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		200	V	
Average rectified forward current	I _{F(AV)}	T _C = 150 °C ⁽¹⁾	2	۸	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	50	Α	
Operating junction and storage temperature range	T _J , T _{Sta}		-65 to +175	°C	

⁽¹⁾ Device on PCB with 8 mm x 16 mm soldering lands

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
Forward voltage	award valtage	I _F = 2 A	-	0.88	0.95	V
Forward voltage	V _F	I _F = 2 A, T _J = 125 °C	-	0.75	0.82	
De conclude ou const		$V_R = V_R$ rated	-	-	2	
Reverse leakage current I _R	T _J = 125 °C, V _R = V _R rated	-	1	8	- μΑ	
Junction capacitance	C _T	V _R = 200 V	-	8	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50 A$	/μs, V _R = 30 V	-	24	-	ns
Reverse recovery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr}$	= 0.25 A	-	-	25	
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 2 A	-	16	-	
		T _J = 125 °C		-	22	-	
Dools recovery oursent		T _J = 25 °C		-	2	-	Α
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _R = 160 V	-	3	-	^	
Develope receivers shows	0	T _J = 25 °C		-	16	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	30	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case	R _{thJC}	Device mounted on PCB with 8 mm x 16 mm soldering lands	-	-	15	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on PCB with 2 mm x 3.5 mm soldering lands	-	-	130	°C/W
Approximate weight				0.015		g
Approximate weight				0.0005		oz.
Marking device		Case style SMF (DO-219AB)		М	EH	

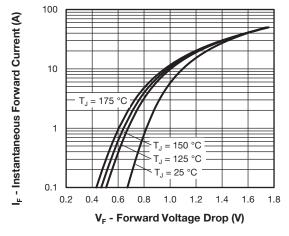


Fig. 1 - Typical Forward Voltage Drop Characteristics

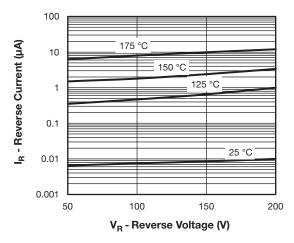


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

D = 0.02D = 0.05D = 0.1D = 0.2D = 0.5

DC

3

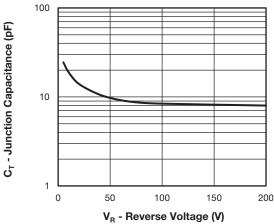
3.5

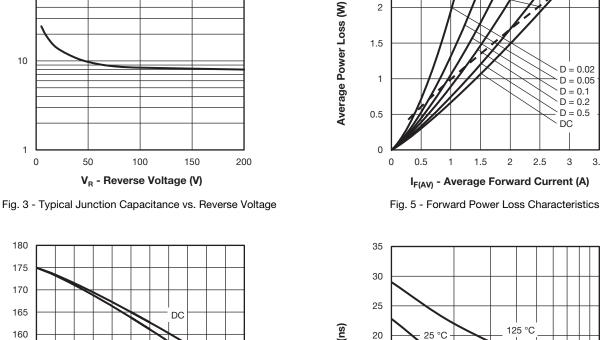
2

2.5

1.5

Vishay Semiconductors





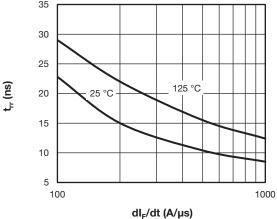
 $0 \quad 0.2 \ 0.4 \ 0.6 \ 0.8 \quad 1 \quad 1.2 \ 1.4 \ 1.6 \ 1.8 \quad 2$ I_{F(AV)} - Average Forward Current (A)

Square wave (D = 0.50)

80 % rated $V_{\rm R}$ applied

See note (1)

Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



2.5

1.5

RMS limit

Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

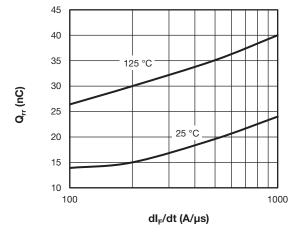


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

Allowable Case Temperature (°C)

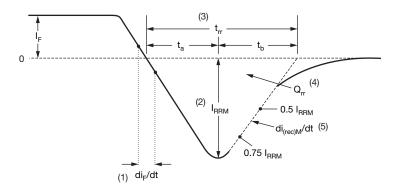
155

150

145

140

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 5); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RBM}$

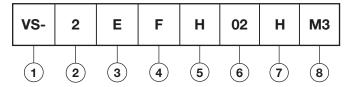
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- **2** Current rating (2 = 2 A)
- 3 Circuit configuration:

E = single diode

- F = SMF package
- 5 Process type,

H = hyperfast recovery

- 6 Voltage code (02 = 200 V)
- 7 H = AEC-Q101 qualified
- M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

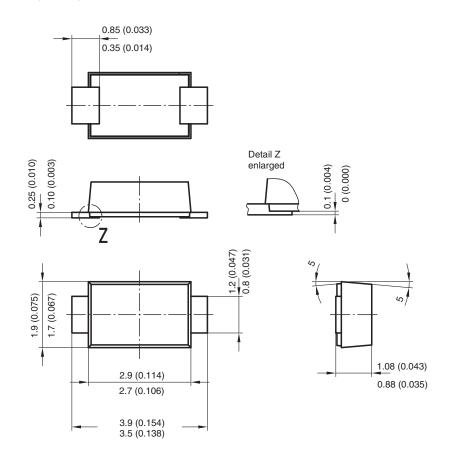
ORDERING INFORMATION (Example)						
PREFERRED P/N	REFERRED P/N QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-2EFH02HM3/I	10 000	10 000	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95572</u>				
Part marking information	www.vishay.com/doc?95618			
Packaging information	www.vishay.com/doc?95577			
SPICE model	www.vishay.com/doc?96013			

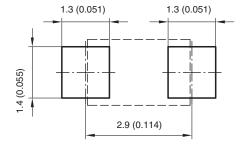


SMF (DO-219AB)

DIMENSIONS in millimeters (inches)



Foot print recommendation:



Created - Date: 15. February 2005 Rev. 3 - Date: 13. March 2007 Document no.:S8-V-3915.01-001 (4) 17247



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Vishay

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