VS-200MT40KPbF

Vishay Semiconductors



Three Phase Bridge (Power Module), 200 A



FEATURES

· Package fully compatible with the industry standard INT-A-PAK power modules series



RoHS

COMPLIANT

- High thermal conductivity package, electrically insulated case
- Low power loss
- Excellent power volume ratio, outline for easy connections to power transistor and IGBT modules
- 4000 V_{RMS} isolating voltage
- UL E78996 approved
- · Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

It extends the existing range of MT...KB bridges an extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
Io		200	А	
	T _C	85	°C	
I _{FSM}	50 Hz	1800	A	
	60 Hz	1880	A	
l ² t	50 Hz	16.2	kA ² s	
	60 Hz	14.7	KA ² S	
l²√t		162	kA²√s	
V _{RRM}		400	V	
T _{Stg} T _J	Range	-40 to +150	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS			
TYPE NUMBER	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA
VS-200MT40KPbF	400	500	6

PRIMARY CHARACTERISTICS 200 A I_{O} 400 V V_{RRM} MTK Package Circuit configuration Three phase bridge

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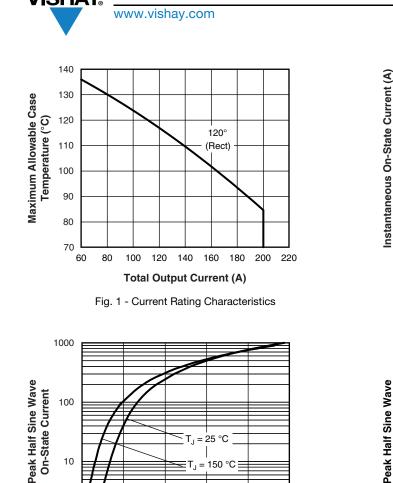
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FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum RMS output current		duction angle		200	A	
at case temperature	10	I _O 120° rect. conduction angle			85	°C
		t = 10 ms	No voltage	Initial T, = T, maximum	1800	A
Maximum peak, one-cycle forward. non-repetitive on state surge current	I _{TSM} t	t = 8.3 ms	reapplied		1880	
		t = 10 ms	100 % V _{RRM} reapplied		1520	
		t = 8.3 ms			1590	
Maximum I ² t for fusing	l ² t	t = 10 ms	No voltage		16.2	kA ² s
		t = 8.3 ms	reapplied		14.7	
		t = 10 ms	100 % V _{RRM}		11.6	
		t = 8.3 ms	reapplied		12.6	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied		162	kA²√s	
Value of threshold voltage	V _{F(TO)}	T, maximum		0.76	V	
Slope resistance	r _t			2.4	mΩ	
Maximum forward voltage drop	V _{FM}	I_{pk} = 200 A, T _J = 25 °C, t _p = 400 µs single junction		1.40	v	
Isolation voltage	VISOL	$T_J = 25$ °C all terminal shorted, f = 50 Hz, t = 1 s		4000	V	

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating and storage temperature range	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation per module	0.12	K/W
		DC operation per junction	0.69	
		120° rect. conduction angle per module	0.14	
		120° rect. conduction angle per junction	0.82	
Maximum thermal resistance, case to heatsink per module	R _{thCS}	Mounting surface smooth, flat and greased. Heatsink compound thermal conductivity = 0.42 W/mK	0.033	
Mounting torque ± 10 % to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow or	4 to 6	Nm
Approximate weight		the spread of the compound. Lubricated threads. 176		g



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ΞT_J = 150 °C

2.0

2.5

3.0

10

1

0.5

1.0

1.5

Number of Equal Amplitude

Half Cycle Current Pulses (N) Fig. 2 - On-State Voltage Drop Characteristics

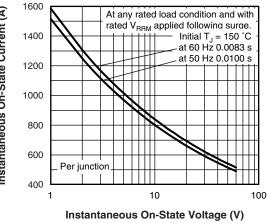
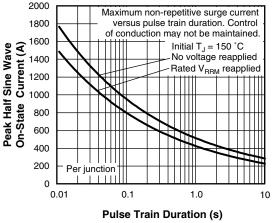


Fig. 3 - Maximum Non-Repetitve Surge Current





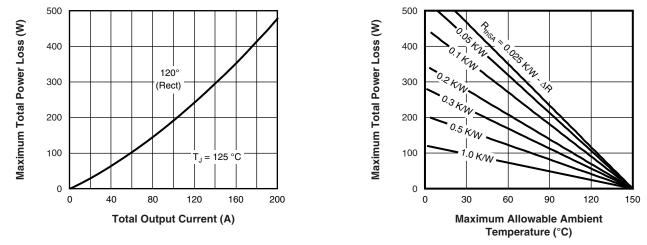


Fig. 5 - Current Rating Nomogram (1 Module Per Heatsink)

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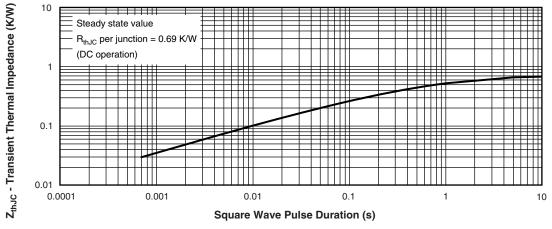
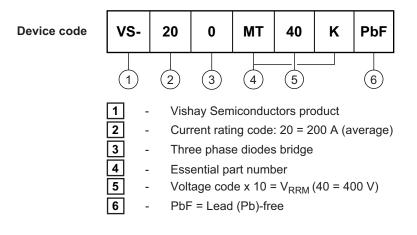


Fig. 6 - Thermal Impedance ZthJC Characteristics

ORDERING INFORMATION TABLE

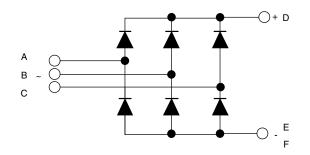
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Note

To order the optional hardware go to <u>www.vishay.com/doc?95172</u>

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95004	

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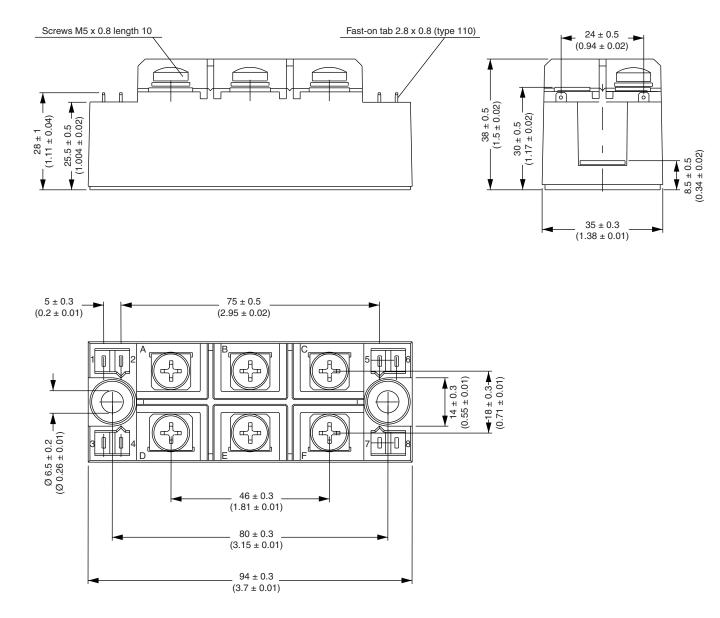


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MTK (with and without optional barrier)

DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

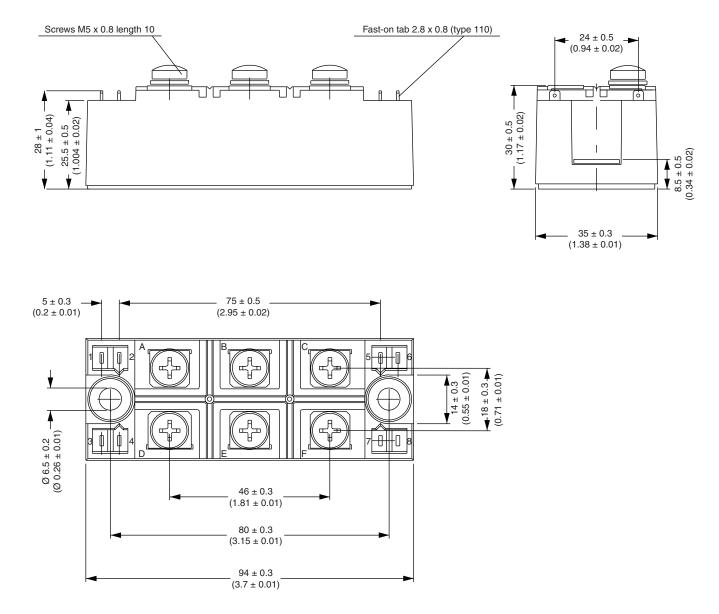
SHAY



Vishay Semiconductors MTK (with and without optional barrier)



DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)





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