RoHS

COMPLIANT

HALOGEN

FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 1.0 A



- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ015HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U					
I _{F(AV)}	Rectangular waveform	1.0	А				
V _{RRM}		15	V				
I _{FSM}	t _p = 5 μs sine	140	А				
V _F	1.0 A _{pk} , T _J = 125 °C	0.21	V				
Т _Ј	Range	-55 to +125	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-10BQ015HM3	UNITS				
Maximum DC reverse voltage	V _R	15	V				
Maximum working peak reverse voltage	V _{RWM}	25	V				

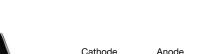
ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_L = 134 °C, rectangular waveform		1.0	А	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	140	A	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse V _{RRM} applied		40		
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 2 \text{ mH}$		1.0	mJ	
Repetitive avalanche current	I _{AR}	$\begin{array}{c} \mbox{Current decaying linearly to zero in 1 } \mu s \\ \mbox{Frequency limited by } T_J \mbox{ maximum } V_A = 1.5 \ x \ V_R \ typical \end{array} \ 1.0$		А		

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1



SMB

1.0 A

15 V

0.21 V

35 mA at 100 °C

125 °C

Single die

1.0 mJ

-0



PRODUCT SUMMARY

Package

I_{F(AV)}

 V_R

V_F at I_F

I_{RM}

T_J max.

Diode variation

E_{AS}

www.vishay.com





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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST	VALUES	UNITS		
		1 A	T.I = 25 °C	0.33	V	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	2 A		0.39		
See fig. 1		1 A	T, = 125 °C	0.21		
		2 A		0.29		
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated V _B	0.5	mA	
See fig. 2		T _J = 100 °C	$v_{\rm R}$ = Raied $v_{\rm R}$	35		
Threshold voltage	V _{F(TO)}		-	V		
Forward slope resistance	r _t	$T_J = T_J maximum$	-	mΩ		
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$, (test signal	390	pF		
Typical series inductance	LS	Measured lead to lead	2.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs		

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +125	°C		
Maximum storage temperature range	T _{Stg}		-55 to +150	C		
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/W		
Approximate weight			0.10	g		
Approximate weight			0.003	oz.		
Marking device		Case style SMB (similar to DO-214AA)	1	С		

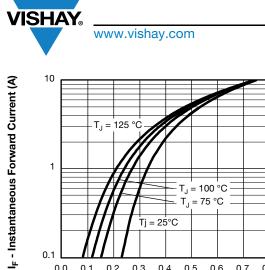
Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB

VS-10BQ015HM3

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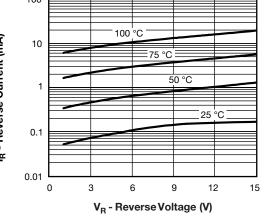


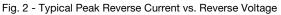
0.1

0.0 0.1 0.2 0.3 0.4

100 I_R - Reverse Current (mA) 10 1 0.1 0.01 3 0 0.5 0.6 0.7 0.8 V_{FM} - Forward Voltage Drop (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics





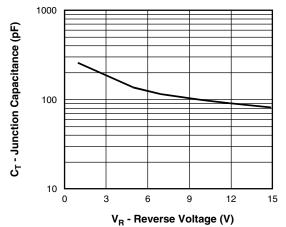


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

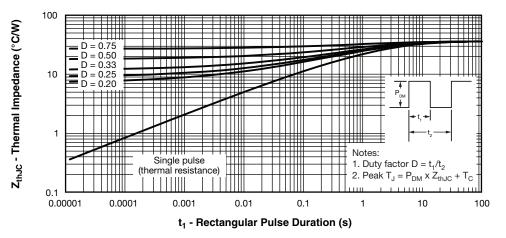
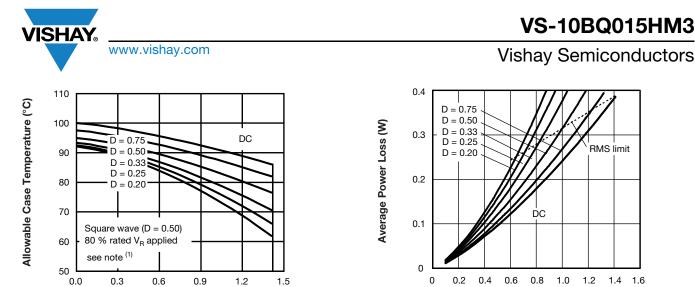


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

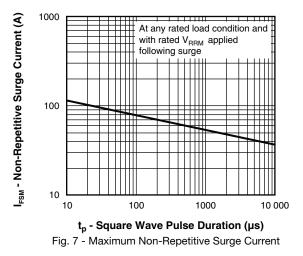
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1.6



Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

Device code	VS-	10	в	Q	015	н	М3
	1	2	3	4	5	6	7
	1 .	- Visl	nay Sen	niconduc	ctors pro	oduct	
	2	- Cur	rent rati	ng			
	3.	- В=	SMB				
	4	- Q =	Schott	ky "Q" se	eries		
	5	- Vol	tage rati	ng (015	= 15 V)		
	6	- H=	AEC-Q	101 qua	lified		
	7	- Env	vironmer	ntal digit	:		
		М3	= halog	en-free,	RoHS o	complia	nt and t

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-10BQ015HM3/5BT	5BT	3200	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95401				
Part marking information	www.vishay.com/doc?95403				
Packaging information	www.vishay.com/doc?95404				
SPICE model	www.vishay.com/doc?95666				

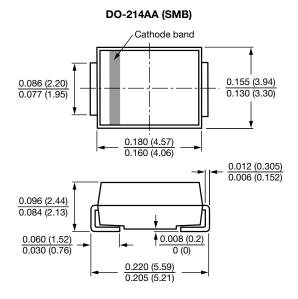


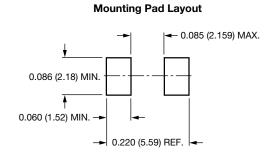
Outline Dimensions

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SMB

DIMENSIONS in inches (millimeters)







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