V40M120C, VI40M120C

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.46$ V at $I_F = 5$ A

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PIN 3 O-



- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
 FREE
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)							
PARAMETER		SYMBOL	V40M120C	VI40M120C	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	120		V		
Maximum average forward rectified current (fig. 1)	per device		40		A		
	per diode	I _{F(AV)}	20				
Peak forward surge current 8.3 ms single half sine-wav superimposed on rated load per diode	e	I _{FSM}	250				
Voltage rate of change (rated V _R)		dV/dt	10 000		V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-40 to) + 1 75	°C		

 $\begin{tabular}{|c|c|c|c|} \hline PRIMARY CHARACTERISTICS \\ \hline I_{F(AV)} & 2 \times 20 \ A \\ \hline V_{RRM} & 120 \ V \\ \hline I_{FSM} & 250 \ A \\ \hline V_F \ at \ I_F = 20 \ A & 0.64 \ V \\ \hline T_J \ max. & 175 \ ^{\circ}C \\ \hline Package & TO-220 \ AB, \ TO-262 \ AA \\ \hline Diode \ variations & Common \ cathode \\ \hline \end{tabular}$



ROHS COMPLIANT



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CASE

PIN 3 O-



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	A T _A = 25 °C	- V _F (1)	0.54	-	- V	
	$I_F = 10 \text{ A}$			0.64	-		
	I _F = 20 A			0.79	0.89		
	$I_F = 5 A$	$T_A = 125 \text{ °C}$		0.46	-		
	I _F = 10 A			0.54	-		
	I _F = 20 A			0.64	0.72		
Reverse current per diode	V _R = 90 V	T _A = 25 °C	I _R ⁽²⁾	4	-	μA	
		T _A = 125 °C		3	-	mA	
		T _A = 25 °C		-	500	μA	
		T _A = 125 °C		6	32	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER		SYMBOL	V40M120C	VI40M120C	UNIT		
Typical thermal resistance ⁽¹⁾	per diode	$R_{ extsf{ heta}JC}$	1.8		°C/W		
	per device		0.85				
	per device	R _{0JA} ⁽²⁾	45	55			

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient dP_D/dT_J < 1/R_{θ JA}

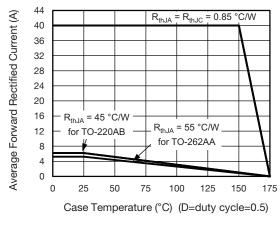
⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	V40M120C-M3/4W	1.88	4W	50/tube	Tube		
TO-262AA	VI40M120C-M3/4W	1.45	4W	50/tube	Tube		

V40M120C, VI40M120C



RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)



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Fig. 1 - Maximum Forward Current Derating Curve

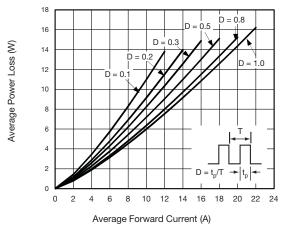


Fig. 2 - Forward Power Loss Characteristics Per Diode

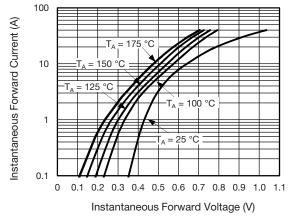


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

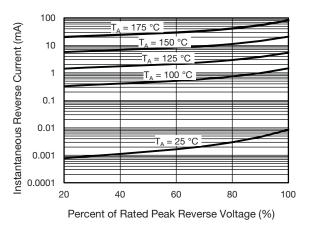


Fig. 4 - Typical Reverse Characteristics Per Diode

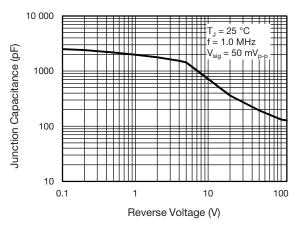


Fig. 5 - Typical Junction Capacitance Per Diode

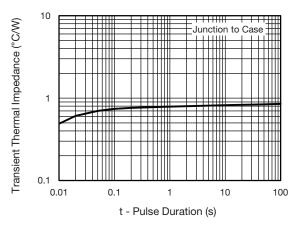


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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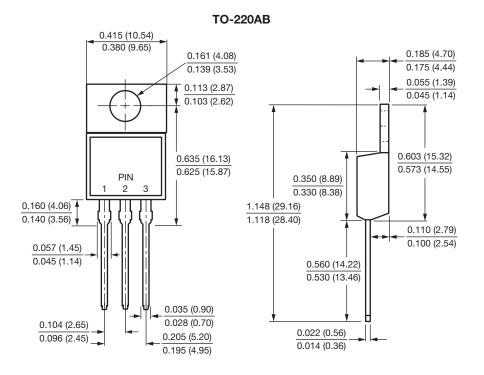
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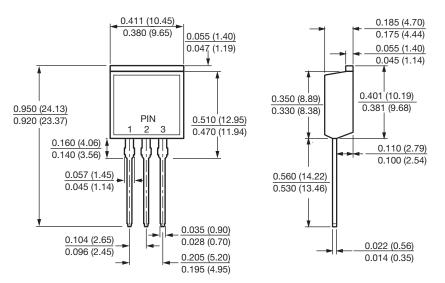
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



TO-262AA





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