HALOGEN

FREE



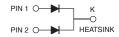
# Vishay General Semiconductor

# High Current Density Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

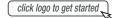
Ultra Low  $V_F = 0.61 \text{ V}$  at  $I_F = 5 \text{ A}$ 







### **DESIGN SUPPORT TOOLS**





PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	20 A			
V <sub>RRM</sub>	150 V			
I <sub>FSM</sub>	150 A			
V <sub>F</sub> at I <sub>F</sub> = 10 A (T <sub>A</sub> = 125 °C)	0.69 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Common cathode			

### **FEATURES**

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- Ideal for automated placement
- Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V20PW15C	UNIT
Device marking code			V20PW15C	
Maximum repetitive peak reverse voltage		$V_{RRM}$	150	V
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	20	А
	per diode		10	А
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	150	А
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C
Storage temperature range		T <sub>STG</sub>	-55 to +150	°C

### Notes

<sup>(1)</sup> With infinite heatsink

 $<sup>^{(2)}</sup>$  The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5.0 \text{ A}$	- T <sub>A</sub> = 25 °C	V <sub>E</sub> (1)	0.81	-	V	
	I <sub>F</sub> = 10 A			1.16	1.24		
	$I_F = 5.0 \text{ A}$	T <sub>A</sub> = 125 °C	T _ 105 °C	<b>V</b> F (1)	0.61	-	v
	I <sub>F</sub> = 10 A		1A = 125 C	0.69	0.75		
Reverse current per diode	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C	- I <sub>R</sub> <sup>(2)</sup>	0.01	-	- mA	
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 125 °C		1.5	-		
	V <sub>P</sub> = 150 V ⊢	T <sub>A</sub> = 25 °C		-	0.15		
		T <sub>A</sub> = 125 °C		3	10		
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	560	=	pF	

### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width  $\leq 5$  ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20PW15C	UNIT	
Tuning the word registered	R <sub>θJA</sub> (1)(2)	55	°C/W	
Typical thermal resistance	R <sub>0JM</sub> (3)	1.8	C/VV	

#### Notes

- $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- $^{(3)}$  Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20PW15C-M3/I	0.20	1	4500	13" diameter plastic tape and reel		
V20PW15CHM3/I (1)	0.20	I	4500	13" diameter plastic tape and reel		

### Note

(1) AEC-Q101 qualified

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# **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

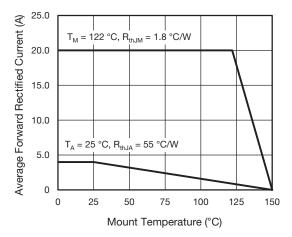


Fig. 1 - Maximum Forward Current Derating Curve

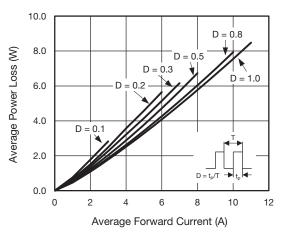


Fig. 2 - Forward Power Loss Characteristics

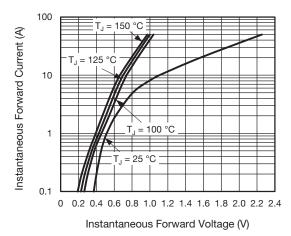


Fig. 3 - Typical Instantaneous Forward Characteristics

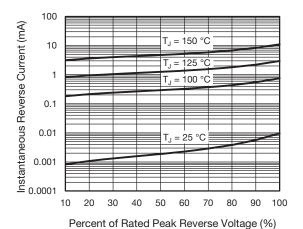


Fig. 4 - Typical Reverse Leakage Characteristics

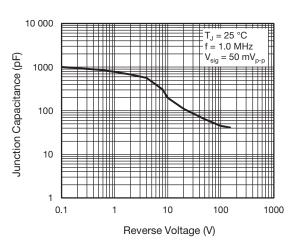


Fig. 5 - Typical Junction Capacitance

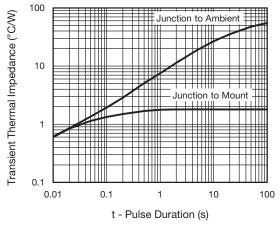


Fig. 6 - Typical Transient Thermal Impedance



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0.075 (1.90) MIN.

0.055 (1.40) MIN.

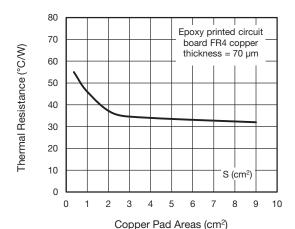
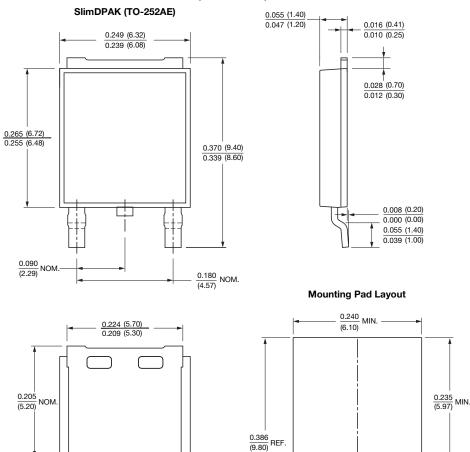


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas

# **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



0.090 NOM:

(2.29)

0.035 (0.90)

0.028 (0.70)



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