

- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation

Dual Low-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier Ultra Low $V_F = 0.43$ V at $I_F = 5.0$ A

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
 Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

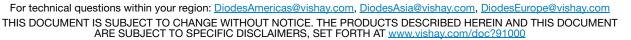
M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20DM60C	UNIT	
Device marking code			V20DM60C		
Maximum repetitive peak reverse voltage		V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	20	А	
	per diode		10	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	150	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +175	- °C	
Storage temperature range		T _{STG}	-55 to +175		

Notes

⁽¹⁾ Mounted on infinite heatsink

⁽²⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{0JA}$

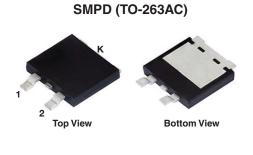


Anode 1 O

Anode 2 C



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _{RRM}	60 V			
I _{FSM}	150 A			
V_F at I_F = 10 A (T_A = 125 °C)	0.54 V			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			



Cathode

eSMP[®] Series



V20DM60C

RoHS

COMPLIANT

Vishay General Semiconductor

V20DM60C



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.52	-	V	
	I _F = 10 A			0.60	0.66		
	I _F = 5 A	T _A = 125 °C		0.43	-		
	I _F = 10 A			0.54	0.60		
Reverse current at rated V_R per diode	V _B = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	0.6	- mA	
	v _R = 00 v	T _A = 125 °C		3.5	14		
Typical junction capacitance	4.0 V, 1 MHz		CJ	1150	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

 $^{(2)}~$ Pulse test: Pulse width $\leq 5~ms$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER SYMBOL V20DM60C		V20DM60C	UNIT		
Typical thermal resistance per device	$R_{\theta JC}^{(1)}$	1.8	°C/W		
	R _{0JA} ⁽²⁾⁽³⁾	58			

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{θ JA} - junction-to-ambient

⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	EFERRED P/N UNIT WEIGHT (g) PACKAGE CODE BASE QUANT		BASE QUANTITY	DELIVERY MODE		
V20DM60C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20DM60CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified

V20DM60C



Vishay General Semiconductor

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

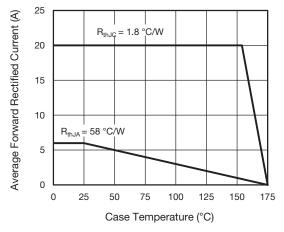


Fig. 1 - Maximum Forward Current Derating Curve

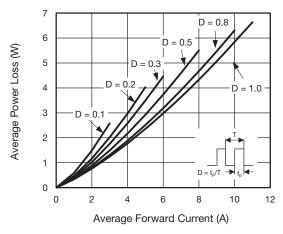


Fig. 2 - Average Power Loss Characteristics

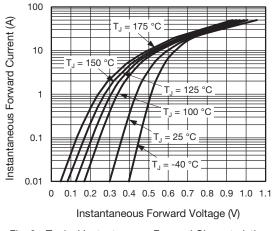


Fig. 3 - Typical Instantaneous Forward Characteristics

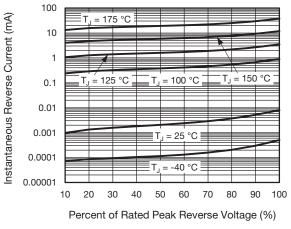


Fig. 4 - Typical Reverse Leakage Characteristics

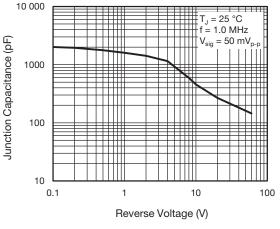


Fig. 5 - Typical Junction Capacitance

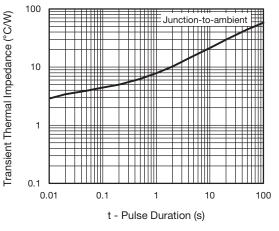


Fig. 6 - Typical Transient Thermal Impedance

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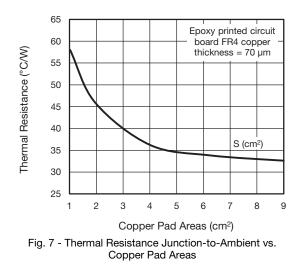
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V20DM60C

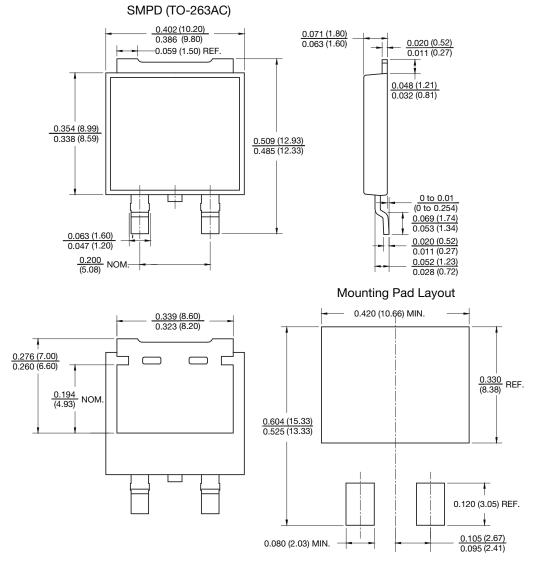




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