HALOGEN

FREE



Vishay General Semiconductor

High Current Density Surface Mount Schottky Barrier Rectifiers



DESIGN SUPPORT TOOLS

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PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.0 A			
V_{RRM}	30 V, 40 V			
I _{FSM}	30 A			
E _{AS}	10 mJ			
V _F	0.40 V, 0.45 V			
T _J max.	150 °C			
Package	SMP (DO-220AA)			
Circuit configuration	Single			

FEATURES

- · Very low profile typical height of 1.0 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency
- · Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

Note

· These devices are not AEC-Q101 qualified

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test Polarity: color band denotes the cathode end

PARAMETER	SYMBOL	SS1P3	SS1P4	UNIT
Device marking code		13	14	
Maximum repetitive peak reverse voltage	V_{RRM}	30	40	V
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	1.0		А
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	3	А	
Non-repetitive avalanche energy at T _J = 25 °C, I _{AS} = 1.5 A, L = 10 mH	E _{AS}	10		
Voltage rate of change (rated V _R)	dV/dt	10 (V/µs	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150		



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	SS1P3	SS1P4	UNIT
Maximum instantaneous forward voltage	I _E = 1.0 A	T _J = 25 °C	V _F ⁽¹⁾	0.50	0.53	- V
	I _F = 1.0 A	T _J = 125 °C	V _F (·)	0.40	0.45	
Maximum reverse current at rated V _R		T _J = 25 °C	I _R ⁽²⁾	15	0	μA
		T _J = 125 °C	IR ^(−)	15		mA
Typical junction capacitance	4.0 V, 1 MHz		C_{J}	70		pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	SS1P3 SS1P4		UNIT		
Typical thermal resistance	R _{θJA} ⁽¹⁾	105		°C/W		
	R _{θJL} ⁽¹⁾	15				
	R ₀ JC ⁽¹⁾	25				

Note

⁽¹⁾ Thermal resistance from junction to ambient and junction to lead mounted on PCB with 5.0 mm x 5.0 mm copper pad areas. R_{θJL} is measured at the terminal of cathode band. R_{θJC} is measured at the top center of the body

ORDERING INFORMATION (Example)						
PREFERRED P/N	P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE		
SS1P3-M3/84A	0.024	84A	3000	7" diameter plastic tape and reel		
SS1P3-M3/85A	0.024	85A	10 000	13" diameter plastic tape and reel		

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

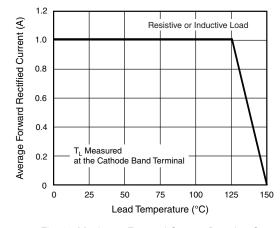


Fig. 1 - Maximum Forward Current Derating Curve

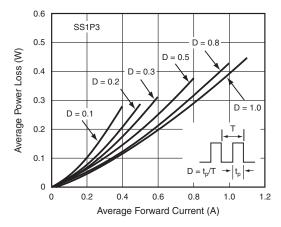


Fig. 2 - Forward Power Loss Characteristics



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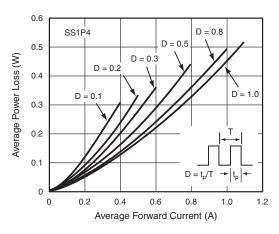


Fig. 3 - Forward Power Loss Characteristics

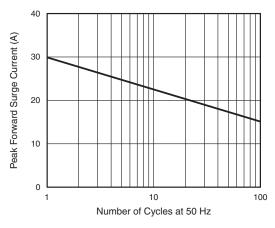


Fig. 4 - Typical Instantaneous Forward Characteristics

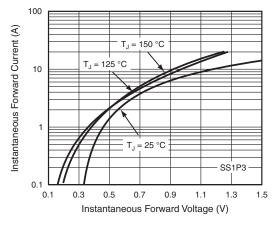


Fig. 5 - Typical Instantaneous Forward Characteristics

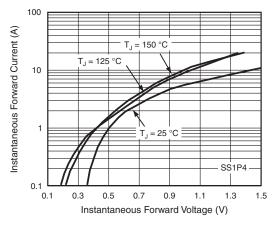


Fig. 6 - Typical Instantaneous Forward Characteristics

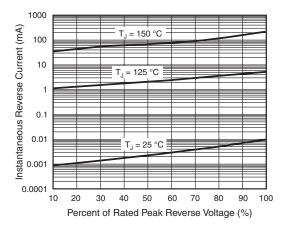


Fig. 7 - Typical Reverse Leakage Characteristics

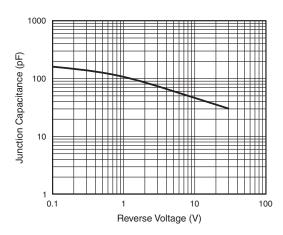


Fig. 8 - Typical Junction Capacitance



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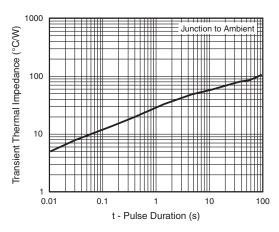
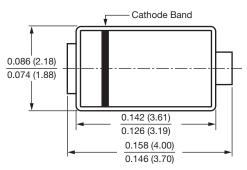
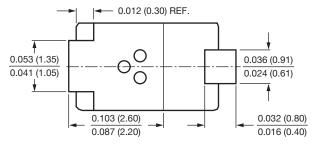


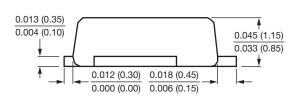
Fig. 9 - Typical Transient Thermal Impedance

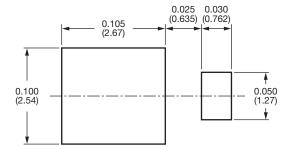
PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMP (DO-220AA)











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