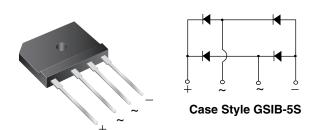
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Vishay General Semiconductor

# Low V<sub>F</sub> Single-Phase Single In-Line Bridge Rectifiers



PRIMARY CHARACTERISTICS				
Package	GSIB-5S			
I <sub>F(AV)</sub>	15 A			
V <sub>RRM</sub>	600 V			
I <sub>FSM</sub>	400 A			
I <sub>R</sub>	10 µA			
$V_F$ at $I_F$ = 7.5 A, $T_A$ = 125 °C	0.73 V			
T <sub>J</sub> max.	150 °C			
Diode variations	In-Line			

## FEATURES

- UL recognition file number E54214, Vol. 1
- Thin single in-line package
- Oxide planar chip junction
- Low forward voltage drop
- High surge current capability
- High case dielectric strength of 2500  $V_{\text{RMS}}$
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances and white-goods applications specially for telecom power supply, high efficiency desktop PC and server SMPS.

### **MECHANICAL DATA**

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked on body

**Mounting Torque:** 10 cm-kg (8.8 in-lbs) maximum **Recommended Torque:** 5.7 cm-kg (5 in-lbs)

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	LVB1560	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	600	V	
Maximum average forward rectified output current at	T <sub>C</sub> = 125 °C	I <sub>O</sub> <sup>(1)</sup>	15	_	
	T <sub>A</sub> = 25 °C	I <sub>O</sub> <sup>(2)</sup>	3.6	A	
Non-repetiitive peak forward surge current 8.3 ms single sine-wave, $T_J$ = 25 $^\circ\text{C}$		I <sub>FSM</sub>	400	А	
Rating for fusing (t < 8.3 ms)	T <sub>J</sub> = 25 °C	l <sup>2</sup> t	664	A <sup>2</sup> s	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150	°C	

#### Notes

<sup>(1)</sup> Unit case mounted on aluminum plate heatsink

<sup>(2)</sup> Units mounted on PCB without heatsink

1

RoHS

LVB1560



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 7.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.87	0.90	V
		T <sub>A</sub> = 125 °C		0.73	-	
Reverse current per diode	V <sub>R</sub> = 600 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.2	10	μA
		T <sub>A</sub> = 125 °C		60	-	
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	1.8	-	μs
Typical junction capacitance	4.0 V, 1 MHz		CJ	260	-	pF

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	LVB1560	UNIT	
Maximum thermal resistance	R <sub>0JA</sub> <sup>(2)</sup>	25	°C/W	
	R <sub>0JC</sub> <sup>(1)</sup>	1.0	0/11	

#### Notes

<sup>(1)</sup> With heatsink

<sup>(2)</sup> Without heatsink, free air

EMC SURGE IMMUNITY TEST STANDARD ( $T_A = 25 \text{ °C}$ , unless otherwise noted)						
STANDARD	TANDARD TEST TYPE TEST CONDITIONS		SYMBOL	CLASS	VALUE	
IEC 61000-4-5	Power supply coupling mode, line to line	1.2/50 $\mu s$ waveform, R = 2 $\Omega,$ T_A = 25 °C $^{(1)}$	V <sub>PEAK</sub>	-	6 kV maximum	

#### Note

(1) Immunity to IEC 61000-4-5 peak pulse voltage test, 1.2/50 µs, 2 Ω, 5 times each of positive and negative polarity test

ORDERING INFORMATION (Example)							
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY DELIVERY MO							
LVB1560-M3/45	6.9	45	20	Tube			

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

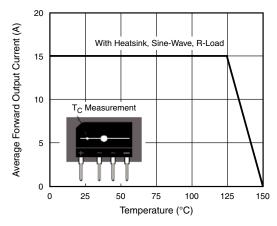


Fig. 1 - Derating Curve Output Rectified Current

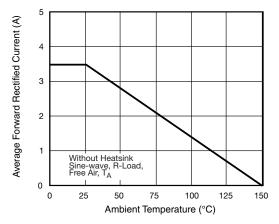


Fig. 2 - Forward Current Derating Curve

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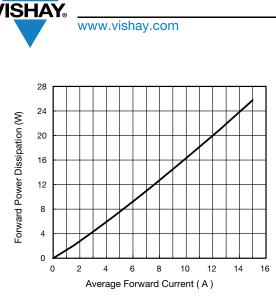
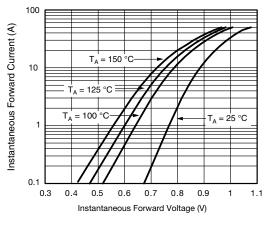
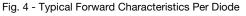


Fig. 3 - Forward Power Dissipation







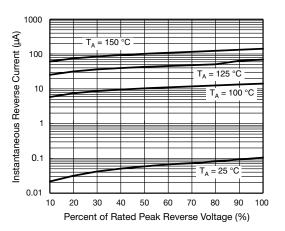
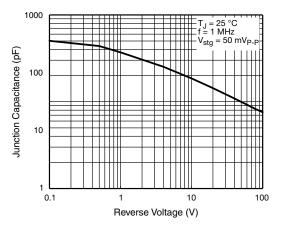
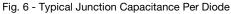
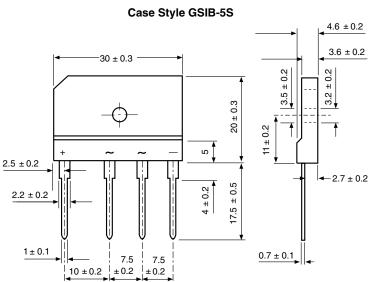


Fig. 5 - Typical Reverse Characteristics Per Diode







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