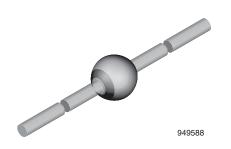


# Vishay Semiconductors

# **Standard Avalanche Sinterglass Diode**



#### **FEATURES**

- · Glass passivated junction
- · Hermetically sealed package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS

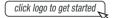
**HALOGEN** 

FREE

#### **APPLICATIONS**

- High voltage rectification
- Effficiency diode in horizontal deflection circuits

#### **DESIGN SUPPORT TOOLS**





#### **MECHANICAL DATA**

Case: SOD-64

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 858 mg

ORDERING INFORMATION (Example)						
DEVICE NAME	DEVICE NAME ORDERING CODE TAPED UNITS MINIMUM ORDER QUA					
BY228-15	BY228-15TR	2500 per 10" tape and reel	12 500			
BY228-15	BY228-15TAP	2500 per ammopack	12 500			

PARTS TABLE						
PART	TYPE DIFFERENTIATION	PACKAGE				
BY228-13	V <sub>R</sub> = 1000 V; I <sub>F(AV)</sub> = 3 A	SOD-64				
BY228-15	$V_R = 1200 \text{ V}; I_{F(AV)} = 3 \text{ A}$	SOD-64				

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
Reverse voltage	See electrical characteristics	BY228-13	$V_R$	1000	V		
neverse voitage	See electrical characteristics	BY228-15	$V_R$	1200	V		
Peak reverse voltage, non repetitive	I <sub>R</sub> = 100 μA	BY228-13	$V_{RSM}$	1300	V		
reak reverse voltage, non repetitive	I <sub>R</sub> = 100 μA	BY228-15	$V_{RSM}$	1500	V		
Peak forward surge current	$t_p = 10 \text{ ms}$ , half sine wave		I <sub>FSM</sub>	50	Α		
Average forward current			I <sub>F(AV)</sub>	3	Α		
Junction temperature			Tj	140	°C		
Storage temperature range			T <sub>stg</sub>	-55 to +175	°C		
Non repetitive reverse avalanche energy	I <sub>(BR)R</sub> = 0.4 A		E <sub>R</sub>	10	mJ		

MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Junction ambient	On PC board with spacing 25 mm	R <sub>thJA</sub>	70	K/W		

 $V_R = V_{RRM}$ 

## Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 5 A		$V_{F}$	-	-	1.5	V
	V <sub>R</sub> = 1000 V	BY228-13	I <sub>R</sub>	-	2	5	μA
Reverse current	V <sub>R</sub> = 1200 V	BY228-15	I <sub>R</sub>	-	2	5	μA
neverse current	V <sub>R</sub> = 1000 V, T <sub>j</sub> = 140 °C	BY228-13	I <sub>R</sub>	-	-	140	μA
	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 140 °C	BY228-15	I <sub>R</sub>	-	-	140	μA
Reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, i <sub>R</sub> = 0.25 A		t <sub>rr</sub>	-	-	2	μs
Total reverse recovery time	$I_F = 1 \text{ A}, - dI_F/dt = 0.05 \text{ A/}\mu\text{s}$		t <sub>rr</sub>	-	-	20	μs

3.5

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

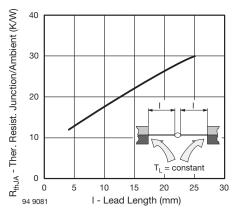


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

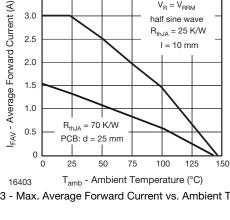


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

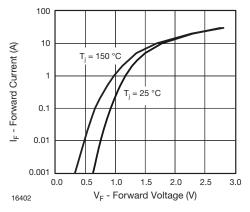


Fig. 2 - Forward Current vs. Forward Voltage

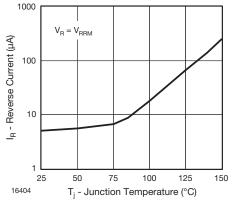


Fig. 4 - Reverse Current vs. Junction Temperature

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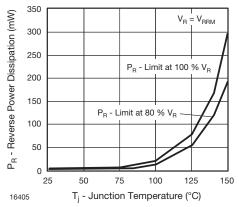


Fig. 5 - Diode Capacitance vs. Reverse Voltage

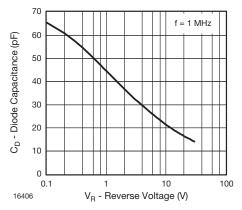
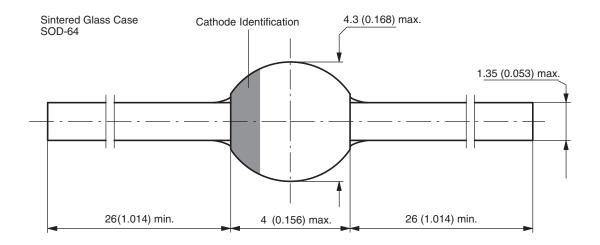


Fig. 6 - Diode Capacitance vs. Reverse Voltage

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-64



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