

**Vishay Semiconductors** 

## Small Signal Schottky Diodes, Single & Dual

#### **Features**

- · These diodes feature very low turn-on voltage and fast switching
- · These devices are protected by a PN junction guard ring against excessive voltage, such as electrostatic discharges



AEC-Q101 qualified

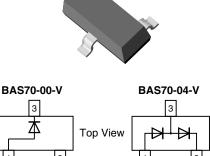
 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

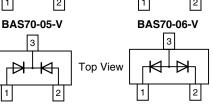
#### **Mechanical Data**

Case: SOT-23

Weight: approx. 8.8 mg **Packaging Codes/Options:** 

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 3 k per 7" reel (8 mm tape), 15 k/box





#### **Parts Table**

Part	Ordering code	Type Marking	Remarks
BAS70-00-V	BAS70-00-V-GS18 or BAS70-00-V-GS08	73	Tape and Reel
BAS70-04-V	BAS70-04-V-GS18 or BAS70-04-V-GS08	74	Tape and Reel
BAS70-05-V	BAS70-05-V-GS18 or BAS70-05-V-GS08	75	Tape and Reel
BAS70-06-V	BAS70-06-V-GS18 or BAS70-06-V-GS08	76	Tape and Reel

#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Repetitive peak reverse voltage		$V_{RRM} = V_{RWM} = V_{R}$	70	V
Forward continuous current		I <sub>F</sub>	200 <sup>1)</sup>	mA
Surge forward current	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	600 <sup>1)</sup>	mA
Power dissipation <sup>1)</sup>		P <sub>tot</sub>	200 <sup>1)</sup>	mW

<sup>1)</sup> Device on fiberglass substrate, see layout on next page

#### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

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Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		R <sub>thJA</sub>	500 <sup>1)</sup>	K/W
Junction temperature		T <sub>j</sub>	125	°C
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C

<sup>1)</sup> Device on fiberglass substrate, see layout on next page

# BAS70-00-V to BAS70-06-V

## **Vishay Semiconductors**



#### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

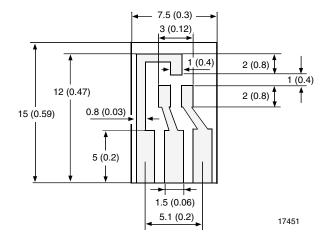
Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Reverse breakdown voltage	$I_R = 10 \mu A \text{ (pulsed)}$	$V_{(BR)}$	70			V
Leakage current	V <sub>R</sub> = 50 V	I <sub>R</sub>		20	100	nA
Forward voltage	I <sub>F</sub> = 1.0 mA	V <sub>F</sub>			410	mV
Forward voltage <sup>1)</sup>	I <sub>F</sub> = 15 mA,	V <sub>F</sub>			1000	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>		1.5	2	pF
Reverse recovery time	$I_F = I_R = 10 \text{ mA}, i_R = 1 \text{ mA},$ $R_L = 100 \Omega$	t <sub>rr</sub>			5	ns

 $<sup>^{1)}</sup>$  Pulse test;  $t_p \leq 300~\mu s$ 

## Layout for R<sub>thJA</sub> test

Thickness:

Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)

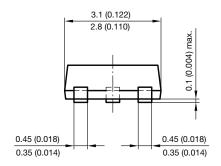


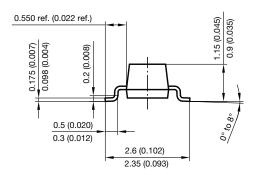


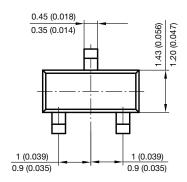


## **Vishay Semiconductors**

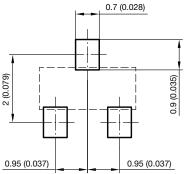
### Package Dimensions in millimeters (inches): SOT-23







Foot print recommendation:



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## **Legal Disclaimer Notice**

Vishay

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