

FlipKY[®], 1 A Chip Scale Package Schottky Barrier Rectifier



FlipKY®

PRODUCT SUMMARY I_{F(AV)} 1 A V_R 30 V V_F at I_F 0.33 V I_{RM} max. at 25 °C 100 μA I_{RM} max. at 125 °C 30 mA T_J max. 150 °C E_{AS} 10 mJ

FEATURES

- Ultra low V_F per footprint area
- · Low leakage
- · Low thermal resistance
- One-fifth footprint of SMA
- Super low profile (0.6 mm)
- · Available tested on tape and reel
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- · Reverse polarity protection
- · Current steering
- Freewheeling
- Flyback
- Oring

DESCRIPTION

Vishay's FlipKY® product family utilizes wafer level chip scale packaging to deliver Schottky diodes with the lowest $V_{\rm F}$ to PCB footprint area in industry. The four bump 1.5 mm x 1.5 mm devices can deliver up to 1 A and occupy only 2.3 mm² of board space. The anode and cathode connections are made through solder bump pads on one side of the silicon enabling designers to strategically place the diodes on the PCB. This design not only minimizes board space but also reduces thermal resistance and inductance, which can improve overall circuit efficiency.

Typical applications include hand-held, portable equipment such as cell phones, MP3 players, bluetooth, GPS, PDAs, and portable hard disk drives where space savings and performance are crucial.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	MAX.	UNITS		
V _{RRM}		30	V		
I _{F(AV)}	Rectangular waveform	1	A		
I _{FSM}		250			
V _F	1 A _{pk} , T _J = 125 °C	0.33	V		
T _J		- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-FCSP130LTR	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}			

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _{PCB} = 120 °C, rectangular waveform		1.0	
Maximum peak one cycle	I	5 μs sine or 3 μs rect. pulse Following any rated	Following any rated	220	А
non-repetitive surge current at 25 °C	IFSM 10 m	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	21	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2.0 \text{A}, L = 5.0 \text{mH}$		10	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2.0	Α

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
		1 A	T _{.1} = 25 °C	0.41	0.45	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	2 A	11 = 23 0	0.46	0.50	V
See fig. 1	V _{FM} (*)	1 A	T. ₁ = 125 °C	0.29	0.33	V
		2 A	1J = 125 C	0.37	0.40	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	1) $V_R = \text{Rated } V_R$ $T_J = 25 ^{\circ}\text{C}$ $T_J = 125 ^{\circ}\text{C}$	T _J = 25 °C	30	100	μA
See fig. 2	IRM ('')		10	30	mA	
Maximum junction capacitance	C _T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		-	210	pF
Maximum voltage rate of charge	dV/dt	Rated V _R		-	10	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C	
Typical thermal resistance, junction to PCB	R _{thJL} (2)	DC operation	40	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		62	C/VV	

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

(2) Mounted 1" square PCB





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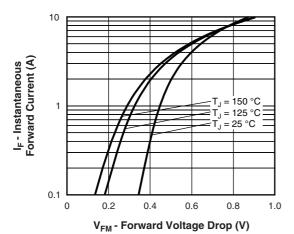


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

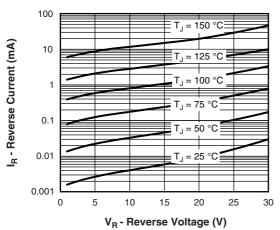


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

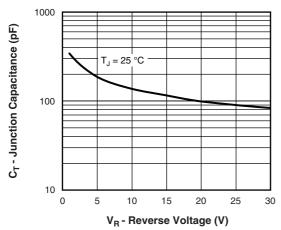
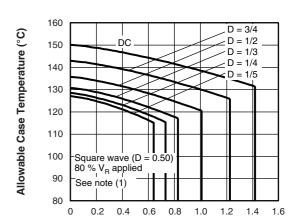


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



I_{F(AV)} - Average Forward Current (A)
Fig. 4 - Maximum Allowable Case Temperature vs.
Average Forward Current (Per Leg)

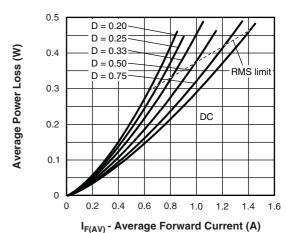


Fig. 5 - Forward Power Loss Characteristics (Per Leg)

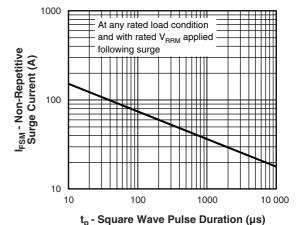


Fig. 6 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

(1) Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at 80 % V_R applied





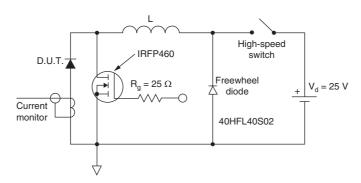


Fig. 7 - Unclamped Inductive Test Circuit

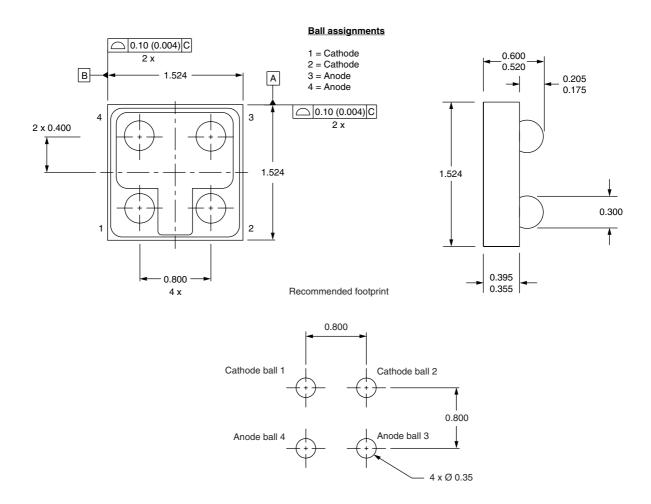
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95282		
Part marking information	www.vishay.com/doc?95281		
Packaging information	www.vishay.com/doc?95062		
Spice model	www.vishay.com/doc?95450		



Vishay High Power Products

FlipKY® 1 A/1.5 A (Large Bump Pad Design)

DIMENSIONS in millimeters



Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Controlling dimension: millimeter

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