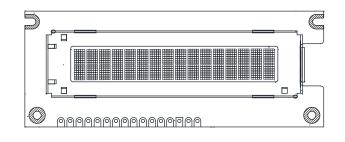


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16 x 2 Character OLED



MECHANICAL DATA							
ITEM	STANDARD VALUE	UNIT					
Module dimension	84.0 x 44.0 x 9.67						
Viewing area	58.22 x 13.52						
Active area	56.22 x 11.52						
Dot size	0.57 x 0.67						
Dot pitch	0.60 x 0.70	mm					
Mounting hole	76.0 x 36.0						
Character size	2.97 x 5.57						
Character pitch	3.55 x 5.95						

FEATURES

- Type: character
- Display format: 16 x 2 characters
- Built-in controller: SSD1311
- Duty cycle: 1/16
- +5 V power supply
- Interface: 6800
- With polarizer
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

ABSOLUTE MAXIMUM RATINGS									
ITEM	SYMBOL	STANDAF	ND VALUE	UNIT					
	STIVIDUL	MIN.	MAX.	UNIT					
Supply voltage for logic	V_{DD} to V_{SS}	-0.3	6.0	V					
Input voltage	VI	-0.3	V _{DD}	V					
Operating temperature	T _{OP}	-40	+80	°℃					
Storage temperature	T _{STG}	-40	+80	C C					

Notes

All the above voltages are on the basis of "V_{SS} = 0 V"

When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to "Electrical Characteristics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate

ELECTRICAL CHARACTERISTICS										
ITEM	CYMBOL	CONDITION	ST	ANDARD VAL	.UE	UNIT				
	SYMBOL CONDITION		MIN.	TYP.	MAX.					
Supply voltage for logic	V _{DD}	-	4.8	5.0	5.3					
Input high voltage	V _{IH}	-	0.8 V _{DD}	-	-					
Input low voltage	V _{IL}	-	-	-	0.2 V _{DD}	V				
Output high voltage	V _{OH}	I _{OH} = -0.5 mA	0.9 V _{DD}	-	-					
Output low voltage	V _{OL}	I _{OL} = 0.5 mA	-	-	0.1 V _{DD}					
50 % check board operating current	I _{DD}	$V_{DD} = 5 V$	43	50	52	mA				

Note

• When you use 5 V for V_{DDI/O} please do not use 3 V or 3.3 V for logic I/O this will cause module does not work

OPTIONS								
EMITTING COLOR								
YELLOW	GREEN	RED	BLUE	WHITE				
Yes	-	-	-	-				

Revision: 10-Aug-17



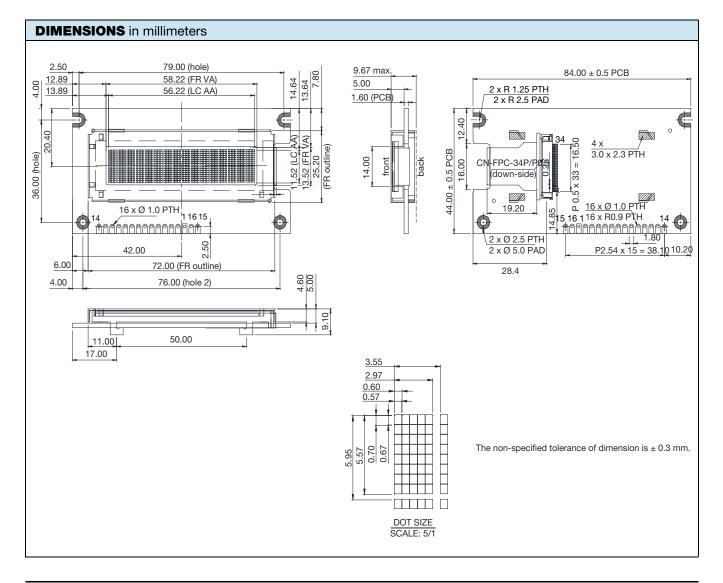
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INTERF/	INTERFACE PIN FUNCTION								
PIN NO.	SYMBOL	PIN TYPE	DESCRIPTION						
1	V _{SS}	Р	Ground pin						
2	V _{DD}	Р	Power supply for core logic operation. A capacitor should be connected between V_{DD} and V_{SS} under all circumstances						
3	NC	-	No connection						
4	D / C#	I	This pin is data / command control pin connecting to the MCU. When the pin is pulled "high", the data at D (7 : 0) will be interpreted as data. When the pin is pulled "low", the data at D (7 : 0) will be transferred to a command register						
5	R/W#	I	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as read / write (R / W#) selection input. Read mode will be carried out when this pin is pulled "high" and write mode when "low"						
6	E	I	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the enable (E) signal. Read / write operation is initiated when this pin is pulled "high" and the chip is selected						
7 to 14	DB0 to DB7	I/O	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie "low"						
15	NC	-	No connection						
16	NC	-	No connection						



Revision: 10-Aug-17

2

Document Number: 37798

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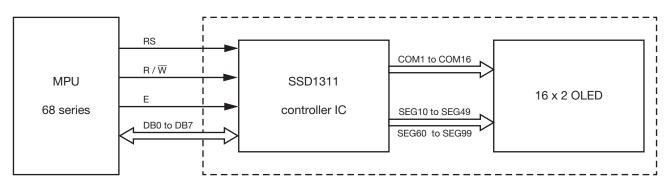
MODULE CLA	SSIFICATION INFORM	ATION						
OLED -	016 O O02 G C C C C C C C C C C	- L P P 5 N 0 0 000 I I I I I I I I I 6 7 8 9 10 11 12 13						
1	Brand	Vishay Intertechnology, Inc.						
2	Horizontal format	16 characters						
		H: graphic type						
3	Display type	N: character type						
3	Display type	O: COG type						
		Y: tab type						
4	Vertical format	2 lines						
5	Serials code	G						
		A: amber						
		B: blue						
		C: full color						
6	Emitting color	G: green						
6		L: yellow						
		R: red						
		W: white						
		Y: yellow green						
7	Polarizer	N: without polarizer						
1	Folarizer	P: with polarizer						
8	Display mode	A: active matrix						
0	Display mode	P: passive matrix						
9	Driver voltage	3: 3.0 V to 3.3 V						
	Divervoltage	5: 5.0 V						
10	Touch panel	N: without touch panel						
		T: with touch panel						
		0: standard						
		1: sunlight readable						
11	Products type	2: transparent OLED (TOLED)						
		3: flexible OLED						
		4: OLED for lighting						
		0: standard (A level)						
		2: B level						
12	Product grades	3: C level						
		4: high class (AA level)						
		5: customer offerings						
13	Serial number	Application serial number (000 to ZZZ)						

3



GENERAL SPECIFICATIONS							
ITEM	DIMENSION	UNIT					
Number of characters	16 characters x 2 lines						
Module dimension	84.0 x 44.0 x 9.67	mm					
View area	58.22 x 13.52	mm					
Active area	56.22 x 11.52	mm					
Dot size	0.57 x 0.67	mm					
Dot pitch	0.60 x 0.70	mm					
Character size	2.97 x 5.57	mm					
Character pitch	3.55 x 5.95	mm					
Panel type	OLED, yellow						
Duty	1/16						
IC	SSD1311						

FUNCTION BLOCK DIAGRAM

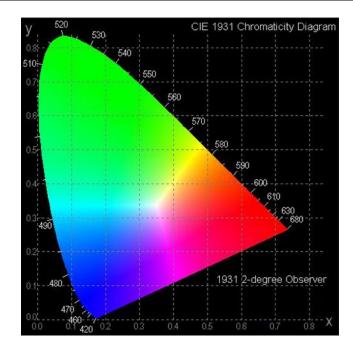


Display position	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DD RAM address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
DD RAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F





OPTICAL CHARACTERISTICS									
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT			
View angle	(V) 0		160	-	-	dog			
	(H)φ		160	-	-	deg			
Contrast ratio	CR	Dark	2000 : 1	-	-	-			
Response time	t _{rise}		-	10	-	μs			
nesponse une	t _{fall}		-	10	-	μs			
Display with 50 % check board brightness			110	130	-	cd/m ²			
CIE _x (yellow)	(CIE1931)		0.45	0.47	0.49				
CIE _y (yellow)	(CIE1931)		0.48	0.50	0.52				



OLED LIFETIME			
ITEM	CONDITIONS	MIN.	TYP.
Operating life time	$T_A = 25$ °C, initial 50 % check board brightness typical value	50 000 h	-

Notes

- Life time is defined the amount of time when the luminance has decayed to < 50 % of the initial value
- This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (PDF) for the product under normal use conditions
- Screen saving mode will extend OLED lifetime

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RELABILITY				
ENVIRONMENTAL TEST				
TEST ITEM		CONTENT OF TEST		TEST CONDITION
High temperature storage	Endurance test app long time	lying the high storag	e temperature for a	80 °C, 240 h
Low temperature storage	Endurance test app long time	lying the low storag	-40 °C, 240 h	
High temperature operation	Endurance test ap current) and the the	plying the electric s rmal stress to the ele		80 °C, 240 h
Low temperature operation	Endurance test ap temperature for a lo	pplying the electric ng time	stress under low	-40 °C, 240 h
High temperature / humidity storage	Endurance test ap humidity storage for	plying the high ten a long time	perature and high	60 °C, 90 % RH, 240 h
	Endurance test appl -40 °C	ying the low and hig 25 °C	h temperature cycle 80 °C	
Temperature cycle		←		-40 °C / 80 °C, 100 cycles
	30 min	5 min	30 min	
		1 cycle		
MECHANICAL TEST				
Vibration test	Endurance test app and using	lying the vibration d	uring transportation	10 Hz to 22 Hz for 1.5 mm peak-to-peak 22 Hz to 500 Hz for 1.5 <i>g</i> , total 0.5 h
Shock test	Constructional and shock during transp	mechanical enduran ortation	ce test applying the	50 <i>g</i> half sin wave 11 ms, 3 times of each direction
Atmospheric pressure test	Endurance test app transportation by air	blying the atmosphe	115 mbar, 40 h	
OTHERS	-			
Static electricity test	Endurance test appl	ying the electric stre	ss to the terminal	$V_S = \pm 600$ V (contact), ± 800 V (air), R _S = 330 Ω , C _S = 150 pF, 10 times

Note

Supply voltage for OLED system = operating voltage at 25 °C

TEST AND MEASUREMENT CONDITIONS

- 1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hours prior to conducting the failure test at 23 °C \pm 5 °C, 55 % \pm 15 % RH
- 2. All-pixels-on is used as operation test pattern
- 3. The degradation of polarizer are ignored for high temperature storage, high temperature / humidity storage, temperature cycle

EVALUATION CRITERIA

- 4. The function test is OK
- 5. No observable defects
- 6. Luminance: > 50 % of initial value
- 7. Current consumption: within ± 50 % of initial value

APPENDIX: RESIDUE IMAGE

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.

6



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INS	PECTION SPECI	FICATION						
NO.	ITEM		CRITERIO	N		AQL		
01	Electrical testing Black or white	 1.1 Missing vertical, horizontal segment, segment contrast defect 1.2 Missing character, dot or icon 1.3 Display malfunction 1.4 No function or no display 1.5 Current consumption exceeds product specifications 1.6 OLED viewing angle defect 1.7 Mixed product types 1.8 Contrast defect 2.1 White and black spots on display ≤ 0.25 mm, no more than three white or black spots present 						
02	spots on OLED (display only)	2.2 Densely spaced: no more	e than two spots or lin	es within 3 mm		2.5		
	OLED black spots,	3.1 Round type: as following $\Phi = (x + y) / 2$ $\Rightarrow x = \frac{y}{y}$	drawing	SIZE $\Phi \le 0.10$ $0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.25$ $0.25 < \Phi$	ACCEPTABLE QTY Accept no dense 2 1 0	2.5		
03	white spots, contamination							
	(non-display)	3.2 Line type (as following drawing) → L ↓ W	LENGTH - L≤3.0 L≤2.5 -	WIDTH $W \le 0.02$ $0.02 < W \le 0.03$ $0.03 < W \le 0.05$ $0.05 < W$	ACCEPTABLE QTY Accept no dense 2 As round type	2.5		
04	Polarizer bubbles	SIZE Φ ACCEPTABLE QTYIf bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.SIZE Φ ACCEPTABLE QTY $\Phi \le 0.20$ Accept no dense $0.20 < \Phi \le 0.50$ 3 $0.50 < \Phi \le 1.00$ 2 $1.00 < \Phi$ 0Total QTY3						
05	Scratches	Follow no. 3 OLED black spots,	white spots, contami	nation				
06	Chipped glass	Symbols: x: chip length k: seal width l: electrode pad length 6.1 General glass chip: 6.1.1 Chip on panel surface an k k k k kk k k k k k k k k k	y: chip width t: glass thickness d crack between pane y y y chip width Not over viewing a Not exceed 1/3 k	els: $x \rightarrow y \rightarrow z$ area	z: chip thickness a: OLED side length x clip length $x \le 1/8$ a $x \le 1/8$ a	2.5		

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10.	ITEM		CRITERION		AQI
		6.1.2 Corner crack:	^z y		
06	Chipped glass				2.5
		z: chip thickness	y: chip width	x: chip length	
		$z \le 1/2$ t	Not over viewing area	$x \le 1/8 a$	
		$1/2 t < z \le 2 t$	Not exceed 1/3 k	x ≤ 1/8 a	
			Not exceed 1/3 k	X 2 1/0 a	
		 Note If there are 2 or more chips, x is total length of each chip 			
		Symbols:			
		x: chip length	y: chip width	z: chip thickness	
		k: seal width	t: glass thickness	a: OLED side length	
		I: electrode pad length	L glass therees	a. OLLD side length	
		6.2 Protrusion over term	inali		
		6.2.1 Chip on electrode pa	ad:		
				z	
			X		
		y: chip width	x: chip length	z: chip thickness	
				0 < z ≤ t	
		y ≤ 0.5 mm	x ≤ 1/8 a	0 12 21	
		y ≤ 0.5 mm 6.2.2 Non-conductive por			
6	Glass crack				2.5
6	Glass crack				2.5
6	Glass crack				2.5
6	Glass crack		tion:		2.5
6	Glass crack				2.5
5	Glass crack	6.2.2 Non-conductive por	tion:		2.5
3	Glass crack	6.2.2 Non-conductive por	tion:		2.5
3	Glass crack	6.2.2 Non-conductive por	tion:	z: chip thickness	2.5
3	Glass crack	6.2.2 Non-conductive por y chip width y ≤ 1	tion:		2.5
5	Glass crack	6.2.2 Non-conductive por y: chip width $y \le 1$ Notes	tion: x: chip length $x \le 1/8 a$	$z: chip thickness 0 < z \le t$	2.5
5	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ $x \le 1/8 a$ $x \le 1/8 a$	$\frac{z: chip thickness}{0 < z \le t}$	2.5
5	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te	tion: x: chip length $x \le 1/8 a$ the sthe ITO terminal, over 2/3 of the ITO	$\frac{z: chip thickness}{0 < z \le t}$	2.5
5	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te	tion: x: chip length $x \le 1/8 a$ x = sche ITO terminal, over 2/3 of the ITO terminal specifications t sealed by the customer, the alignment	$\frac{z: chip thickness}{0 < z \le t}$	2.5
6	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ x = sche ITO terminal, over 2/3 of the ITO terminal specifications t sealed by the customer, the alignment	$\frac{z: chip thickness}{0 < z \le t}$	2.5
6	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ the set the ITO terminal, over 2/3 of the ITO the secifications the sealed by the customer, the alignment ince and internal crack:	$\frac{z: chip thickness}{0 < z \le t}$	2.5
6	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ t sealed by the customer, the alignment ince and internal crack: y: width	$\frac{z: chip thickness}{0 < z \le t}$ D must remain and be inspected t mark not be damaged $x: length$	2.5
6	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ t sealed by the customer, the alignment ince and internal crack: y: width	$\frac{z: chip thickness}{0 < z \le t}$ D must remain and be inspected t mark not be damaged $x: length$	2.5
5	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ t sealed by the customer, the alignment ince and internal crack: y: width	$\frac{z: chip thickness}{0 < z \le t}$ D must remain and be inspected t mark not be damaged $x: length$	2.5
5	Glass crack	6.2.2 Non-conductive por 6.2.2 Non-conductive por y: chip width $y \le 1$ Notes • If the chipped area touch according to electrode te • If the product will be hear	tion: x: chip length $x \le 1/8 a$ $x \le 1/8 a$ t sealed by the customer, the alignment ince and internal crack: y: width	$\frac{z: chip thickness}{0 < z \le t}$ D must remain and be inspected t mark not be damaged x: length	2.5

Revision: 10-Aug-17

8

Document Number: 37798



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INSPECTION SPECIFICATION						
NO.	ITEM	CRITERION	AQL			
	Backlight elements	8.1 Illumination source flickers when lit	0.65			
08		8.2 Spots or scratched that appear when lit must be judged. Using OLED spot, lines and contamination standards	2.5			
		8.3 Backlight does not light or color wrong	0.65			
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination	2.5			
03		9.2 Bezel must comply with job specifications	0.65			
	PCB, COB	10.1 COB seal may not have pinholes larger than 0.2 mm or contamination	2.5			
		10.2 COB seal surface may not have pinholes through to the IC	2.5			
		10.3 The height of the COB should not exceed the height indicated in the assembly diagram	0.65			
		10.4 There may not be more than 2 mm of sealant outside the seal area on the PCB. And there should be no more than three places	2.5			
10		10.5 No oxidation or contamination PCB terminals	2.5			
		10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts	0.65			
		10.7 The jumper on the PCB should conform to the product characteristic chart	0.65			
		10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down	2.5			
	Soldering	11.1 No un-melted solder paste may be present on the PCB	2.5			
11		11.2 No cold solder joints, missing solder connections, oxidation or icicle	2.5			
11		11.3 No residue or solder balls on PCB	2.5			
		11.4 No short circuits in components on PCB	0.65			
		12.1 No oxidation, contamination, curves or, bends on interface pin (OLB) of TCP	2.5			
		12.2 No cracks on interface pin (OLB) of TCP	0.65			
	General appearance	12.3 No contamination, solder residue or solder balls on product	2.5			
		12.4 The IC on the TCP may not be damaged, circuits	2.5			
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever	2.5			
12		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color	2.5			
		12.7 Sealant on top of the ITO circuit has not hardened	2.5			
		12.8 Pin type must match type in specification sheet	0.65			
		12.9 OLED pin loose or missing pins	0.65			
		12.10 Product packaging must the same as specified on packaging specification sheet	0.65			
		12.11 Product dimension and structure must conform to product specification sheet	0.65			

Revision: 10-Aug-17



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OLED-016O002G-LPP5N00000

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CHECK ITEM	CLASSIFICATION	CRITERIA
No display	Major	
Missing line	Major	
Pixel short	Major	
Darker short	Major	
Wrong display	Major	
Un-uniform B/A x 100 % < 70 %	Major	
B/A x 100 % < 70 % A/C x 100 % < 70 %		A Normal B Dark pixel C Light pixel

Revision: 10-Aug-17

10 For technical questions, contact: <u>displays@vishay.com</u> Document Number: 37798

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PRECAUTIONS IN USE OF OLED MODULES

MODULES

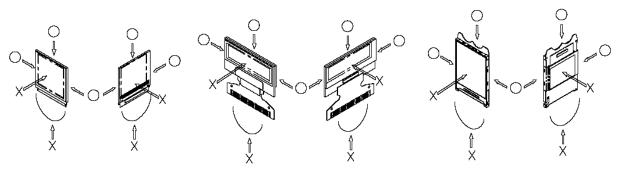
- 1. Avoid applying excessive shocks to module or making any alterations or modifications to it
- 2. Do not make extra holes on the printed circuit board, modify its shape or change the components of OLED display module
- 3. Do not disassemble the OLED display module
- 4. Do not operate it above the absolute maximum rating
- 5. Do not drop, bend or twist OLED display module
- 6. Soldering: only to the I/O terminals
- 7. Storage: please storage in anti-static electricity container and clean environment
- 8. It is pretty common to use "screen saver" to extend the lifetime and do not use fix information for long time in real application
- 9. Do not use fixed information in OLED panel for long time, that will extend "screen burn" effect time
- 10. Vishay has the right to change the passive components, including R2 and R3 adjust resistors. (Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier)
- 11. Vishay have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization, and the best product performance... etc, under the premise of not affecting the electrical characteristics and external dimensions, Vishay have the right to modify the version)

HANDLING PRECAUTIONS

- 1. Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position
- 2. If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance
- 3. If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections
- 4. The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module
- 5. When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape
- Scotch mending tape no. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy. Also, pay attention that the following liquid and solvent may spoil the polarizer:

- Water
- Ketone
- Aromatic solvents
- 6. Hold OLED display module very carefully when placing OLED display module into the system housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases



- 7. Do not apply stress to the LSI chips and the surrounding molded sections
- 8. Do not disassemble nor modify the OLED display module
- 9. Do not apply input signals while the logic power is off

Revision: 10-Aug-17



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- 10. Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity
 - Be sure to make human body grounding when handling OLED display modules
 - Be sure to ground tools to use or assembly such as soldering irons
 - To suppress generation of static electricity, avoid carrying out assembly work under dry environments
 - Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film
- 11. Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above no. 5
- 12. If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above

STORAGE PRECAUTIONS

- 1. When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps and, also, avoiding high temperature and high humidity environment or low temperature (less than 0 °C) environments. We recommend you to store these modules in the packaged state when they were shipped from Vishay. At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them
- 2. If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above

DESIGNING PRECAUTIONS

- 1. The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen
- To prevent occurrence of malfunctioning by noise, pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible
- 3. We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}) (recommend value: 0.5 A)
- 4. Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices
- 5. As for EMI, take necessary measures on the equipment side basically
- 6. When fastening the OLED display module, fasten the external plastic housing section
- 7. If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module
 - Connection (contact) to any other potential than the above may lead to rupture of the IC

PRECAUTIONS WHEN DISPOSING OF THE OLED DISPLAY MODULES

1. Request the qualified companies to handle industrial wastes when disposing of the OLED display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations

OTHER PRECAUTIONS

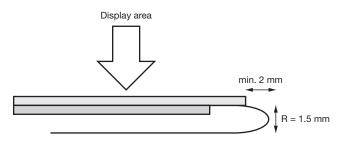
- 1. When an OLED display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur. Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module
- 2. To protect OLED display modules from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the OLED display modules
 - Pins and electrodes
 - Pattern layouts such as the TCP and FPC
- 3. With this OLED display module, the OLED driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this OLED driver is exposed to light, malfunctioning may occur
 - Design the product and installation method so that the OLED driver may be shielded from light in actual usage
 - Design the product and installation method so that the OLED driver may be shielded from light during the inspection processes

Revision: 10-Aug-17



Vishay

- 4. Although this OLED display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. It therefore is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design
- 5. We recommend you to construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise
- 6. Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier
- 7. Our company will has the right to upgrade and modify the product function
- 8. The limitation of FPC bending





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