

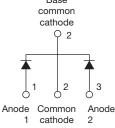
**Vishay Semiconductors** 

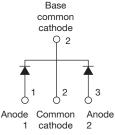
# Ultrafast Rectifier, 2 x 5 A FRED Pt<sup>®</sup>





**TO-262AA** 





VS-MURB1020CTPbF

VS-MURB1020CT-1PbF

PRODUCT SUMMARY						
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA					
I <sub>F(AV)</sub>	2 x 5 A					
V <sub>R</sub>	200 V					
V <sub>F</sub> at I <sub>F</sub>	0.87 V					
t <sub>rr</sub>	25 ns					
T <sub>J</sub> max.	175 °C					
Diode variation	Common cathode					

### **FEATURES**

- · Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- RoHS • Meets MSL level 1, per J-STD-020, LF maximum COMPLIANT peak of 260 °C HALOGEN FREE
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage		V <sub>RRM</sub>		200	V		
Average rectified forward current	per leg			5			
Average rectified forward current	total device	I <sub>F(AV)</sub>	Rated V <sub>R</sub> , T <sub>C</sub> = 149 °C	10	А		
Non-repetitive peak surge current per leg		I <sub>FSM</sub>		50	A		
Peak repetitive forward current per leg		I <sub>FM</sub>	Rated $V_R$ , square wave, 20 kHz, $T_C = 149 \ ^\circ C$	10			
Operating junction and storage tem	peratures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-		
Forward voltage		I <sub>F</sub> = 5 A, T <sub>J</sub> = 25 °C	-	0.99	1.08	v	
	V <sub>F</sub>	I <sub>F</sub> = 5 A, T <sub>J</sub> = 125 °C	-	0.87	0.99		
		I <sub>F</sub> = 10 A, T <sub>J</sub> = 25 °C	-	1.12	1.25		
		I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C	-	1.02	1.20		
	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	10		
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA	
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	8	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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## Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50$	$I_F$ = 1.0 A, $dI_F/dt$ = 50 A/µs, $V_R$ = 30 V		-	35		
	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>REC</sub> = 0.25 V		-	-	25		
		T <sub>J</sub> = 25 °C		-	24	-	ns	
		T <sub>J</sub> = 125 °C		-	35	-		
Deals receivers aurrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 5 A$	-	3.3	-	А	
Peak recovery current		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	-	5.0	-	A	
Reverse recovery charge C	0	T <sub>J</sub> = 25 °C		-	33	-	nC	
	$Q_{rr}$ $T_J = 125$	T <sub>J</sub> = 125 °C		-	76	-	110	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	5		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
W/oixht			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking davias		Case style TO-263AB (D <sup>2</sup> PAK)	MURB1020CT				
Marking device		Case style TO-262	MURB1020CT-1				



## **Vishay Semiconductors**

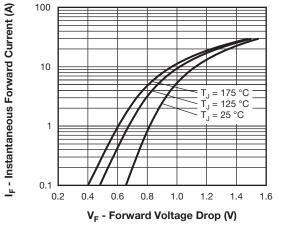


Fig. 1 - Typical Forward Voltage Drop Characteristics

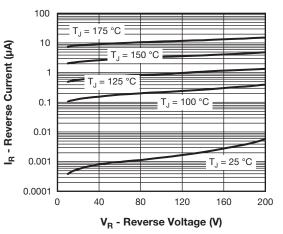


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

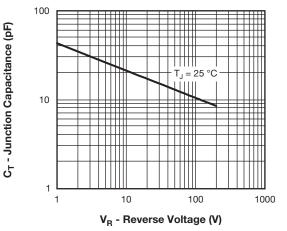


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

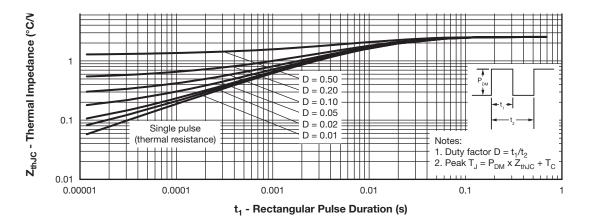


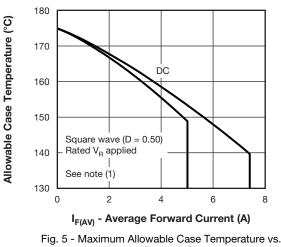
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

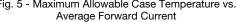
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## VS-MURB1020CTPbF, VS-MURB1020CT-1PbF

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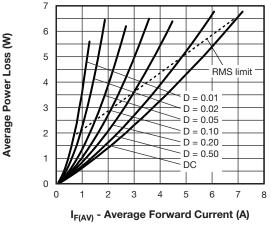
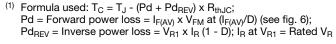
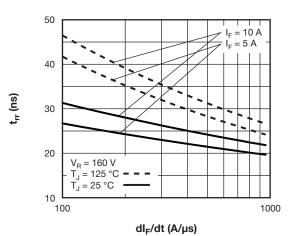


Fig. 6 - Forward Power Loss Characteristics

#### Note







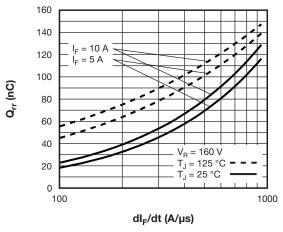


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



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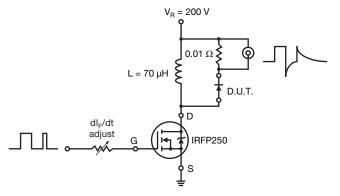


Fig. 9 - Reverse Recovery Parameter Test Circuit

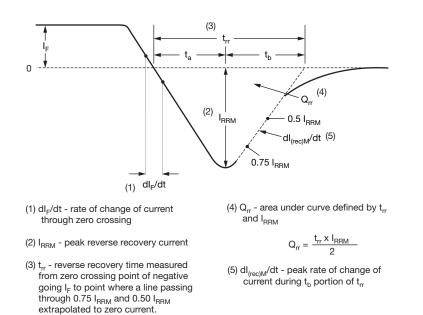
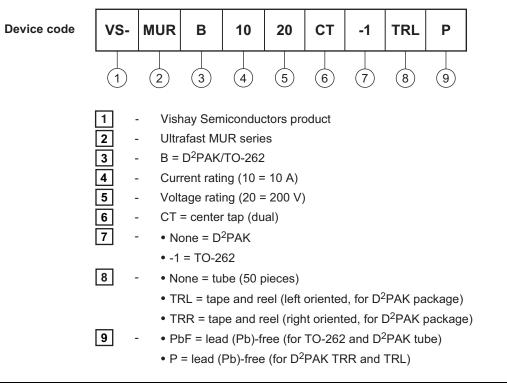


Fig. 10 - Reverse Recovery Waveform and Definitions



## **Vishay Semiconductors**

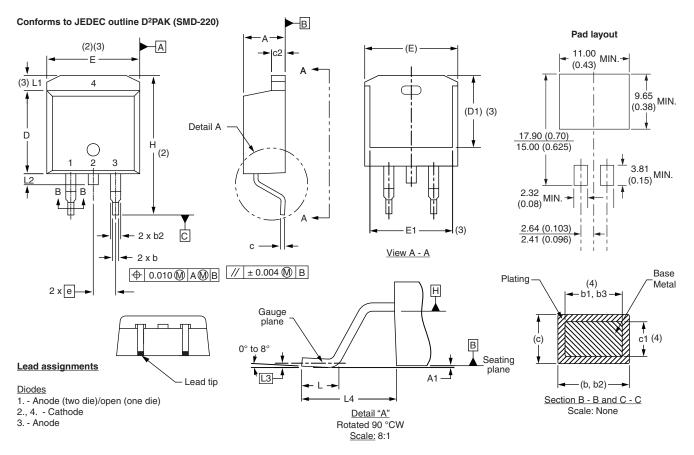
### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95014						
Part marking information	www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032					

Vishay Semiconductors

# D<sup>2</sup>PAK, TO-262



### DIMENSIONS - D<sup>2</sup>PAK in millimeters and inches

SHA

SYMBOL	MILLIMETERS		INCHES		NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MROL		NOTES		
STWIDUL	MIN.	MAX.	MIN.	MAX.	NULES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	SC 0.010		
L4	4.78	5.28	0.188	0.208	

INCHES

NOTES

MILLIMETERS

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

SYMBOL

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

Document Number: 95014 Revision: 31-Mar-09

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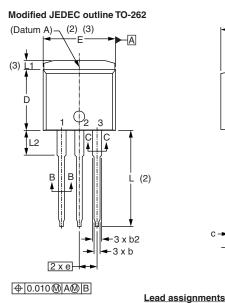
## **Outline Dimensions**

## Vishay Semiconductors

D<sup>2</sup>PAK, TO-262



#### DIMENSIONS - TO-262 in millimeters and inches

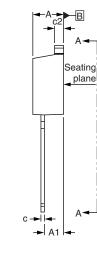


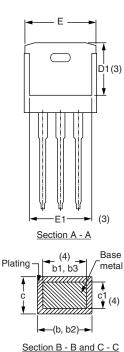
Lead tip

Diodes

3. - Anode

2., 4. - Cathode





Scale: None

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the

CYMDOL	MILLIMETERS		INC	NOTES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.10	0 BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

1. - Anode (two die)/open (one die)

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

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actual package outline

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