COMPLIANT

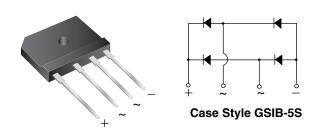
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Vishay General Semiconductor

Low V_F Single-Phase Single In-Line Bridge Rectifiers



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	25 A			
V_{RRM}	600 V			
I _{FSM}	550 A			
I _R	10 μA			
V_F at $I_F = 12.5$ A, $T_A = 125$ °C	0.76 V			
T _J max.	150 °C			
Package	GSIB-5S			
Circuit configuration	In-line			

FEATURES

- UL recognition file number E54214, Vol. 1
- Thin single in-line package
- Oxide planar chip junction
- Low forward voltage drop
- High surge current capability
- High case dielectric strength of 2500 V_{RMS}
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances and white-goods applications specially for telecom power supply, high efficiency desktop PC and server SMPS.

MECHANICAL DATA

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked on body

Mounting Torque: 10 cm-kg (8.8 in-lbs) maximum **Recommended Torque:** 5.7 cm-kg (5 in-lbs)

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	LVB2560	UNIT	
Maximum repetitive peak reverse voltage		V_{RRM}	600	V	
Maximum average forward rectified output current at	T _C = 105 °C	I _O ⁽¹⁾	25		
	T _A = 25 °C	I _O ⁽²⁾	3.6	A	
Non-repetitive peak forward surge current 8.3 ms single sine-wave, $T_J = 25\ ^{\circ}\text{C}$		I _{FSM}	550	Α	
Rating for fusing (t < 8.3 ms)	T _J = 25 °C	l ² t	1255	A ² s	
Operating junction and storage tempera	ture range	T _J , T _{STG}	-55 to +150	°C	

Notes

- (1) Unit case mounted on aluminum plate heatsink
- (2) Units mounted on PCB without heatsink

Revision: 09-Jul-2020 1 Document Number: 89393



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	1054	T _A = 25 °C	V _F ⁽¹⁾	0.89	0.92	- v
	I _F = 12.5 A	T _A = 125 °C		0.76	-	
Reverse current per diode	V _R = 600 V	T _A = 25 °C	I _R ⁽²⁾	0.2	10	μА
		T _A = 125 °C		140	=	
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	1.8	-	μs
Typical junction capacitance	4.0 V, 1 MHz		CJ	330	=	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER SYMBOL LVB2560				
Maximum thermal resistance	R ₀ JA (2)	25	°C/W	
	R ₀ JC ⁽¹⁾	1.0	C/VV	

Notes

(1) With heatsink

(2) Without heatsink, free air

EMC SURGE IMMUNITY TEST STANDARD (T _A = 25 °C, unless otherwise noted)						
STANDARD	ANDARD TEST TYPE TEST CONDITIONS		SYMBOL	CLASS	VALUE	
IEC 61000-4-5	Power supply coupling mode, line to line	1.2/50 μ s waveform, R = 2 Ω , T _A = 25 °C ⁽¹⁾	V_{PEAK}	-	6 kV maximum	

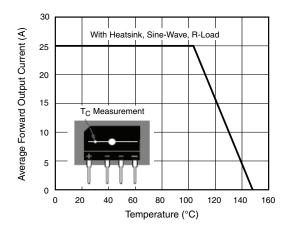
Note

(1) Immunity to IEC 61000-4-5 peak pulse voltage test, 1.2/50 μ s, 2 Ω , 5 times each of positive and negative polarity test

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
LVB2560-M3/45	7.1	45	20	Tube		

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



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Fig. 1 - Derating Curve Output Rectified Current

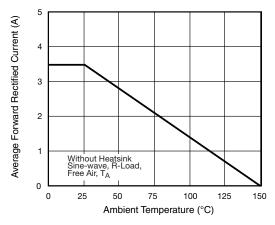


Fig. 2 - Forward Current Derating Curve

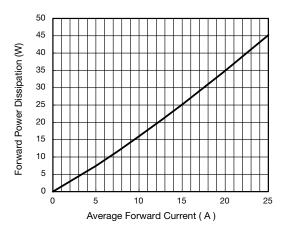


Fig. 3 - Forward Power Dissipation

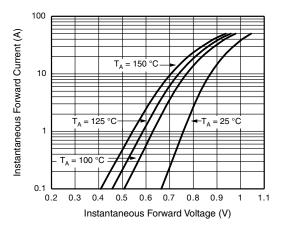


Fig. 4 - Typical Forward Characteristics Per Diode

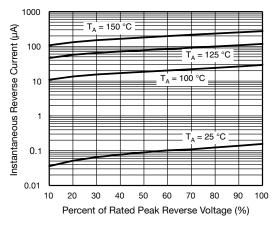


Fig. 5 - Typical Reverse Characteristics Per Diode

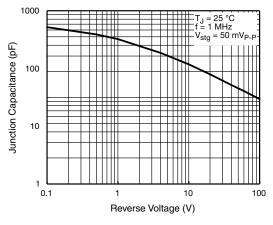
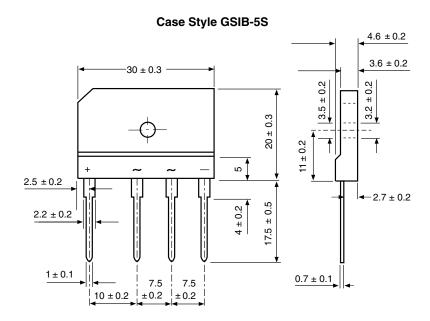


Fig. 6 - Typical Junction Capacitance Per Diode



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PACKAGE OUTLINE DIMENSIONS in millimeters



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