

## Vishay Semiconductors

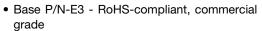
# **Small Signal Switching Diode, High Voltage**



#### **FEATURES**

- Silicon epitaxial planar diode
- Fast switching diode, especially suited for applications requiring high voltage capability

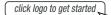






- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESIGN SUPPORT TOOLS**





#### **MECHANICAL DATA**

Case: SOD-123

Weight: approx. 10.3 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
GSD2004W	GSD2004W-E3-08 or GSD2004W-E3-18 GSD2004W-HE3-08 or GSD2004W-HE3-18	Single	B6	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Continuous reverse voltage		$V_{R}$	240	V
Repetitive peak reverse voltage		$V_{RRM}$	300	V
Forward current (continuous)		I <sub>F</sub>	225	mA
Repetitive peak forward current		I <sub>FRM</sub>	625	mA
Non-repetitive peak forward current	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	4	Α
Non-repetitive peak forward current	t <sub>p</sub> = 1 s	I <sub>FSM</sub>	1	Α
Power dissipation (1)		P <sub>tot</sub>	350	mW

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Typical thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	357	K/W
Junction temperature		T <sub>j</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C
Operating temperature range		T <sub>op</sub>	-55 to +150	°C

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature

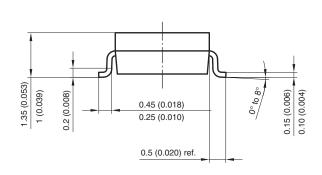


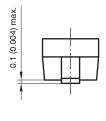
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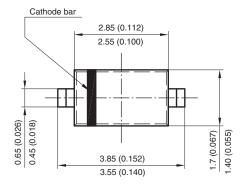
<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 100 μA	V <sub>(BR)</sub>	300			V
Laska as sumant	V <sub>R</sub> = 240 V	I <sub>R</sub>			100	nA
Leakage current	V <sub>R</sub> = 240 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>			100	μΑ
Campand valtage	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1	V
Forward voltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>		0.83	0.87	V
Diode capacitance	$V_F = V_R = 0$ , $f = 1$ MHz	C <sub>D</sub>			5	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $i_R = 3$ mA, $R_L = 100 \Omega$	t <sub>rr</sub>			50	ns

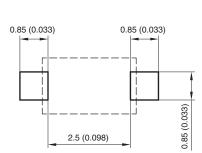
## PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





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