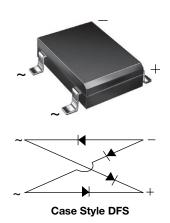


# EDF1AS, EDF1BS, EDF1CS, EDF1DS

Vishay General Semiconductor

# Miniature Glass Passivated Ultrafast Surface-Mount Bridge Rectifiers



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	1 A				
$V_{RRM}$	50 V, 100 V, 150 V, 200 V				
I <sub>FSM</sub>	50 A				
I <sub>R</sub>	5 μΑ				
$V_F$ at $I_F = 1.0 A$	1.05 V				
t <sub>rr</sub>	50 ns				
T <sub>J</sub> max.	150 °C				
Package	DFS				
Circuit configuration	Quad				

### **FEATURES**

- UL recognition, file number E54214
- Ideal for automated placement
- Glass passivated pellet chip junction
- Ultrafast reverse recovery time for high frequency
- · High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

### **MECHANICAL DATA**

Case: DFS

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked on body

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	EDF1AS	EDF1BS	EDF1CS	EDF1DS	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$	50 100 150 200				V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	106	140	V
Maximum DC blocking voltage	$V_{DC}$	<sub>DC</sub> 50 100 150 200			200	V
Maximum average forward output rectified current at T <sub>A</sub> = 40 °C (1)	I <sub>F(AV)</sub>	1.0				Α
Peak forward surge current single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50				Α
Rating for fusing (t < 8.3 ms)	I <sup>2</sup> t	10			A <sup>2</sup> s	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150				°C

### Note

(1) Pulse test: 300 ms pulse width, 1 % duty cycle

Revision: 09-Jul-2020 **1** Document Number: 88578 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS	SYMBOL	EDF1AS	EDF1BS	EDF1CS	EDF1DS	UNIT
Maximum instantaneous forward voltage drop per diode	1.0 A <sup>(1)</sup>	V <sub>F</sub>	1.05				V
Maximum DC reverse current at rated DC	T <sub>A</sub> = 25 °C	1_		5.	.0		μA
blocking voltage per diode	T <sub>A</sub> = 125 °C	IR	1.0				mA
Maximum reverse recovery time per diode	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	t <sub>rr</sub>	50			ns	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse test: 300 ms pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	EDF1AS	EDF1BS	EDF1CS	EDF1DS	UNIT
Typical thermal resistance (1)	$R_{\theta JA}$	38			°C/W	
	$R_{\theta JL}$		1:	2		7 5/1

### Note

<sup>(1)</sup> PCB mounted with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
EDF1DS-E3/45	0.406	45	50	Tube		
EDF1DS-E3/77	0.406	77	1500	13" diameter paper tape and reel		





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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

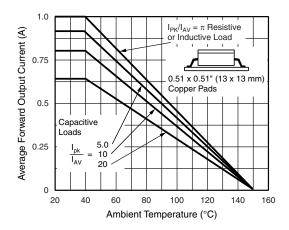


Fig. 1 - Derating Curves Output Rectified Current

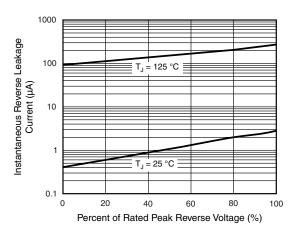


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

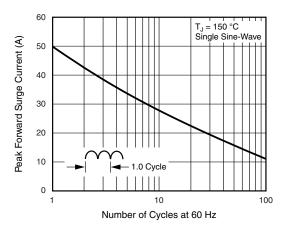


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

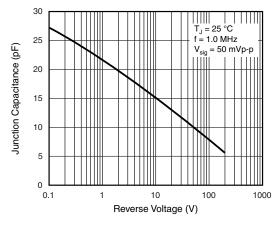


Fig. 5 - Typical Junction Capacitance Per Diode

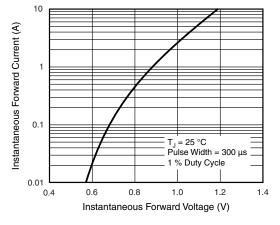


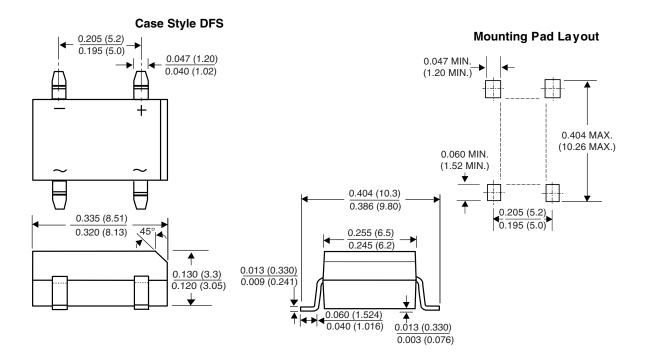
Fig. 3 - Typical Forward Characteristics Per Diode



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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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