

CMOS Digital Integrated Circuits Silicon Monolithic

TC7WZ14FK

1. Functional Description

· Triple Schmitt Inverter

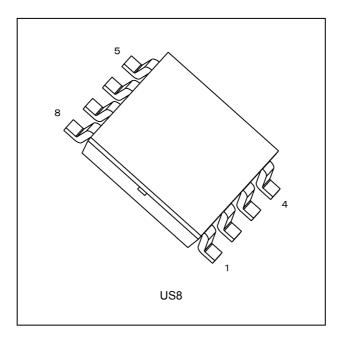
2. Features

- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 2)
- (3) High output current: ± 24 mA (min) at $V_{CC} = 3.0$ V
- (4) Super high speed operation: $t_{pd} = 3.7 \text{ ns}$ (typ.) at $V_{CC} = 5.0 \text{ V}$, $C_L = 50 \text{ pF}$
- (5) Operation voltage range: $V_{CC} = 1.65$ to 5.5 V
- (6) 5.5 V tolerant inputs
- (7) 5.5 V power down protection output
- (8) Matches the performance of TC74LCX series when operated at $3.3\ V\ V_{CC}$

Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

Note 2: For devices with the ordering part number ending in J(CT. T_{opr} = -40 to 85 °C for the other devices.

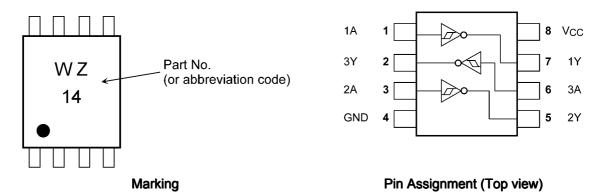
3. Packaging



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4. Marking and Pin Assignment



5. IEC Logic Symbol



6. Truth Table

А	Y
L	Н
Н	L

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 6.0	V
Input voltage	V _{IN}		-0.5 to 6.0	٧
DC output voltage	V _{OUT}	(Note 1)	-0.5 to 6.0	\
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-20	mA
Output diode current	I _{OK}	(Note 3)	-20	mA
DC output current	I _{OUT}		±50	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	P _D		200	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND



8. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.65 to 5.5	V
		(Note 1)	1.5 to 5.5	
Input voltage	V _{IN}		0 to 5.5	V
Output voltage	V _{OUT}	(Note 2)	0 to 5.5	V
		(Note 3)	0 to V _{CC}	
Operating temperature	T _{opr}	(Note 4)	-40 to 125	°C
		(Note 5)	-40 to 85	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V_{CC} or GND.

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High (H) or Low (L) state.

Note 4: For devices with the ordering part number ending in J(CT.

Note 5: For devices except those with the ordering part number ending in J(CT.



9. Electrical Characteristics

9.1. DC Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
Positive threshold voltage	V _P	_		1.65	0.6	1.0	1.4	V
				1.8	0.7	1.1	1.5	
				2.3	1.0	1.4	1.8	
				3.0	1.3	1.75	2.2	
				4.5	1.9	2.45	3.1	
				5.5	2.2	2.9	3.6	
Negative threshold voltage	V _N	_		1.65	0.2	0.5	0.8	V
				1.8	0.25	0.55	0.9	
				2.3	0.4	0.75	1.15	
				3.0	0.6	1.0	1.5	
				4.5	1.0	1.43	2.0	
				5.5	1.2	1.7	2.4	
Hysteresis voltage	V _H	_		1.65	0.1	0.48	0.9	V
				1.8	0.15	0.54	1.0	
				2.3	0.25	0.65	1.1	
				3.0	0.4	0.77	1.2	
				4.5	0.6	1.01	1.5	
				5.5	0.7	1.18	1.7	
High-level output voltage	V _{OH}	$V_{IN} = V_{N}$	I _{OH} = -100 μA	1.65	1.55	1.65	_	V
				2.3	2.2	2.3	_	
				3.0	2.9	3.0	_	
				4.5	4.4	4.5	_	
			I _{OH} = -4 mA	1.65	1.29	1.52	_	
			I _{OH} = -8 mA	2.3	1.9	2.14	_	
			I _{OH} = -16 mA	3.0	2.4	2.75	_	
			I _{OH} = -24 mA	3.0	2.3	2.62	_	
			I _{OH} = -32 mA	4.5	3.8	4.13	_	
Low-level output voltage	V _{OL}	V _{IN} = V _P	I _{OL} = 100 μA	1.65	_	0.0	0.1	V
				2.3	_	0.0	0.1	
				3.0	_	0.0	0.1	
				4.5	_	0.0	0.1	
			I _{OL} = 4 mA	1.65	_	0.08	0.24	
			I _{OL} = 8 mA	2.3	_	0.1	0.3	
			I _{OL} = 16 mA	3.0	_	0.16	0.4	
			I _{OL} = 24 mA	3.0	_	0.24	0.55	
			I _{OL} = 32 mA	4.5	_	0.25	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	•	0 to 5.5	_	_	±1	μΑ
Power-OFF leakage current	I _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0	_	_	1	μА
Quiescent supply current	I _{CC}	V _{IN} = 5.5 V or GND		1.65 to 5.5			1	μΑ



9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit	
Positive threshold voltage	V _P	_		1.65	0.6	1.4	V
				1.8	0.7	1.5	
				2.3	1.0	1.8	
				3.0	1.3	2.2	
				4.5	1.9	3.1	
				5.5	2.2	3.6	
Negative threshold voltage	V _N	_		1.65	0.2	0.8	V
				1.8	0.25	0.9	
				2.3	0.4	1.15	
				3.0	0.6	1.5	
				4.5	1.0	2.0	
				5.5	1.2	2.4	
Hysteresis voltage	V _H	_		1.65	0.1	0.9	V
				1.8	0.15	1.0	
				2.3	0.25	1.1	
				3.0	0.4	1.2	
				4.5	0.6	1.5	
				5.5	0.7	1.7	
High-level output voltage	V _{OH}	$V_{IN} = V_{N}$	I _{OH} = -100 μA	1.65	1.55	_	V
				2.3	2.2	_	
				3.0	2.9	_	
				4.5	4.4	_	
			I _{OH} = -4 mA	1.65	1.29	_	
			I _{OH} = -8 mA	2.3	1.9	_	
			I _{OH} = -16 mA	3.0	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	_	
			I _{OH} = -32 mA	4.5	3.8	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{P}$	I _{OL} = 100 μA	1.65	_	0.1	V
				2.3	_	0.1	
				3.0	_	0.1	
				4.5	_	0.1	
			I _{OL} = 4 mA	1.65	_	0.24	
			I _{OL} = 8 mA	2.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	1
			I _{OL} = 32 mA	4.5	_	0.55	1 l
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	•	0 to 5.5	_	±10	μА
Power-OFF leakage current	I _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0	_	10	μА
Quiescent supply current	Icc	V _{IN} = 5.5 V or GND		1.65 to 5.5	_	10	μА



9.3. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Positive threshold voltage	V _P	_		1.65	0.6	1.4	V
				1.8	0.7	1.5	
				2.3	1.0	1.8]
				3.0	1.3	2.2	
				4.5	1.9	3.1	
				5.5	2.2	3.6]
Negative threshold voltage	V _N	_		1.65	0.2	0.8	V
				1.8	0.25	0.9	
				2.3	0.4	1.15	
				3.0	0.6	1.5	
				4.5	1.0	2.0	
				5.5	1.2	2.4]
Hysteresis voltage	V _H	_		1.65	0.1	0.9	V
				1.8	0.15	1.0	
	İ			2.3	0.25	1.1]
				3.0	0.4	1.2	
				4.5	0.6	1.5]
				5.5	0.7	1.7]
High-level output voltage	V _{OH}	$V_{IN} = V_{N}$	I _{OH} = -100 μA	1.65	1.55	_	V
				2.3	2.2	_	
				3.0	2.9	_]
				4.5	4.4	_	
			I _{OH} = -4 mA	1.65	0.95	_]
			I _{OH} = -8 mA	2.3	1.7	_]
			I _{OH} = -16 mA	3.0	2.2	_	
			I _{OH} = -24 mA	3.0	2.0	_]
			I_{OH} = -32 mA	4.5	3.4	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{P}$	I _{OL} = 100 μA	1.65	_	0.1	V
				2.3		0.1	
				3.0	_	0.1]
				4.5	_	0.1	
			I _{OL} = 4 mA	1.65		0.7]
			I _{OL} = 8 mA	2.3		0.45]
			I _{OL} = 16 mA	3.0		0.6]
			I _{OL} = 24 mA	3.0		0.8]
			I _{OL} = 32 mA	4.5		0.8]
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	•	0 to 5.5	_	±20.0	μА
Power-OFF leakage current	I _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0		100	μА
Quiescent supply current	I _{CC}	V _{IN} = 5.5 V or GND		1.65 to 5.5		100	μА

Note: For devices with the ordering part number ending in J(CT.



9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	1.8 ± 0.15	15	2.0	9.1	15.0	ns
				2.5 ± 0.2		1.0	5.0	9.0	
				3.3 ± 0.3		1.0	3.7	6.3	
				5.0 ± 0.5		0.5	3.1	5.2	
			$R_L = 500 \Omega$	3.3 ± 0.3	50	1.5	4.4	7.2	ns
				5.0 ± 0.5		0.8	3.7	5.9	
Input capacitance	C _{IN}		_	0 to 5.5	_	_	3.0	_	pF
Power dissipation	C_{PD}	(Note 1)	_	3.3	_	1	33	_	pF
capacitance				5.5	_		43	_	

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ (per 1 gate)

9.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	1.8 ± 0.15	15	2.0	15.6	ns
			2.5 ± 0.2		1.0	9.5	
			3.3 ± 0.3		1.0	6.5	
			5.0 ± 0.5		0.5	5.5	
		R_L = 500 Ω	3.3 ± 0.3	50	1.5	7.5	ns
			5.0 ± 0.5		0.8	6.2	

9.6. AC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C, Input: t_r = t_f = 3 ns)

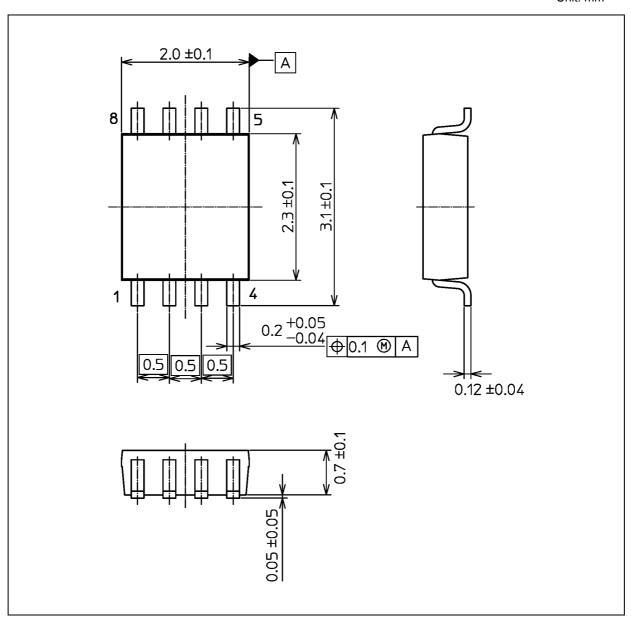
Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	1.8 ± 0.15	15	2.0	17.5	ns
			2.5 ± 0.2		1.0	10.5	
			3.3 ± 0.3		1.0	7.5	
			5.0 ± 0.5		0.5	6.5	
		R_L = 500 Ω	3.3 ± 0.3	50	1.5	8.5	ns
			5.0 ± 0.5		0.8	7.0	

Note: For devices with the ordering part number ending in J(CT.



Package Dimensions

Unit: mm



Weight: 0.01 g (typ.)

	Package Name(s)
JEDEC: SOT-765	
Nickname: US8	



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