(USV)

SSOP5-P-0.65A

Weight: 0.006 g (typ.)

5

4 OUT Y

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG126FU

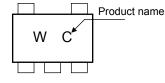
Bus Buffer with 3-STATE Output

Features

- High-level output current
- High-speed operation
- : ±8 mA (min) at V_{CC} = 3.0 V
 - : t_{pd} = 2.4 ns (typ.) at V_{CC} = 3.3 V, C_{L} = 15pF
- : V_{CC} = 0.9 to 3.6 V Operating voltage range
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.
- ESD performance
 - : Machine model ≥ ±200 V Human body model ≥ ±2000 V

Marking

Pin Assignment (top view)



G 1

IN A 2

GND 3

Absolute Maximum Ratings (Ta = 25°C) Symbol Unit Characteristic Rating -0.5 to 4.6 V Supply voltage Vcc -0.5 to 7.0 V DC input voltage VIN -0.5 to 4.6 (Note 1) V DC output voltage VOUT -0.5 to V_{CC} + 0.5 (Note 2) Input diode current IK -20 mΑ Output diode current lok -20 (Note 3) mΑ DC output current ±25 mΑ TOUT DC V_{CC}/ground current ICC ±50 mΑ Power dissipation 200 mW P_D Storage temperature -65 to 150 °C ́Т_{stg}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

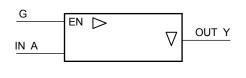
Note 3: V_{OUT} < GND

2005-04

Start of commercial production Note 2: High or Low State. Do not exceed I_{OUT} of absolute maximum ratings.

TOSHIBA

IEC Logic Symbol



G	А	Y
L	Х	Z
Н	L	L
Н	Н	Н

Truth Table

Operating Ranges

perating Ranges			~ ((77
Characteristic	Symbol	Rating		Unit
Supply voltage	V _{CC}	0.9 to 3.6	()	\geq v
Input voltage	V _{IN}	0 to 5.5	\sim	V
Output voltage	V _{OUT}	0 to 3.6 0 to V _{CC}	(Note 4) (Note 5)	v
		±8.0	Note 6)	(0)
		±4.0	(Note 7)	
	I _{OH} /I _{OL}	±3.0	(Note 8)	R
Output current		±1.7	(Note 9)	mA
		±0.3	(Note 10)	
		±0.02	(Note 11)	\mathcal{V}
Operating temperature	T _{opr}	-40 to 85		°C
Input rise and fall time	dt/dv	0 to 10	(Note 12)	ns/V
Note 4: $V_{CC} = 0V$))	\bigvee	
Note 5: High or Low state.	$(C \wedge$			
Note 6: $V_{CC} = 3.0$ to 3.6 V		$\langle \rangle$		
Note 7: $V_{CC} = 2.3$ to 2.7 V	775			
Note 8: V _{CC} = 1.65 to 1.95 V	$\langle O \rangle$	$\overline{\Omega}$		
Note 9: V _{CC} = 1.4 to 1.6 V		$\langle \langle \vee \rangle \rangle$		
Note 10: $V_{CC} = 1.1$ to 1.3 V				
	/			

Note 11: $V_{CC} = 0.9 V$

Note 12: $V_{\text{IN}} = 0.8$ to 2.0 V, $V_{\text{CC}} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristic		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Unit
		Symbol	1651	Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
					0.9	V _{CC}	_ <	X	V _{CC}	_	
	High level				1.1 to 1.3	$V_{CC} \times 0.7$			V _{CC} ≫0.7		
		VIH		_	1.4 to 1.6	V _{CC} × 0.65	6		V _{CC} × 0.65	_	
					1.65 to 1.95	V _{CC} × 0.65		2	V _{CC} × 0.65	_	
					2.3 to 2.7	1.7)r		1.7	—	
Input voltage					3.0 to 3.6	2.0	\geq	—	2.0	_	v
input voltage					0.9	Ϋ́́		GND	9£ _	GND	v
					1.1 to 1.3		_	V _{CC} × 0.3		$V_{CC} \times 0.3$	
	Low level	V _{IL}		_	1.4 to 1.6	۷_	_	Vcc × 0.35	L)	V _{CC} × 0.35	
				<	1.65 to 1.95	—	-((V _{CC} × 0.35	 — 	V _{CC} × 0.35	
				\bigcirc	2.3 to 2.7	—	$\overline{\mathcal{T}}$	0.7	—	0.7	
					3.0 to 3.6	_	XÐ)	0.8	—	0.8	
				I _{OH} =-0.02 mA	0.9	0.75)	_	0.75	_	
	High level	Vон	VIN = VIH	$1_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75))_	_	V _{CC} × 0.75	_	
				I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	—	_	V _{CC} × 0.75	_	
				Юн = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	—	—	V _{CC} -0.45	_	
			(/)	I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0		
Output voltage		\sum		I _{OH} = -8.0 mA	3.0 to 3.6	2.48	—	—	2.48	_	V
				I _{OL} = 0.02 mA	0.9	—	_	0.1	—	0.1	
			<	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
	Low level	Vol	V _{IN} = V _{IL} or VIH	I _{OL} = 1.7 mA	1.4 to 1.6	—	_	V _{CC} × 0.25	—	V _{CC} × 0.25	
(I _{OL} = 3.0 mA	1.65 to 1.95	—	—	0.45	—	0.45		
	\bigcirc			IOL = 4.0 mA	2.3 to 2.7	—	_	0.4	—	0.4	
			(())	l _{OL} = 8.0 mA	3.0 to 3.6	—	_	0.4	—	0.4	
Input leakage current		IN	V _{IN} = 0 to 5.5V		0 to 3.6	—	_	±0.1	—	±1.0	μA
3-state output off-state current		I _{OZ}	V _{IN} = V _{IH} 0 V _{OUT} = 0 t		0.9 to 3.6	_		1.0	—	10.0	μA
Power off leakage current		IOFF	V _{IN} = 0 to 4 V _{OUT} = 0 t		0.0			1.0	_	10.0	μA
Quiescent supply current		ICC	$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0	_	10.0	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristic Symbol		Test Condition		-	Ta = 25°C	;	$Ta = -40$ to $85^{\circ}C$		Linit
Characteristic	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	- Unit
			0.9	_	15.3	—	—	_	
			1.1 to 1.3	—	8.3	18.4	1.0	34.2	
		C _L = 10 pF,	1.4 to 1.6	—	5.0	8.5	1.0	10.0	
		$R_{L}^{-} = 1 M\Omega^{-}$	1.65 to 1.95	_	4.0	6.2	1.0	6.7	
			2.3 to 2.7	_	2.6	3.9	1.0	4.4	
			3.0 to 3.6		2.1	31	1.0	3.7	
			0.9	_	17.7	92	—	_	
			1.1 to 1.3	_((9.6	21.5	1.0	37.2	
Dranspotian dalau tima	t _{pLH}	C _L = 15 pF,	1.4 to 1.6		5.6	9.3	1.0	11.2	
Propagation delay time	t _{pHL}	$R_{L}^{T} = 1 M\Omega^{T}$	1.65 to 1.95	4(4.5	6.9	1.0	7.1	ns
			2.3 to 2.7	$\left(-\right) $	2.9	4.4	1.0	5.0	
			3.0 to 3.6	$\langle A \rangle$	2.4	3.4) 1,0	3.9	
			0.9	92	29.0	Q T	ĽA)	_	
			1.1 to 1.3	_	14.5	29.6	1.0	56.0	
		C _L = 30 pF, <	1.4 to 1.6	_	8.2	13.1	1.0	15.9	
		$R_L = 1 M\Omega$	1.65 to 1.95	—	6.0	9.2	1.0	9.6	
			2.3 to 2.7	_	4.0	5.7	1.0	6.1	
			3.0 to 3.6		3.3	4.4	1.0	4.8	
		$\begin{array}{l} C_{L} = 10 \ pF, \\ R_{L} = 100 \ k\Omega \end{array}$	0.9		22.7			_	
			1.1 to 1.3		10.9	18.7	1.0	29.8	
	(($\langle \uparrow \rangle$	1.4 to 1.6		5.9	8.7	1.0	9.8	
		C _L = 10 pF, R _L = 5 kΩ	1.65 to 1.95	<u> </u>	4.5	6.3	1.0	6.8	
	(7/5)		2.3 to 2.7	_	3.1	4.2	1.0	4.5	
	\sim		3.0 to 3.6	_	2.4	3.2	1.0	3.5	
		C _L = 15 pF, R _L = 100 kΩ	0.9	_	25.3	_		_	
			1.1 to 1.3	_	11.9	20.7	1.0	34.7	
Output enable time	t _{pZL}		1.4 to 1.6		6.5	9.5	1.0	11.1	ns
	t _{pZH}	$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	4.9	6.8	1.0	7.2	
	2		2.3 to 2.7	_	3.3	4.4	1.0	4.8	
$\langle (()) \rangle$			3.0 to 3.6	_	2.5	3.4	1.0	3.7	
		C _L = 30 pF, R _L = 100 kΩ	0.9		37.7	_		_	
	\bigwedge		1.1 to 1.3	_	17.1	30.7	1.0	50.5	
\sim	\searrow		1.4 to 1.6		8.8	13.1	1.0	15.1	
		$C_L = 30 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95	_	6.6	9.2	1.0	9.9	
		1.1 - 0 K75	2.3 to 2.7		4.1	5.4	1.0	5.8	
			3.0 to 3.6		3.1	4.1	1.0	4.5	

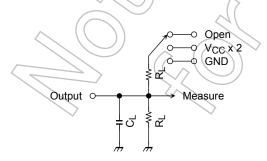
Characteristic	Symbol Test Cor		Fest Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$	
Characteristic	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$\begin{array}{l} C_L = 10 \text{ pF}, \\ R_L = 100 \text{ k}\Omega \end{array}$	0.9	_	117.6	_	_	_	
			1.1 to 1.3	_	9.2	16.0	1.0	22.4	
			1.4 to 1.6		7.1	1. 95	1.0	10.4	
		$C_L = 10 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.65 to 1.95		6.7	8.3	1.0	9.0	
		-	2.3 to 2.7		6.2	7.3	1.0	8.8	
			3.0 to 3.6		5.8	6.9	1.0	7.6	
	t _{pLZ} t _{pHZ}	C _L = 15 pF, R _L = 100 kΩ	0.9		139.2	Ľ	—	—	
		$C_L = 15 \text{ pF},$ $R_L = 5 \text{ k}\Omega$	1.1 to 1.3		10.0	16.9	1.0	25.1	ns
Output disable time			1.4 to 1.6		7.8	9.8	1.0	11.3	
			1.65 to 1.95	ł	7.4	9.2	(1.0	10.6	
			2.3 to 2.7		7.0	8.2	1.0	10.3	
			3.0 to 3.6	\mathcal{A}	6.8	7.7	1.0	9.5	
		C _L = 30 pF, R _L = 100 kΩ	0.9		230.8		GO/	—	
		$C_L = 30 \text{ pE}, \\ R_L = 5 \text{ k}\Omega$	1.1 to 1.3		14.0	20.8	1.0	31.9	
			1.4 to 1.6	_	12.2	13.5	1.0	14.9	
			1.65 to 1.95	1	11.5) 13.0	1.0	13.9	
			2.3 to 2.7	(-	11.3	12.2	1.0	13.5	
			3.0 to 3.6	-	10.9	11.8	1.0	12.9	
Input capacitance	C _{IN}	(())	3.6	\swarrow	3	_	_	_	pF
Power dissipation capacitance	CPD	(Note 13)	0.9 to 3.6	-	8		_		pF

Note 13:C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

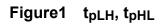
Average operating current can be obtained by the equation

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

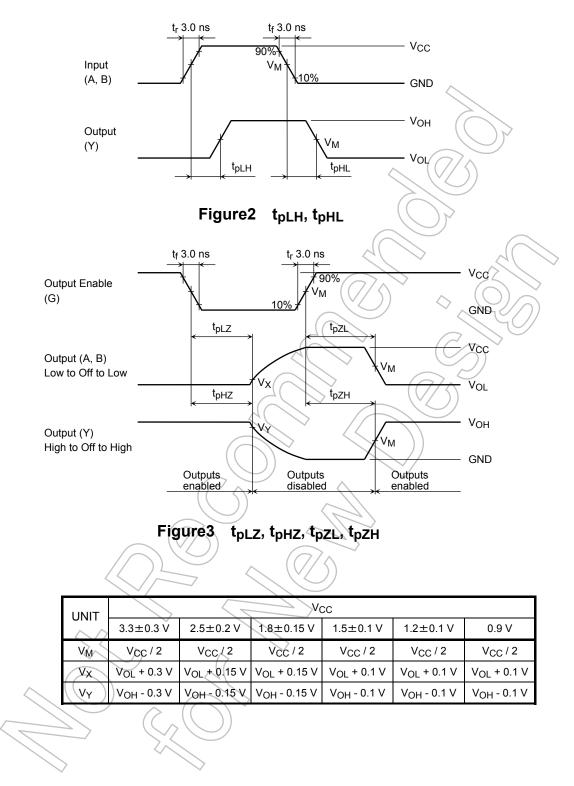
AC Characteristics Measurement Circuit



Characteristics	Switch				
t _{pLH} , t _{pHL}	Open				
t _{pLZ} , t _{pZL}	V _{CC} x 2				
t _{pHZ} , t _{pZH}	GND				

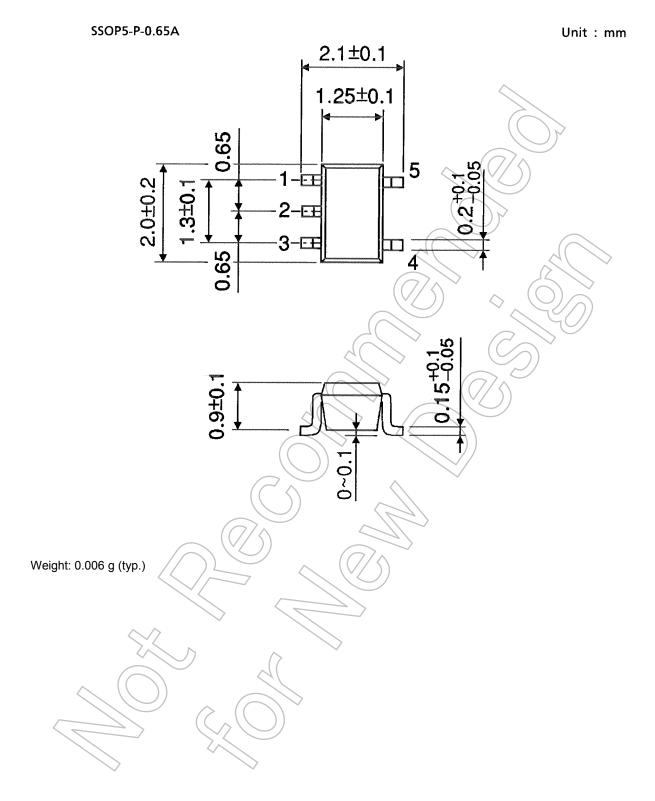


AC Characteristics Measurement Circuit



TOSHIBA

Package Dimensions



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