(US6

SSOP6-P-0.65A

Weight: 0.0068 g (typ.)

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PAU04FU

Dual Inverter (unbuffer) with 3.6 V Tolerant Input

Features

- Low voltage operation: V_{CC} = 1.8 to 3.6 V
- Quiescent supply current: $I_{CC} < 20\mu A (max)$
- $(V_{CC} = 3.6 \text{ V}, \text{ Ta} = -40 \text{ to } 85^{\circ}\text{C})$ High-speed operation: t_{pd} = 3.5 ns (max) (V_{CC} = 3.0 to 3.6 V)
- t_{pd} = 4.2 ns (max) (V_{CC} = 2.3 to 2.7 V) t_{pd} = 8.4 ns (max) (V_{CC} = 1.8 V)
- High-output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA} (min) (V_{CC} = 3.0 \text{ V})$ $I_{OH}/I_{OL} = \pm 18 \text{ mA} (\text{min}) (V_{CC} = 2.3 \text{ V})$
 - $I_{OH}/I_{OL} = \pm 6 \text{ mA} (\text{min}) (V_{CC} = 1.8 \text{ V})$
- Latch-up performance: ±300 mA
- ESD Performance: ±200 V (JEITA)



3.6 V tolerant function for input and power down protection are provided.

Abs

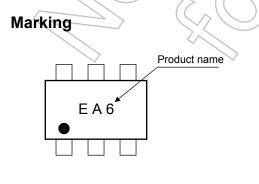
bsolute Maximum Ratings (Ta = 25°C)						
Characteristics	Symbol	Rating	Unit	5		
Supply voltage	V _{CC}	-0.5 to 4.6	\sim			
DC input voltage	VIN	-0.5 to 4.6	V			
DC output voltage	Vout	-0.5 to V _{CC} + 0.5(Note 1)	$\langle v \rangle$			
Input diode current	IIK /	-50	mA			
Output diode current	IOK .	±50 (Note 2)	mA			
DC output current	(IOUT (±50	mA			
DC V _{CC} /ground current	Icc	±100	mA			
Power dissipation	RD	200	mW			
Storage temperature	T _{sta}	-65 to 150	°C			

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

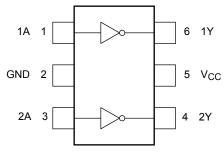
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: High or low state. VOUT absolute maximum rating must be observed.

Note 2: VOUT < GND, VOUT > VCC



Pin Assignment (top view)

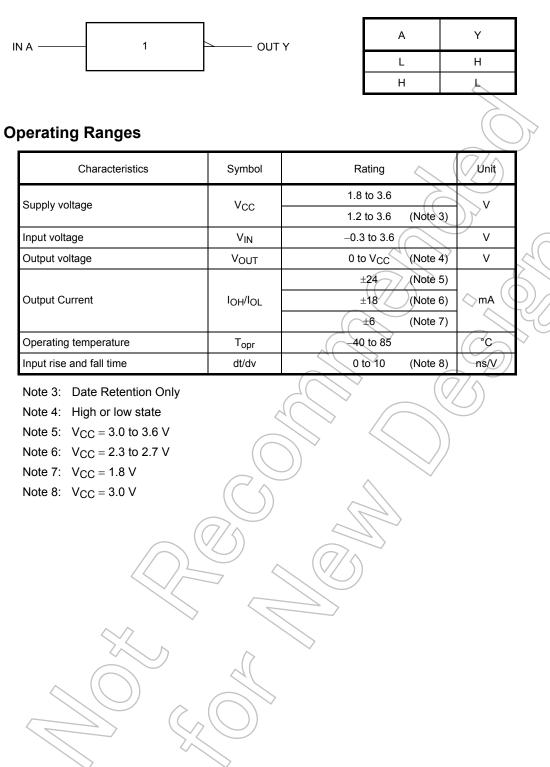


Start of commercial production 2001-10

TOSHIBA

IEC Logic Symbol

Truth Table



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characte	eristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
"H" level	Maria		1.8	V _{CC} × 0.85			
Innutveltage		VIH	_	2.3 to 3.6	Vcc × 0.8	_	V
Input voltage	"L" level	Ma		1.8	_	V _{CC} × 0.15	v
	Lievei	VIL		2.3 to 3.6	_	$V_{CC} \times 0.2$	
		I _{OH} = -100 µА	1.8 to 3.6	V _{CC} - 0.2	_		
		V _{OH}	I _{OH} = −6 mA	1.8	(14	\rightarrow	
	"11 "		$I_{OH} = -12 \text{ mA}$	2.3	1.8	> -	-
	"H" level		V _{IN} = V _{IL}	2.3	25) —	
			1 _{OH} = -12 mA	2.7	2.2	_	
Output voltage "L" level		I _{OH} = -18 mA	3.0	2.4	—		
		1 _{OH} = -24 mA	3.0	2.2		V	
		I _{OL} = 100 μA	1.8 to 3.6		0.2		
	Vol	I _{OH} = 6 mA	1.8		0.3		
		$I_{OL} = 12 \text{ mA}$	2.3		0.4		
		$V_{IN} = V_{IH}$ $I_{OL} = 18 \text{ mA}$	2.3		0.6		
		$I_{OL} = 12 \text{ mÅ}$	2.7		0.4		
		$I_{OL} = 18 \text{ mA}$ $I_{OL} = 24 \text{ mA}$	3.0		0.4		
Input leakage currer	nt (7/<1 _{IN}	V _{IN} = 0~3.6 V	2.7 to 3.6		±5.0	μA
Quiescent supply current		$V_{IN} = V_{CC}$ or GND	2.7 to 3.6		20.0	. na	
		$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 V$	2.7 to 3.6	-	±20.0	μA	

AC Characteristics (Ta = -40 to 85°C, input $t_r = t_f = 2.0 \text{ ns}$, $C_L = 30 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH} t _{pHL}	(Fig.1, 2)	1.8	1.0	8.4	
			$\textbf{2.5}\pm\textbf{0.2}$	0.8	4.2	ns
			3.3 ± 0.3	0.6	3.5	

For $C_L = pF$,add approximately 300 ps to the Ac maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}		1.8 2.5	0.25 0.6	ns
Quiet output maximum dynamic VOL	V _{OLV}	$\begin{split} & V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V} & (\text{Note 9}) \\ & V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V} & (\text{Note 9}) \\ & V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V} & (\text{Note 9}) \\ \end{split}$	3.3 1.8 2.5	0.8 -0.25 -0.6	ns
		V _{IN} = 3,3 V, V _{IL} = 0 V (Note 9) V _{IN} = 1,8 V, V _{IL} = 0 V (Note 9)	3.3 1.8	-0.8 1.5	
Quiet output maximum dynamic VOH	V _{OHP}	V _{IN} = 2.5 V, V _{IL} = 0 V (Note 9)	2.5	1.9	ns
	<	V _{IN} = 3.3 V, V _{IL} = 0 V (Note 9)	3.3	2.2	1

Note 9: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition			Typ.	Unit
	7/			V _{CC} (V)	,,	
Input capacitance	CIN			1.8, 2.5, 3.3	4	pF
Power dissipation capacitance	CPD	f _{IN} = 10 MHz	(Note 10)	1.8, 2.5, 3.3	7	pF

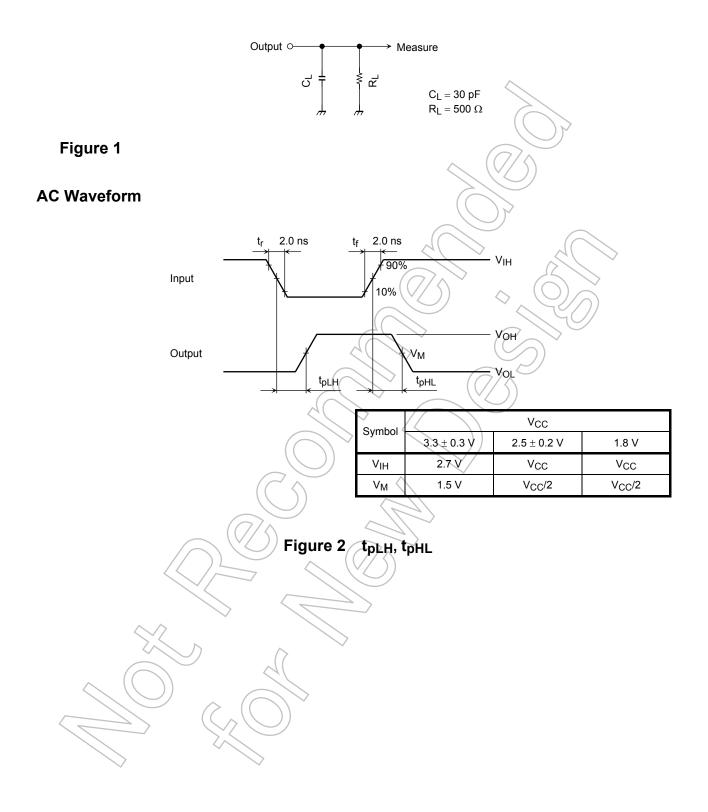
Note 10: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

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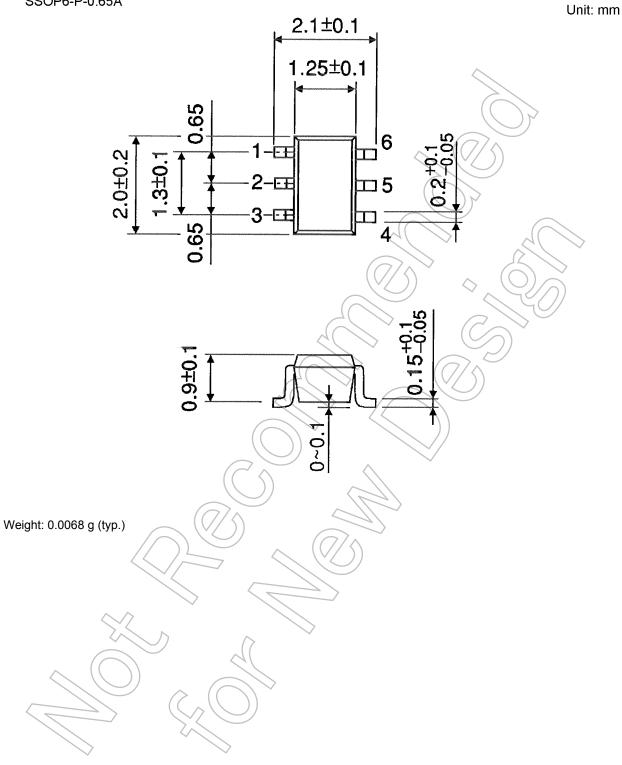
Test Circuit



TOSHIBA

Package Dimensions

SSOP6-P-0.65A



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