TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PA17FU

#### **Dual Schmitt Buffer**

#### **Features**

- Operating voltage range: V<sub>CC</sub> = 1.8 to 3.6 V
- High-speed operation:  $t_{pd}$  = 4.0 ns (max) at  $V_{CC}$  = 3.0 to 3.6 V

 $t_{pd}$  = 4.3 ns (max) at  $V_{CC}$  = 2.3 to 2.7 V

 $t_{pd}$  = 8.6 ns (max) at  $V_{CC}$  = 1.8 V

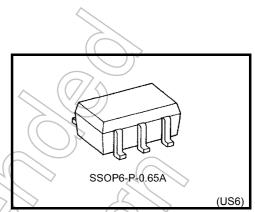
High-level output current:

 $I_{OH}/I_{OL}$  = ±24 mA (min) at  $V_{CC}$  = 3.0 V

 $I_{OH}/I_{OL}$  = ±18 mA (min) at  $V_{CC}$  = 2.3 V

 $I_{OH}/I_{OL} = \pm 6$  mA (min) at  $V_{CC} = 1.8$  V

- 3.6-V tolerant inputs.
- 3.6-V power down protection outputs

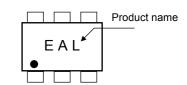


Weight: 0.0068 g (typ.)

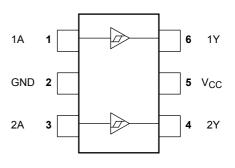
## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 4.6	7 \h
DC output voltage	(7/	-0.5 to 4.6 (Note 1)	) v
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	_/ V
Input diode current		-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	OUT	±50	mA
Power dissipation	PD	200	mW
DC V <sub>CC</sub> /ground current	Icc	±100	mA
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

#### Marking



#### Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2002-12

2014-03-01

## **IEC Logic Symbol**



#### **Truth Table**

А	Υ
L	L
Н	Н

## **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	V <sub>CC</sub>	1.8 to 3.6
очррту чопаде	VCC .	1.2 to 3.6 (Note 4)
Input voltage	V <sub>IN</sub>	-0.3 to 3.6 V
Output voltage	V <sub>OUT</sub>	0 to 3.6 (Note 5)
Output voitage		0 to V <sub>CC</sub> (Note 6)
		±24 (Note 7)
Output Current	I <sub>OH</sub> /I <sub>OL</sub>	±18 (Note 8) mA
		±6 (Note 9)
Operating temperature	T <sub>opr</sub>	-40 to 85 °€

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or Low state

Note 7:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.8 \text{ V}$ 

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#### **Electrical Characteristics**

### DC Characteristics (2.7 V < V<sub>CC</sub> ≤ 3.6 V)

Characteristics		Symbol Test Condition		Test Condition		Ta = 40 to 85°C		Unit
Characteris	lics	Symbol	1651	Soridition	V <sub>CC</sub> (V)	Min	Max	Offic
High level Threshold Voltage		V <sub>P</sub>		3.6	_	2.2	V	
		VΡ		_	3.0	_	2.0	V
Threshold Voltage	Low level	V <sub>N</sub>			3.6	0.8	_	V
	LOW level	۷N		_	3,0	0.7	_	V
Hysteresis Voltage		$V_{H}$	-		3.6	0.3	1.2	V
Trysteresis voltage		۷П			3.0	0.3	1.2	V
Hi	High level			I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_	
		V <sub>OH</sub>	$V_{IN} = V_{IH}$	I <sub>OH</sub> = -12 mA	2.7	22	$\rightarrow$	V
				1 <sub>OH</sub> = -18 mA	3.0	2.4	> -	
Output Voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
		Low level V <sub>OL</sub>		I <sub>OL</sub> = 100 μA	2.7 to 3.6	4	0.2	
	I ow level		$V_{IN} = V_{IL}$	I <sub>OL</sub> = 12 mA	2.7	>_	0.4	
	Low icvei			I <sub>OL</sub> = 18 mA	3.0	—	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input Leakage Current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		2.7 to 3.6	_	±5.0	μΑ
Power-off Leakage Cur	rent	loff	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	_	10.0	μΑ
Quiescent Supply Current		loo	$V_{IN} = V_{CC}$ or GND		2.7 to 3.6	_	20.0	
		ico	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub> ) ≤ 3.6 V		2.7 to 3.6	_	±20.0	μΑ
Increase in I <sub>CC</sub> per Inpu	ut	Alcc	V <sub>IH</sub> = V <sub>CC</sub> − 0.6 (	<u> </u>	2.7 to 3.6		750	

# DC Characteristics (2.3 V ≤ V<sub>CC</sub> ≤ 2.7 V)

Characteristics		Symbol Test Condition			Ta = 40	to 85°C	Unit				
Charac	teristics	Symbol	Test C	oridition	V <sub>CC</sub> (V)	Min	Max	Offic			
Threshold Voltage	High level			_	2.3	_	1.8	٧			
Threshold Voltage	Low level	$V_N$		_	2.3	0.5		V			
Hysteresis Voltage		VH		_	2.3	0.3	1.0	٧			
High level			$I_{OH} = -100 \mu A$	2.3 to 2.7	V <sub>CC</sub> - 0.2	_					
	High level	VoH	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	V			
				I <sub>OH</sub> = -12 mA	2.3	1.8	_				
Output Voltage	Output Voltage			I <sub>OH</sub> = -18 mA	2.3	1.7	_				
				$I_{OL} = 100 \mu A$	2.3 to 2.7	_	0.2				
	Low level	$V_{OL}$	$V_{OL}$	$V_{OL}$	V <sub>OL</sub> V	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				I <sub>OL</sub> = 18 mA	2.3	_	0.6				
Input Leakage Current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μΑ			
Power-off Leakage	Current	l <sub>OFF</sub>	$V_{IN}$ , $V_{OUT} = 0$ to 3	3.6 V	0	_	10.0	μΑ			
Quiescent Supply Current			V <sub>IN</sub> = V <sub>CC</sub> or GND		2.3 to 2.7	_	20.0	μА			
Quiescent Supply (	Juli elit	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub> ) ≤ 3.6 V		2.3 to 2.7		±20.0	μΑ			

## DC Characteristics (1.8 V $\leq$ V<sub>CC</sub> < 2.3 V)

Characteristics		Cumbal	Test Condition			Ta = 40 to 85°C		Unit
Charac	tenstics	Symbol	-,		V <sub>CC</sub> (V)	Min	Max	Unit
Threshold Voltage	High level	$V_{P}$	-	_	1.8	_	1.4	V
Threshold Voltage	Low level	$V_N$	-	_	1.8	0.25		V
Hysteresis Voltage		V <sub>H</sub>	_		1.8	0.2	0.95	V
High level	High level	V <sub>OH</sub>	$V_{IN} = V_{IH}$	I <sub>OH</sub> = -100 μA	1.8	V <sub>CC</sub> - 0.2	_	
Output Voltage				$I_{OH} = -6 \text{ mA}$	1,8	1.4		V
	Low level	V	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 100 μA	1.8	_	0.2	
	Low level V <sub>OL</sub>	VOL		I <sub>OL</sub> = 6 mA	1.8	_	0.3	
Input Leakage Curi	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.8	_	±5.0	μΑ
Power-off Leakage	Current	l <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	$\mathcal{I}$	10.0	μΑ
Quiescent Supply Current		laa	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.8	<i>///</i>	20.0	μА
Quicacent Supply (	Janeni	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OUT</sub> ) ≤ 3.6 V		1.8	J-/	±20.0	μΛ

# AC Characteristics (Input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500~\Omega$ )

Characteristics	Cymhal	Test Condition		Ta = 40 t	to 85°C	Unit
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Max	Unit
	<b>.</b> <		1.8	1.0	8.6	
Propagation delay time		(Figure 1 and 2)	2.5 ± 0.2	0.8	4.3	ns
	t <sub>pHL</sub>		$3.3 \pm 0.3$	0.6	4.0	

For  $C_L = 50$  pF, add approximately 300 ps to the AC maximum specification.

### Dynamic Switching Characteristics (Input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Cumbal	Toot Co		Ta = 25°C	Unit	
Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур	Utill
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	1.8	0.25	
Quiet Output Maximum Dynamic Vo	L V <sub>OLP</sub>	$V_{IN} = 2.5 \ V, \ V_{IL} = 0 \ V$	(Note 10)	2.5	0.6	ns
		$V_{IN}=3.3\;V,\;V_{IL}=0\;V$	(Note 10)	3.3	0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	1.8	-0.25	
Quiet Output Minimum Dynamic VO	V <sub>OLV</sub>	$V_{IN} = 2.5 \ V, \ V_{IL} = 0 \ V$	(Note 10)	2.5	-0.6	ns
		$V_{IN}=3.3\;V,\;V_{IL}=0\;V$	(Note 10)	3.3	-0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	1.8	1.5	
Quiet Output Minimum Dynamic VO	H V <sub>OLP</sub>	V <sub>IN</sub> = 2.5 V, V <sub>IL</sub> = 0 V	(Note 10)	2.5	1.9	ns
		V <sub>IN</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note 10)	3.3	2.2	

Note 10: Characteristics guaranteed by design.

## **Capacitive Characteristics**

Characteristics	Symbol	Test Condition		Ta = 25°C	Unit
Characteristics	Symbol	rest condition	Vcc (V)	Тур	Offic
Input Capacitance	C <sub>IN</sub>		1.8, 2.5, 3.3	4	pF
Power Dissipation Capacitance	$C_{PD}$	f <sub>IN</sub> = 10 MHz (No	te 11) 1.8, 2.5, 3.3	27	pF

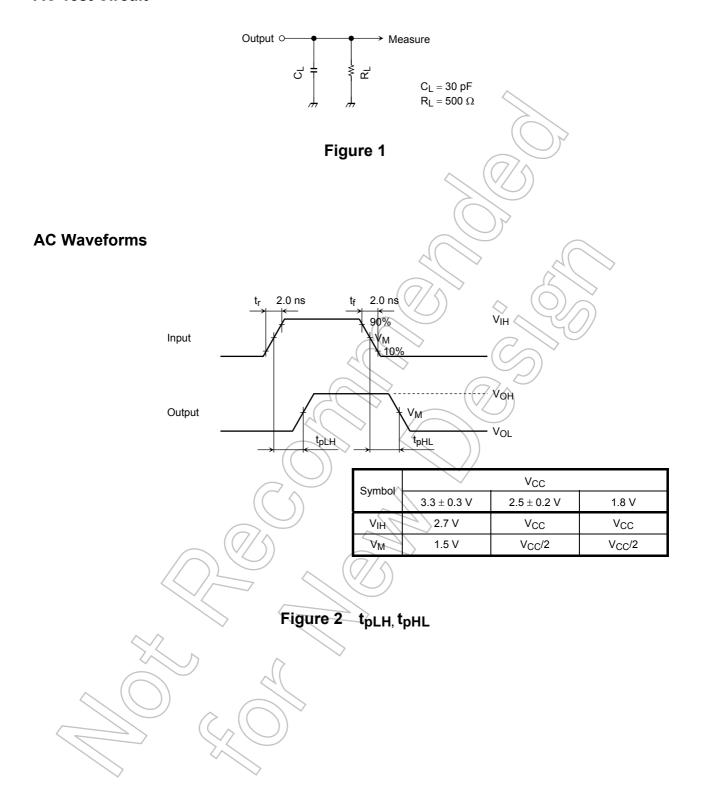
Note 11: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

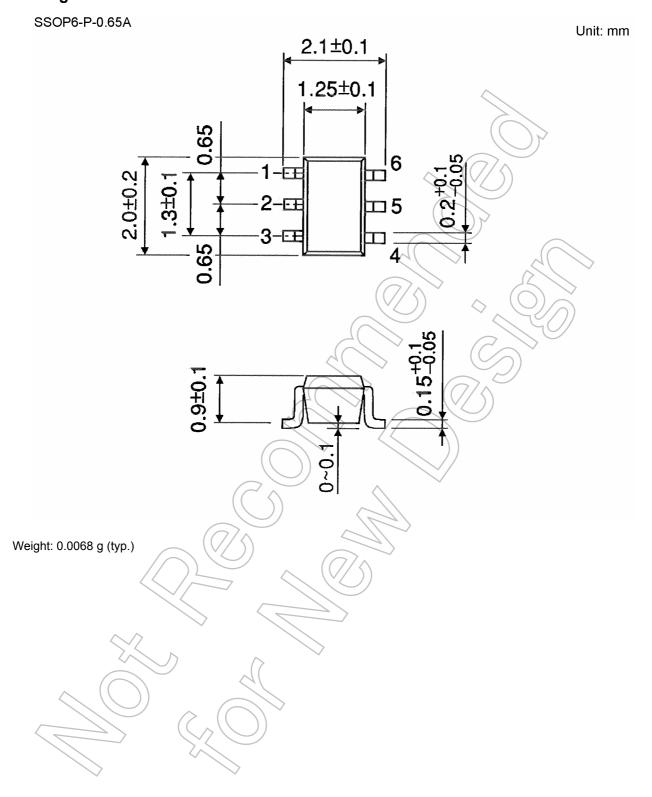
 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

#### **AC Test Circuit**





### **Package Dimensions**



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