TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC04F, TC74VHC04FT, TC74VHC04FK

Hex Inverter

The TC74VHC04 is an advanced high speed CMOS INVERTER fabricated with silicon gate $\rm C^2MOS$ technology.

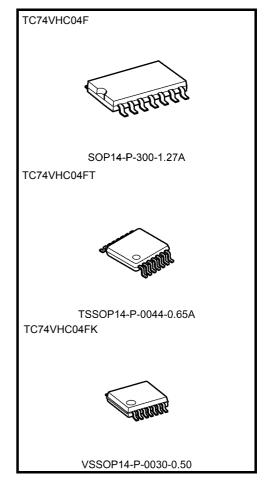
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5~V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5~V to 3~V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

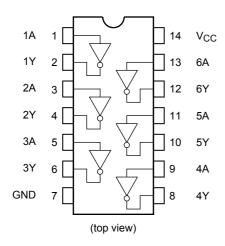
- High speed: $t_{pd} = 3.8 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: t_{pLH} ≃ t_{pHL}
- Wide operating voltage range: $V_{CC \text{ (opr)}} = 2 \text{ V to } 5.5 \text{ V}$
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS04



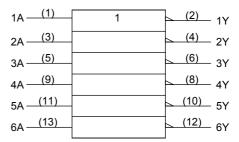
Weiaht

SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Pin Assignment



IEC Logic Symbol



Truth Table

Α	Υ
L	Н
Н	L

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	−0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	−40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 ($V_{CC} = 3.3 \pm 0.3 \text{ V}$)	ns/V	
input rise and fail tille	ui/uv	0 to 20 ($V_{CC} = 5 \pm 0.5 \text{ V}$)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condi		Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
				V _{CC} (V)	Min	Тур.	Max	Min	Max	
High-level input					1.50	_	_	1.50	_	
voltage	V _{IH}		_		V _{CC} × 0.7	-	_	V _{CC} × 0.7	_	V
Low-level input				2.0	_	-	0.50	_	0.50	
voltage	V _{IL}	_		3.0 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
				2.0	1.9	2.0	_	1.9	_	
			$I_{OH} = -50 \mu A$	3.0	2.9	3.0	_	2.9	_	
High-level output voltage	V _{OH}	$V_{IN} = V_{IL}$		4.5	4.4	4.5	_	4.4	-	V
Ŭ			$I_{OH} = -4 \text{ mA}$	3.0	2.58	-	_	2.48	_	
		$I_{OH} = -8 \text{ mA}$	4.5	3.94	1	_	3.80	1		
			2.0	-	0.0	0.1	_	0.1		
			$I_{OL} = 50 \mu A$	3.0	_	0.0	0.1	_	0.1	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH}$		4.5	_	0.0	0.1	_	0.1	V
		I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44		
		$I_{OL} = 8 \text{ mA}$	4.5	_	1	0.36	_	0.44		
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1	ı	±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	or GND	5.5	_	_	2.0	_	20.0	μΑ

3



AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics Syml	Symbol	Symbol		st Condition		Ta = 25°C			Ta = −40 to 85°C	
	- ,			C _L (pF)	Min	Тур.	Max	Min	Max	
Propagation delay t _{pLH} time t _{pHL}		_	3.3 ± 0.3	15	_	5.0	7.1	1.0	8.5	- ns
	· ·			50	_	7.5	10.6	1.0	12.0	
			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
				50	_	5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}	_			_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)		18	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

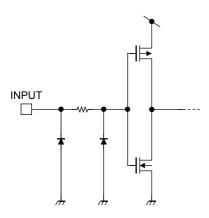
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$

Noise Characteristics (input: $t_r = t_f = 3$ ns)

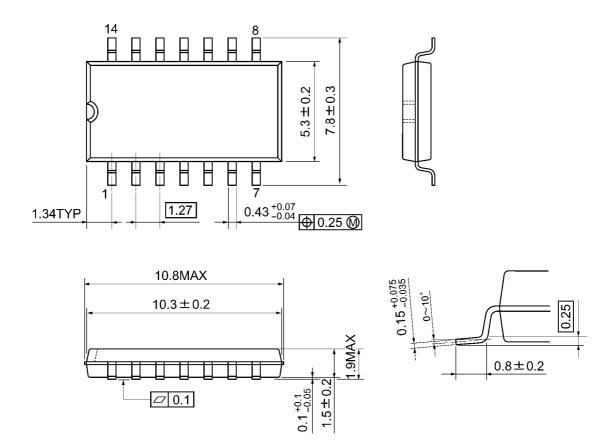
Characteristics	Symbol	Test Condition	Ta =	- Unit		
			V _{CC} (V)	Тур.	Limit	Offic
Quiet output maximum dynamic V _{OL}	V_{OLP}	C _L = 50 pF	5.0	0.4	8.0	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0	_	1.5	V

Input Equivalent Circuit



Package Dimensions

SOP14-P-300-1.27A Unit: mm



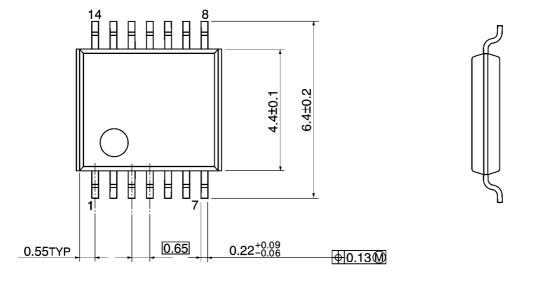
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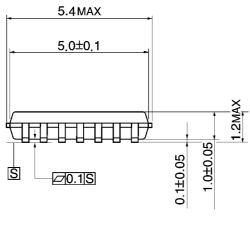
Weight: 0.18 g (typ.)

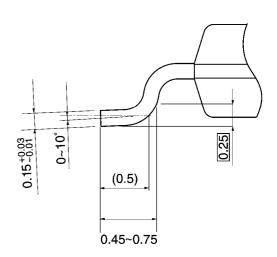
Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



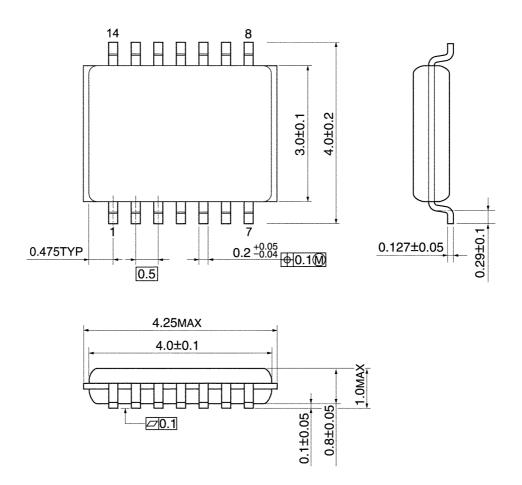




Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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