TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX244F, TC74LCX244FT, TC74LCX244FK

Low-Voltage Octal Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX244 is a high-performance CMOS octal bus buffer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

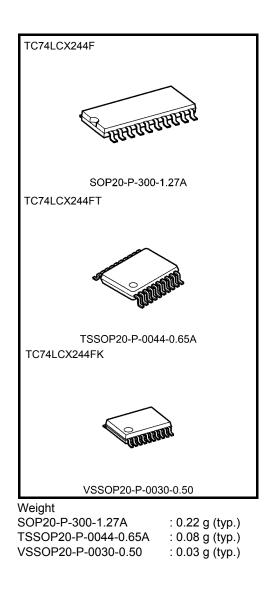
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

The 74LCX244F/FT is a non-inverting 3-state buffer having two active-low output enables. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.

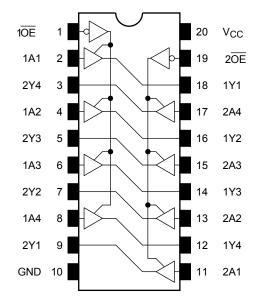
Features

- Low-voltage operation: V_{CC} = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: $\geq \pm 500 \text{ mA}$
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 244 type



Note: The Electrical Characteristics of V_{CC}=1.8 \pm 0.15V is only applicable for products which manufactured from January 2009 onward.

Pin Assignment (top view)



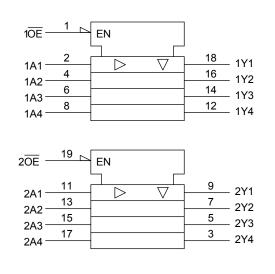
Truth Table

| Inputs | | Outoute | | |
|--------|----|---------|--|--|
| ŌĒ | An | Outputs | | |
| L | L | L | | |
| L | Н | Н | | |
| Н | Х | Z | | |

X: Don't care

Z: High impedance

IEC Logic Symbol



Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|------------------------------------|-----------------------------------|---|------|
| Power supply voltage | V _{CC} | -0.5 to 7.0 | V |
| DC input voltage | V _{IN} | -0.5 to 7.0 | V |
| | | -0.5 to 7.0 (Note 2) | |
| DC output voltage | Vout | -0.5 to V _{CC} + 0.5 (Note 3) | V |
| Input diode current | IIK | -50 | mA |
| Output diode current | I _{OK} | ±50 (Note 4) | mA |
| DC output current | IOUT | ±50 | mA |
| Power dissipation | PD | 180 | mW |
| DC V _{CC} /ground current | I _{CC} /I _{GND} | ±100 | mA |
| Storage temperature | T _{stg} | –65 to 150 | °C |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 2: Output in OFF state
- Note 3: High or low state. IOUT absolute maximum rating must be observed.
- Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

| Characteristics | Symbol | Rating | Unit | |
|--------------------------|------------------|-------------------------------|------|--|
| Power supply voltage | V _{CC} | 1.65 to 3.6 | V | |
| Fower supply voltage | v.cc | 1.5 to 3.6 (Note 2) | v | |
| Input voltage | V _{IN} | 0 to 5.5 | V | |
| Output voltage | | 0 to 5.5 (Note 3) | V | |
| Output voltage | Vout | 0 to V _{CC} (Note 4) | | |
| Output current | lev/lev | ±24 (Note 5) | mA | |
| Output current | IOH/IOL | ±12 (Note 6) | ША | |
| Operating temperature | T _{opr} | -40 to 85 | °C | |
| Input rise and fall time | dt/dv | 0 to 10 (Note 7) | ns/V | |

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

- Note 2: Data retention only
- Note 3: Output in OFF state
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0$ to 3.6 V
- Note 6: $V_{CC} = 2.7$ to 3.0 V
- Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

3

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

| Characteris | tics | Symbol | Test Condition V _{CC} (V) | | V _{CC} (V) | Min | Max | Unit | | | |
|------------------------------------|---|------------------|--|---------------------------|-------------------------|----------------------|----------------------|--------------------------|-----|---|-----|
| H-level | | | _ | | 1.65 to 2.3 | V _{CC} ×0.9 | | | | | |
| | | VIH | | | 2.3 to 2.7 | 1.7 | — | | | | |
| Input voltage | | | | | 2.7 to 3.6 | 2.0 | — | V | | | |
| input voltage | | | | | 1.65 to 2.3 | _ | V _{CC} ×0.1 | | | | |
| | L-level | VIL | | - | 2.3 to 2.7 | | 0.7 | | | | |
| | | | | | 2.7 to 3.6 | | 0.8 | | | | |
| | | | | $I_{OH} = -100 \ \mu A$ | 1.65 to 3.6 | V _{CC} -0.2 | — | | | | |
| | | | | I _{OH} = -4 mA | 1.65 | 1.05 | — | · · · | | | |
| | H-level | V _{OH} | V _{IN} = V _{IH} or V _{IL} | I _{OH} = -8 mA | 2.3 | 1.7 | — | | | | |
| | | VОН | | $I_{OH} = -12 \text{ mA}$ | 2.7 | 2.2 | — | | | | |
| | | | | I _{OH} = -18 mA | 3.0 | 2.4 | — | | | | |
| Output voltage | | | | $I_{OH} = -24 \text{ mA}$ | 3.0 | 2.2 | — | | | | |
| Output voltage | | | $V_{IN} = V_{IH} \text{ or } V_{IL}$ | $I_{OL} = 100 \ \mu A$ | 1.65 to 3.6 | — | 0.2 | | | | |
| | | | | $I_{OL} = 4 \text{ mA}$ | 1.65 | _ | 0.45 | | | | |
| | L-level | V _{OL} | | $I_{OL} = 8 \text{ mA}$ | 2.3 | _ | 0.7 | | | | |
| | L-IEVEI | VOL | | | | | | $I_{OL} = 12 \text{ mA}$ | 2.7 | _ | 0.4 |
| | | | | | I _{OL} = 16 mA | 3.0 | _ | 0.4 | | | |
| | | | | $I_{OL} = 24 \text{ mA}$ | 3.0 | _ | 0.55 | | | | |
| Input leakage current | | I _{IN} | $V_{IN} = 0$ to 5.5 V | | 1.65 to 3.6 | — | ±5.0 | μA | | | |
| 3-state output off-state | state output off-state current I_{OZ} $V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5 \text{ V}$ | | 1.65 to 3.6 | | ±5.0 | μA | | | | | |
| Power off leakage cur | wer off leakage current I _{OFF} V _{IN} /V _{OUT} = 5.5 V | | 0 | _ | 10.0 | μA | | | | | |
| | O de constante de constante | | V _{IN} = V _{CC} or GND | | 1.65 to 3.6 | _ | 10.0 | | | | |
| Quiescent supply curr | ent | Icc | $V_{IN}/V_{OUT} = 3.6$ to 5.5 V | | 1.65 to 3.6 | | ±10.0 | μA | | | |
| Increase in I _{CC} per in | out | ∆l _{CC} | $V_{IH} = V_{CC} - 0.6V$ | | 2.7 to 3.6 | | 500 | | | | |

AC Characteristics (Ta = -40 to 85°C)

| Characteristics | Symbol | Symbol Test Condition | | Min | Max | Unit |
|------------------------|--------------------------------------|-------------------------------|-------------------------------|--------|------|------|
| Characteristics | Cymbol | | V _{CC} (V) | IVIIII | Max | Unit |
| | | | 1.8 ± 0.15 | _ | 25.0 | ns |
| Propagation delay time | t _{pLH} | Figure 1, Figure 2 | 2.5 ± 0.2 | | 8.5 | |
| Topagation delay time | t _{pHL} | | 2.7 | _ | 7.5 | |
| | | | $\textbf{3.3}\pm\textbf{0.3}$ | 1.5 | 6.5 | |
| | | Figure 1, Figure 3 | 1.8 ± 0.15 | | 32.0 | ns |
| Output enable time | t _{pZL} t _{pZH} | | 2.5 ± 0.2 | | 16.0 | |
| Output enable time | | | 2.7 | | 9.0 | |
| | | | $\textbf{3.3}\pm\textbf{0.3}$ | 1.5 | 8.0 | |
| | | Figure 1, Figure 3 | 1.8 ± 0.15 | _ | 30.0 | |
| Outeut dischla time | t _{pLZ} t _{pHZ} | | 2.5 ± 0.2 | _ | 15.0 | |
| Output disable time | | | 2.7 | _ | 8.0 | ns |
| | | | $\textbf{3.3}\pm\textbf{0.3}$ | 1.5 | 7.0 | |
| | t _{osLH} | (Note) | 2.7 | _ | _ | ns |
| Output to output skew | t _{osHL} | $\textbf{3.3}\pm\textbf{0.3}$ | | 1.0 | 115 | |

Note: Parameter guaranteed by design.

 $(t_{osLH} = |t_{pLHm} - t_{pLHn}|, t_{osHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: t_r = t_f = 2.5 ns, C_L = 50 pF, R_L = 500 Ω)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|---------------------------------------|--------|--|---------------------|------|------|
| Quiet output maximum dynamic V_{OL} | VOLP | $V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ | 3.3 | 0.8 | V |
| Quiet output minimum dynamic V_{OL} | Volv | $V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ | 3.3 | 0.8 | V |

Capacitive Characteristics (Ta = 25°C)

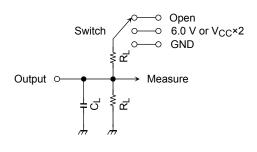
| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|-------------------------------|------------------|------------------------------|---------------------|------|------|
| Input capacitance | C _{IN} | — | 3.3 | 7 | pF |
| Output capacitance | C _{OUT} | | 3.3 | 8 | pF |
| Power dissipation capacitance | C _{PD} | f _{IN} = 10 MHz (No | e) 3.3 | 25 | pF |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation: $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$ (per bit)

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AC Test Circuit

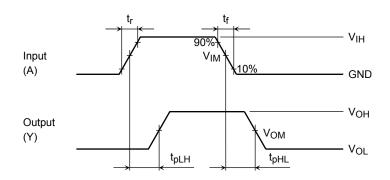


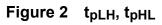
| Parameter | Switch | | |
|-------------------------------------|--------------------|------------------------------|--|
| t _{pLH} , t _{pHL} | Open | | |
| | 6.0 V | @ V _{CC} =3.3±0.3V | |
| t., | | @ V _{CC} =2.7V | |
| t _{pLZ} , t _{pZL} | V _{CC} ×2 | @ V _{CC} =2.5±0.2V | |
| | | @ V _{CC} =1.8±0.15V | |
| t _{pHZ} , t _{pZH} | GND | | |

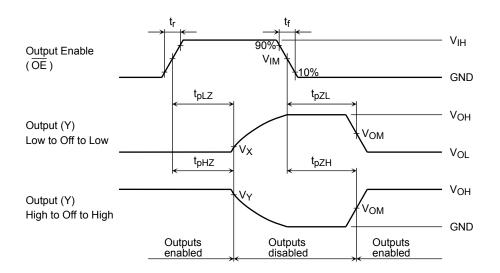
Figure 1

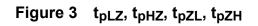
AC Waveform

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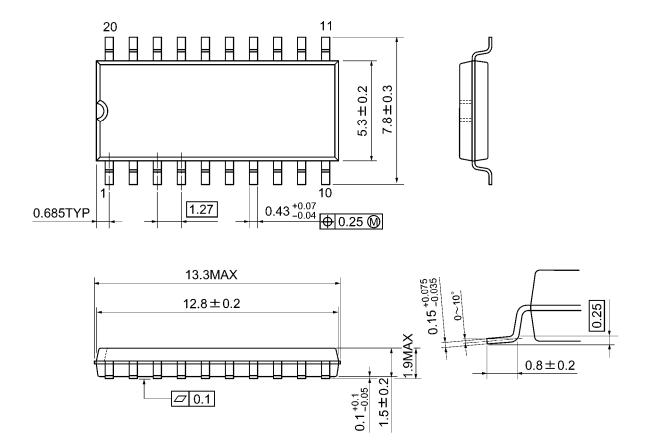
| | | V _{CC} | | | |
|--------|---------------------------------|-----------------------|------------------------|------------------------|--|
| | Symbol | $3.3\pm0.3~\text{V}$ | 2.5 ± 0.2 V | 1.8 ± 0.15 V | |
| | | 2.7V | 2.5 ± 0.2 V | 1.8 ± 0.15 V | |
| Input | VIH | 2.7V | V _{CC} | V _{CC} | |
| | VIM | 1.5V | V _{CC} /2 | V _{CC} /2 | |
| | t _r , t _f | 2.5ns | 2.0ns | 2.0ns | |
| Output | V _{OM} | 1.5V | V _{OH} /2 | V _{OH} /2 | |
| | VX | V _{OL} +0.3V | V _{OL} +0.15V | V _{OL} +0.15V | |
| | VY | V _{OH} -0.3V | V _{OH} -0.15V | V _{OH} -0.15V | |
| Load | CL | 50pF | 30pF | 30pF | |
| | RL | 500Ω | 500Ω | 1kΩ | |



Package Dimensions

SOP20-P-300-1.27A

Unit: mm



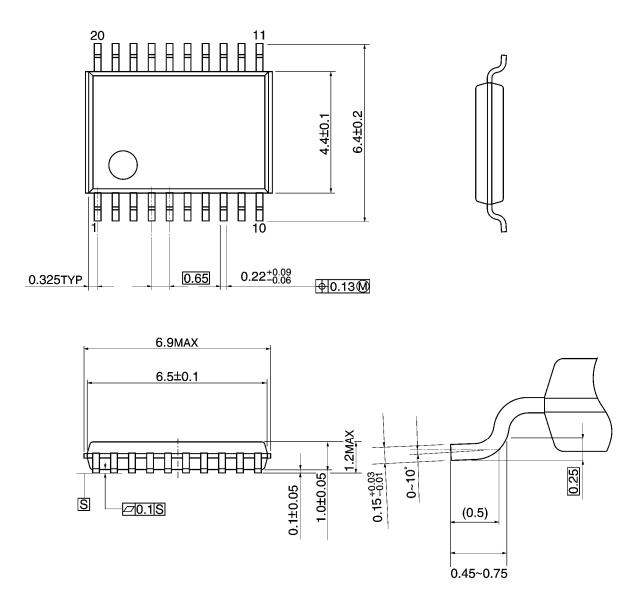
Weight: 0.22 g (typ.)

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Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm



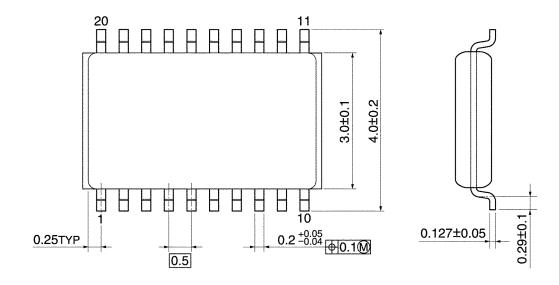
Weight: 0.08 g (typ.)

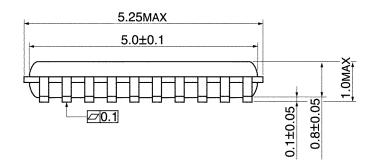


Package Dimensions

VSSOP20-P-0030-0.50

Unit: mm





Weight: 0.03 g (typ.)

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