

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX08F, TC74LCX08FK

Low-Voltage Quad 2-Input AND Gate with 5-V Tolerant Inputs and Outputs

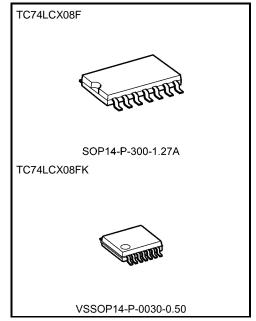
The TC74LCX08 is a high-performance CMOS 2-input AND gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) $V_{\rm CC}$ applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 5.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEITA SOP, VSSOP (US)
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 08 type



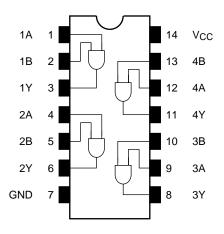
Weight

SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

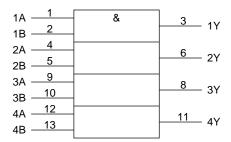
Start of commercial production 1994-10



Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs
А	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	Vcc	-0.5 to 7.0	V	
DC input voltage	V _{IN}	-0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)	V	
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)		
Input diode current	lıK	-50	mA	
Output diode current	lok	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	180	mW	
DC Vcc/ground current	ICC/IGND	±100	mA	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: VCC = 0 V

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Dower Cupply voltage	Voc	2.0 to 3.6	V
Power Supply voltage	Vcc	1.5 to 3.6 (Note 2)	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vout	0 to 5.5 (Note 3)	V
Output voltage		0 to Vcc (Note 4)	V
Output ourropt	IOH/IOL	±24 (Note 5)	mA
Output current	IOH/IOL	±12 (Note 6)	IIIA
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: VCC = 0 V

Note 4: High or low state Note 5: VCC = 3.0 to 3.6 V

Note 6: VCC = 2.7 to 3.0 V

Note 7: VIN = 0.8 to 2.0 V, VCC = 3.0 V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Currente est	Test Condition			N 4:	Max	Unit
Character	Characteristics Symbol Test Condition		Condition	V _{CC} (V)	Min			
lancit coltana	H-level	VIH		_		2.0	_	V
Input voltage	L-level	VIL		_	2.7 to 3.6	_	0.8	
			I _{OH} = -100 μA	2.7 to 3.6	V _C C - 0.2	_		
	H-level	Voн	VIN = VIH	I _{OH} = -12 mA	2.7	2.2	_	
Output voltage L-level			I _{OH} = -18 mA	3.0	2.4	_		
				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V
		VoL	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.7 to 3.6	_	0.2	
	Llovel			$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
	L-level			I _{OL} = 16 mA	3.0	_	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 5.5 V	V _{IN} = 0 to 5.5 V		_	±5.0	μΑ
Power-off leakage	current	loff	V _{IN} /V _{OUT} = 5.5 V	V _{IN} /V _{OUT} = 5.5 V		_	10.0	μΑ
Quiescent supply current		las	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	10.0	
		Icc	V _{IN} = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μΑ
Increase in I _{CC} pe	r input	Δlcc	V _{IH} = V _{CC} - 0.6 V (per 1 input)		2.7 to 3.6	_	500	



AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{PLH}	Figure 1, Figure 2	2.7	_	6.2	- ns
			3.3 ± 0.3	1.5	5.5	
Output to output skew	t _{osLH}	(Note)	2.7		_	20
			3.3 ± 0.3		1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic VoL	VOLP	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic VOL	Volv	V _{IH} = 3.3 V, V _{IL} = 0 V	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Cout	_	0	8	pF
Power dissipation capacitance	CPD	f _{IN} = 10 MHz (Not	e) 3.3	25	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = $CPD \cdot VCC \cdot fIN + ICC/4$ (per gate)



AC Test Circuit

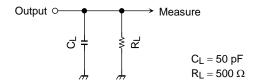


Figure 1

AC Waveform

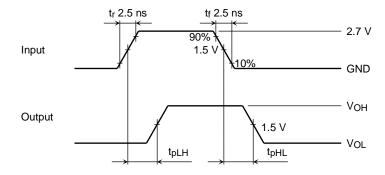
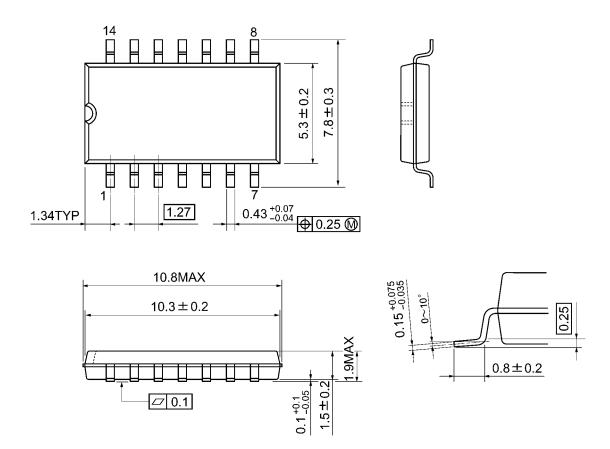


Figure 2 t_{pLH}, t_{pHL}



Package Dimensions

SOP14-P-300-1.27A Unit: mm

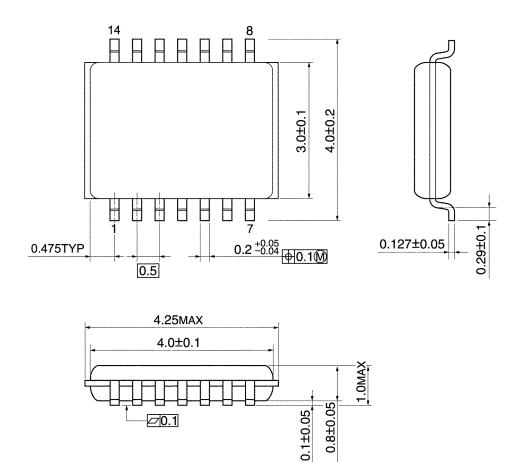


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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