TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74ACT139P, TC74ACT139F, TC74ACT139FT

#### Dual 2-to-4 Line Decoder

The TC74ACT139 is an advanced high speed CMOS 2 to 4 LINE DECODER fabricated with silicon gate and double-layer metal wiring  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

When the enable input is held "H", all four outputs are fixed at a high logic level independent of the other inputs.

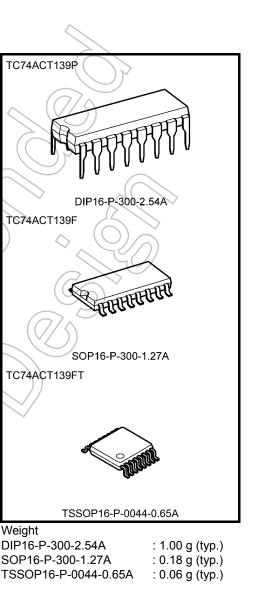
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### Features

- High speed:  $t_{pd}$  = 5.5 ns (typ.) at V<sub>CC</sub> = 5 V
- Low power dissipation:  $I_{CC} = 8 \mu A \pmod{at}$  at  $Ta = 25^{\circ}C$
- Compatible with TTL outputs: V<sub>IL</sub> = 0.8 V (max)
  - $V_{IH} = 2.0 V (min)$
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24 \text{ mA} (\text{min})$

Capability of driving 50 Ω transmission lines.

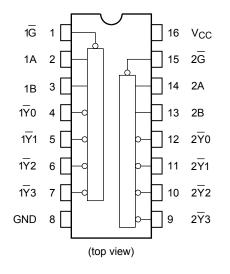
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74F139



2014-03-01

# **TOSHIBA**

## **Pin Assignment**



## **IEC Logic Symbol**

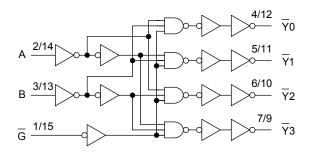
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			210

#### **Truth Table**

					10-1			
Inp	((	7 Qût	$\langle \rangle$					
Enable	Select		Ϋ́ο		۲2	T <sub>3</sub>	Selected Output	
G	В	A		$\overline{7}$	12	13	$\langle \rangle \rangle$	
Н	Х	X	Ц	Н	H	H	None	
L	L	L	Ś	Н	F	Н	<u>₹</u> 0	
L	Ŕ	Н	Н	L	н	È	T1	
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L	H	Н	Н	H	Н	L	¥3	
		1			$\sim$			

X: Don't care





#### Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	v
Input diode current	IIК	±20	mA
Output diode current	IOK	±50	mA
DC output current	IOUT	±50	mA
DC V <sub>CC</sub> /ground current	ICC	±200	)) mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	4.5 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	VOUT	0 to V <sub>CC</sub>	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 10	ns/V

## **Operating Ranges (Note)**

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.



## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			-	Ta = 25°C			Ta = −40 to 85°C		Unit
	- <b>,</b>				$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
High-level input voltage	V <sub>IH</sub>	-			4.5 to 5.5	2.0	4	K	2.0	Ι	V
Low-level input voltage	V <sub>IL</sub>		—		4.5 to 5.5		_(	0.8	2	0.8	V
		Vini			4.5	4.4	4.5	<u>}</u>	4.4	-	
High-level output voltage	V <sub>OH</sub>	= V <sub>IH</sub> or V <sub>IL</sub>			4.5	3.94	V.	))	3.80	—	V
5			I <sub>OH</sub> = −75 mA (	Note)	5.5	(T	$\times$	_	3.85	—	
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or	I <sub>OL</sub> = 50 μA		4.5	7	0.0	0.1		0.1	
Low-level output voltage			I <sub>OL</sub> = 24 mA		4.5	1.5		0.36	$\bigcirc$	0.44	V
		VIL	I <sub>OL</sub> = 75 mA (	Note)	5.5	2	-	- ~	$\frac{1}{2}$	1.65	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>C</sub>	<sub>C</sub> or GND		5.5	1	14	±0,1		±1.0	μA
	ICC	$V_{IN} = V_C$	<sub>C</sub> or GND	6	5.5	_	~ <	8.0	ZE)/	80.0	μA
Quiescent supply current	Ι <sub>C</sub>	-	: V <sub>IN</sub> = 3.4 V ut: V <sub>CC</sub> or GND	$\frac{1}{2}$	5.5	_	E	1.35		1.5	mA

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

## AC Characteristics (C<sub>L</sub> = 50 pF, R<sub>L</sub> = 500 $\Omega$ , input: t<sub>r</sub> = t<sub>f</sub> = 3 ns)

Characteristics	Symbol	Test Condition			a = 25°C	)	Ta −40 to	Unit	
	5	$( \subset \$	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Propagation delay time	t <sub>pLH</sub>	7 - 4	5.0±0.5	>_	6.2	9.2	1.0	10.5	ns
(A, B- <del>Y</del> )	tpHL	$(\bigcirc)$							
Propagation delay time	tpLH		5.0 ± 0.5	_	6.3	9.6	1.0	11.0	ns
(	<sup>t</sup> pHL		>						
Input capacitance	CIN	<u> </u>		_	5	10		10	pF
Power dissipation capacitance	Срр	$\sim$	(Note)	_	51	_			pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

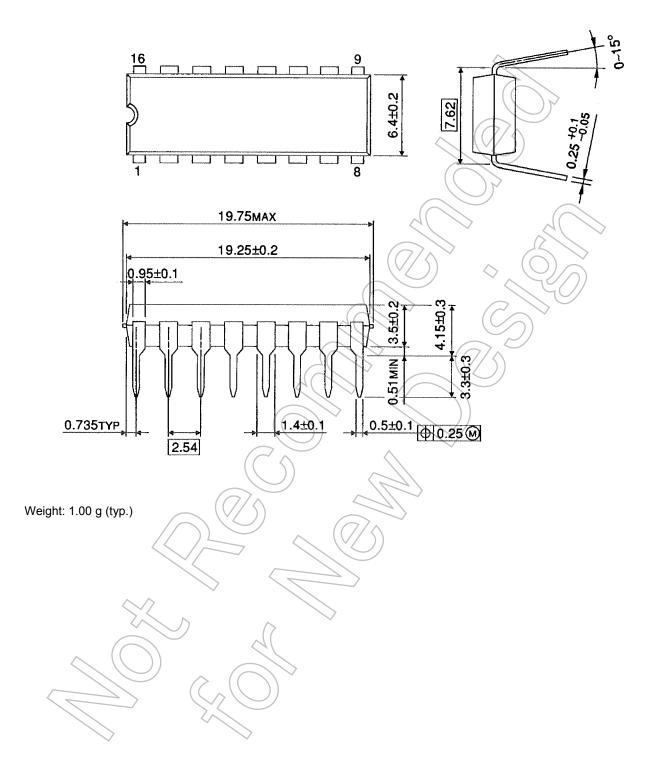
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$  (per becoder)

#### **Package Dimensions**

DIP16-P-300-2.54A

Unit : mm

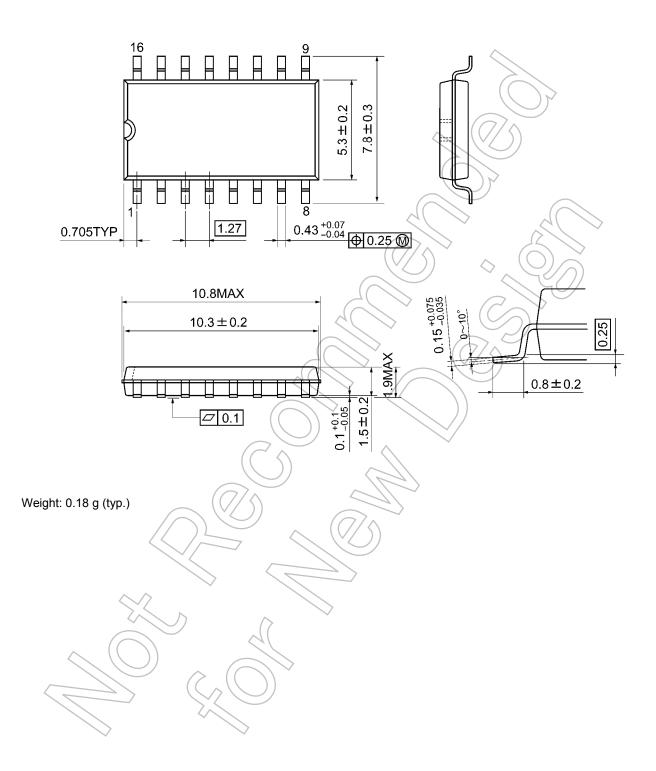




#### **Package Dimensions**

SOP16-P-300-1.27A

Unit: mm

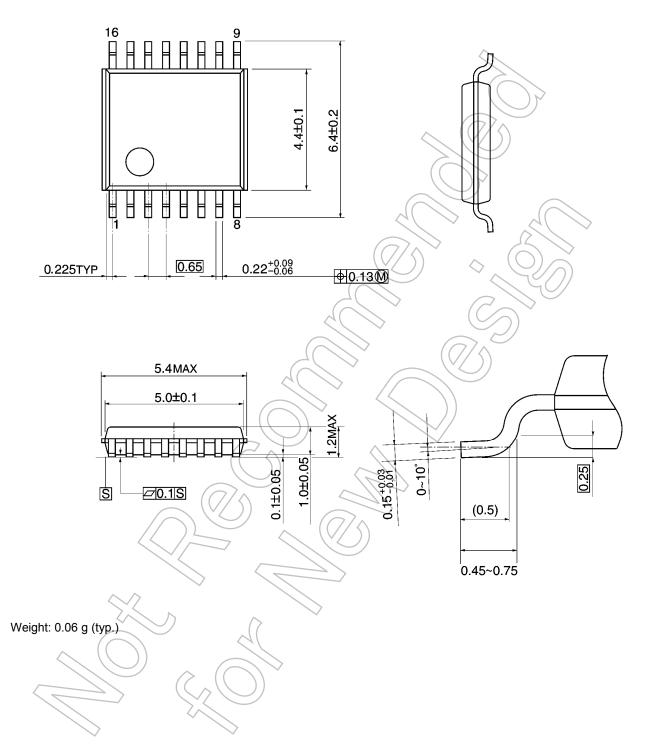


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## **Package Dimensions**

TSSOP16-P-0044-0.65A

Unit: mm



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