TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC540P, TC74AC540F, TC74AC540FT TC74AC541P, TC74AC541F, TC74AC541FT

Octal Bus Buffer

TC74AC540P/F/FT Inverting, 3-State

Outputs

TC74AC541P/F/FT Non-Inverting, 3-State

Outputs

The TC74AC540/TC74AC541 are advanced high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate and double-layer metal wiring $\rm C^2MOS$ technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The TC74AC540 is an inverting type, and the TC74AC541 is a non-inverting type.

When either $\overline{G}1$ or $\overline{G}2$ are high, the terminal outputs are in the high-impedance state.

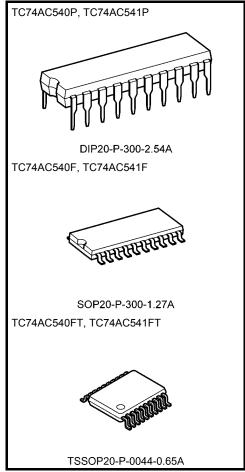
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 4.0 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 8 \mu A$ (max) at $T_a = 25$ °C
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω

transmission lines.

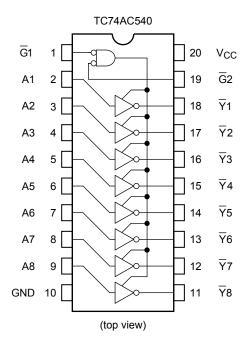
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: $V_{CC \text{ (opr)}} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74F540/541

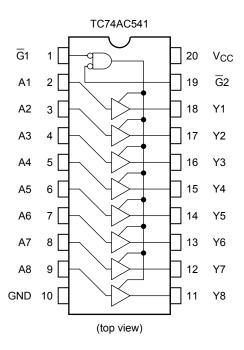


Weight

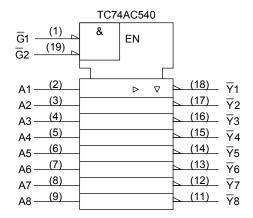
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.)

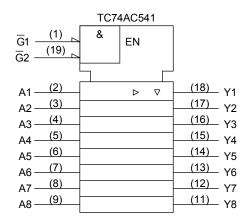
Pin Assignment





IEC Logic Symbol





Truth Table

	Inputs	Outputs			
G1	G2	An	Yn	\overline{Y}_n	
Н	Х	Х	Z	Z	
Х	Н	Х	Z	Z	
L	L	Н	Н	L	
L	L	L	L	Н	

X: Don't care

Z: High impedance

Y_n: AC541

Y_n: AC540

2



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±200	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to V _{CC}	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
input rise and fail tille	ui/uv	0 to 20 (V _{CC} = 5 ± 0.5 V)		

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

3



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit		
					V _{CC} (V)	Min	Тур.	Max	Min	Max	Ullit
		_		2.0	1.50	_	_	1.50	_		
High-level input voltage	V_{IH}			3.0	2.10	_	_	2.10	_	V	
ŭ					5.5	3.85	-	_	3.85	_	
					2.0		_	0.50	_	0.50	
Low-level input voltage	V_{IL}		_		3.0	_	_	0.90	_	0.90	V
ŭ					5.5	1	-	1.65	1	1.65	
					2.0	1.9	2.0	_	1.9	_	
			I _{OH} = -50 μA		3.0	2.9	3.0	_	2.9	_	
High-level output	V _{OH}	V _{IN} = V _{IH} or V _{IL}			4.5	4.4	4.5	_	4.4	_	V
voltage	VОН		I _{OH} = -4 mA		3.0	2.58	_	_	2.48	_	V
			I _{OH} = −24 mA		4.5	3.94	_	_	3.80	_	
			I _{OH} = -75 mA	(Note)	5.5	1	-	_	3.85	_	
		V _{IN} = V _{IH} or V _{IL}			2.0		0.0	0.1	_	0.1	- V
			I _{OL} = 50 μA		3.0	_	0.0	0.1	_	0.1	
Low-level output	V _{OL}				4.5	1	0.0	0.1	1	0.1	
voltage			I _{OL} = 12 mA		3.0	_	_	0.36	_	0.44	
			I _{OL} = 24 mA		4.5	_	_	0.36	_	0.44	
			I _{OL} = 75 mA	(Note)	5.5	1	-	_	-	1.65	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5	_		±0.5	-	±5.0	μΑ	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_	_	±0.1	_	±1.0	μA	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	8.0	_	80.0	μΑ	

4

Note: This spec indicates the capability of driving 50 Ω transmission lines. One output should be tested at a time for a 10 ms maximum duration.



AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	- ,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay	t _{pLH}		3.3 ± 0.3	_	6.8	10.5	1.0	12.0	ns
time (Note 2)	t_{pHL}	_	5.0 ± 0.5	_	4.7	7.0	1.0	8.0	
Propagation delay	t _{pLH}		3.3 ± 0.3	_	6.8	11.4	1.0	13.0	ns
time (Note 3)	t_{pHL}	_	5.0 ± 0.5	_	4.7	7.5	1.0	8.5	
Output enable time	t_{pZL}		3.3 ± 0.3	_	9.6	15.8	1.0	18.0	ns
	t_{pZH}	_	5.0 ± 0.5	_	6.4	10.0	1.0	11.4	
Output disable time	t _{pLZ}		3.3 ± 0.3	_	7.7	12.3	1.0	14.0	
	t_{pHZ}	_	5.0 ± 0.5	_	6.4	9.2	1.0	10.5	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Output capacitance	C _{OUT}	_		_	10	_	_	_	pF
Power dissipation capacitance	C _{PD}	TC74AC540		_	25	_	_	_	, r
	(Note 1)	TC74AC541		_	28	_	_	_	pF

Note1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

5

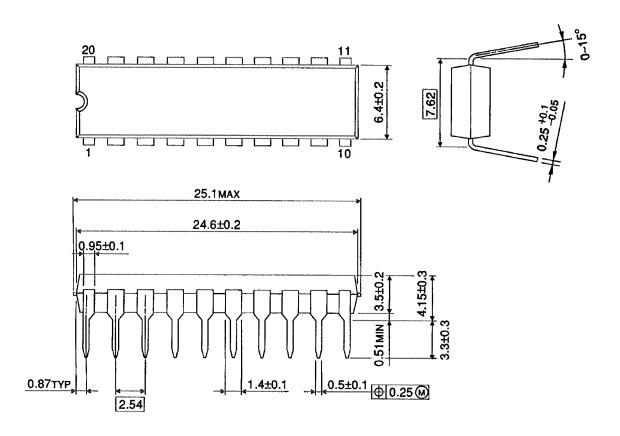
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 (per bit)$

Note 2: For TC74AC540 only Note 3: For TC74AC541 only

Package Dimensions

DIP20-P-300-2.54A Unit: mm

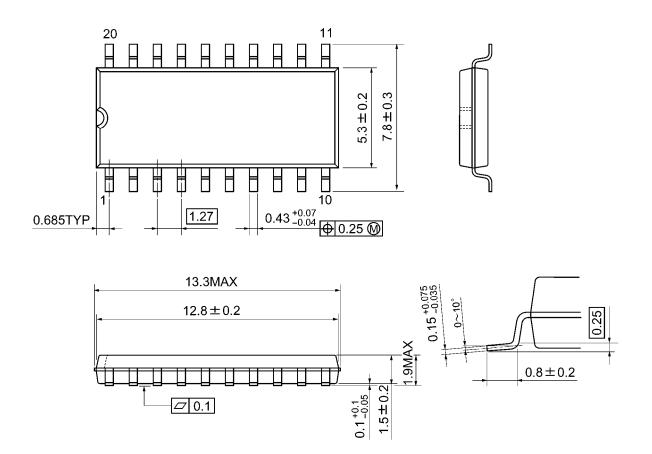


Weight: 1.30 g (typ.)



Package Dimensions

SOP20-P-300-1.27A Unit: mm



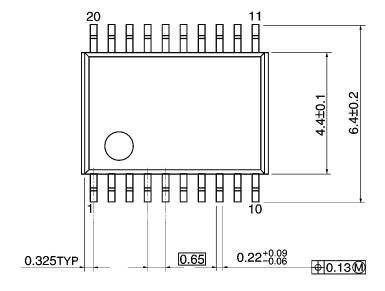
Weight: 0.22 g (typ.)

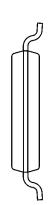


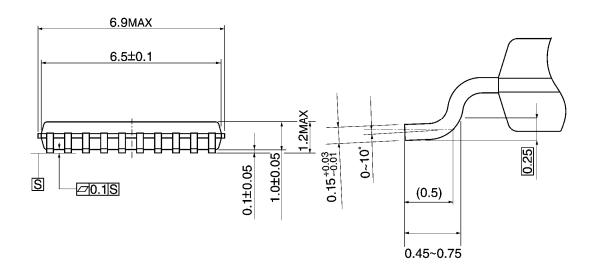
Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm







Weight: 0.08 g (typ.)

RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
 EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH
 MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
 ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without
 limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for
 automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions,
 safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE
 PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your
 TOSHIBA sales representative.
- · Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
 applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE
 FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY
 WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR
 LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND
 LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO
 SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS
 FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
 Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES
 OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

9 2014-03-01