TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

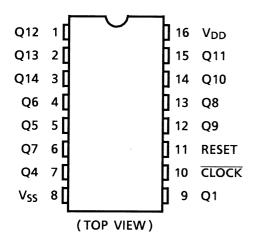
TC4020BP, TC4020BF

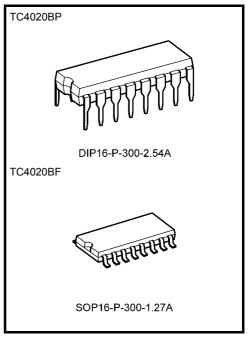
TC4020B 14 Stage Ripple-Carry Binary Counter/Dividers

TC4020B is 14 stage ripple carry binary counter having asynchronous clear function. The counter advances its counting stage by falling edge of \overline{CLOCK} input. When RESET input is placed "H", all the circuits are reset regardless of \overline{CLOCK} input making all the outputs (Q1, Q4 to Q14) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

Pin Assignment





Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

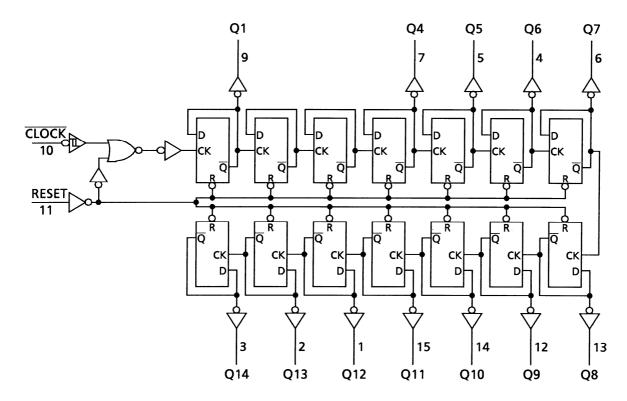
Truth Table

$\overline{CLOCK}\Delta$	RESET	Output State		
* H		All Outputs = "L"		
L		No Change		
→ L		Advance to Next State		

Δ: Level change

*: Don't care

Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output voltage	V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOP)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V_{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

I haracteristics 1		Sym-	Test Condition		-40°C		25°C			85°C			
		bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
High-level output voltage		V _{OH}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10	4.95 9.95	_ _	4.95 9.95	5.00 10.00	_ _	4.95 9.95	_ _	V	
				15	14.95	_	14.95	15.00	_	14.95			
Low-level	outnut		 I _{OUT} < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05		
voltage	σαιραί	V_{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V	
				15	_	0.05	_	0.00	0.05	_	0.05		
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_		
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA	
Output hig	h current	loh	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_		
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_		
			$V_{IN} = V_{SS}, V_{DD}$										
			V _{OL} = 0.4 V	5	0.61	_	0.51	1.2	_	0.42	_	mA	
Output lov	v ourront		V _{OL} = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_		
Output low current		l _{OL}	V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	IIIA	
			$V_{IN} = V_{SS}, V_{DD}$										
		VIH	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	٧	
ماند الماندات			V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_		
Input high	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_		
			I _{OUT} < 1 μA										
		.,	V _{OUT} = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	.,	
	14		V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0		
Input low voltage		V _{IL}	V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V	
			I _{OUT} < 1 μA										
Input	"H" level	I _{IH}	V _{IH} = 18 V	18	_	0.1	_	10^{-5}	0.1	_	1.0		
current	"L" level	I _{IL}	V _{IL} = 0 V	18	_	-0.1	_	-10^{-5}	-0.1	_	-1.0	μΑ	
	•	I _{DD}	$V_{IN} = V_{SS}, V_{DD}$ (Note)	5	_	5	_	0.005	5	_	150		
Quiescent current	supply			10	_	10	_	0.010	10	_	300	μА	
			(Note)	15	_	20	_	0.015	20		600		

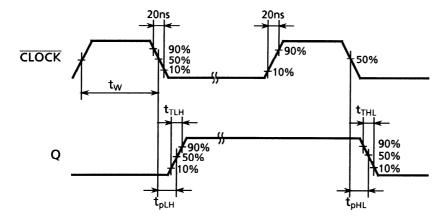
Note: All valid input combinations.

Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

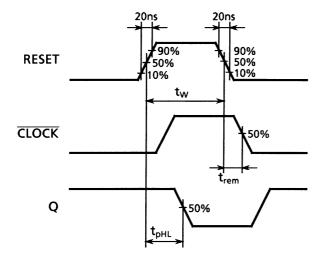
Charastaristics	Comple al	Test Condition	Min	T	May	Linit	
Characteristics	Symbol		V _{DD} (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
Output transition time	t _{TLH}	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
Output transition time	t _{THL}	_	10	_	35	100	ns
(high to low)			15	_	30	80	
Propagation delay time			5	_	160	360	
(CLOCK -Q1)	t _{pLH}	_	10	_	80	160	ns
(CLOCK -Q1)			15	_	65	130	
Draw a station delevations			5	_	160	360	
Propagation delay time (CLOCK -Q1)	t _{pHL}	_	10	_	80	160	ns
(CLOCK -Q1)			15	_	65	130	
Propagation delay time			5	_	1000	2000	
(CLOCK -Q14)	t _{pLH}	_	10	_	500	1000	ns
(CLOCK -Q14)			15	_	400	800	
Drangation delay time			5	_	1000	2000	
Propagation delay time (CLOCK -Q14)	t _{pHL}	_	10	_	500	1000	ns
(CLOCK -Q14)			15	_	400	800	
Propagation delay time			5	_	150	280	
(RESET-Q)	t _{pHL}	_	10	_	70	120	ns
(RESET-Q)			15	_	50	100	
			5	3.5	10	_	
Max clock frequency	f_{CL}	_	10	8.0	20	_	MHz
			15	12.0	25	_	
Min clock pulse width			5	_	50	140	
Min clock pulse width	t _W	_	10	_	20	60	ns
(RESET)			15	_	15	40	
			5	_	100	200	
Min pulse width	t _W	_	10	_	40	80	ns
			15	_	30	60	
Min removal time			5	_	_	350	
(RESET-CLOCK)	t _{rem}	_	10	_	_	150	ns
(NESET-OLOGN)			15		_	100	
May alack input rise fire			5				
Max clock input rise time	trCL	_	10	No limit			μS
Max clock input fall time	t _{fCL}		15				
Input capacitance	C _{IN}	_			5	7.5	pF

Operating Supply Current Test Circuit

Waveform 1

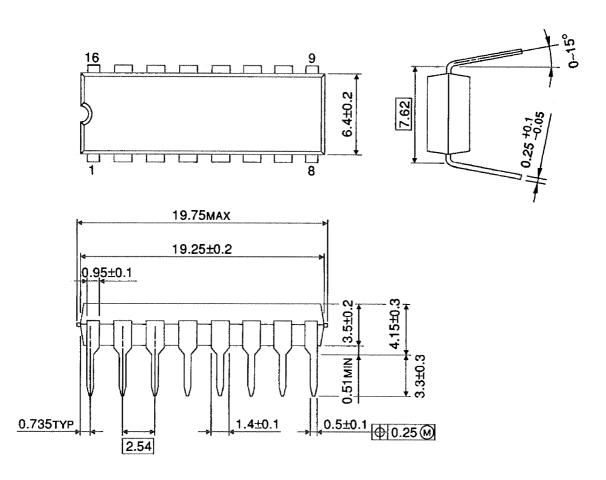


Waveform 2



Package Dimensions

DIP16-P-300-2.54A Unit: mm

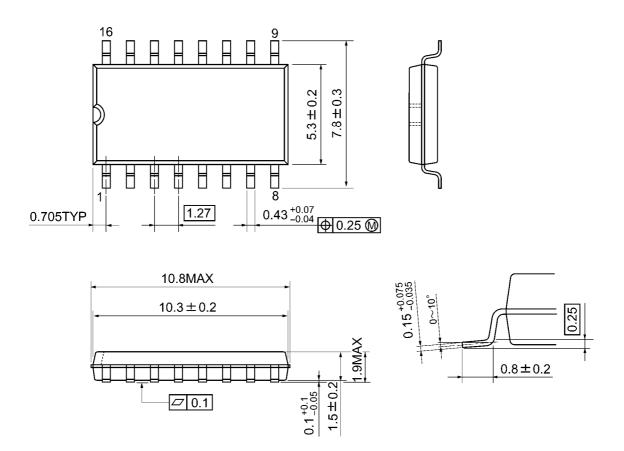


Weight: 1.00 g (typ.)



Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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