TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA76431S

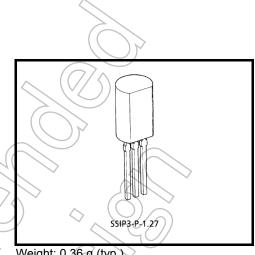
Adjustable Precision Shunt Regulator

Features

- Precision reference voltage: $V_{REF} = 2.495 \text{ V} \pm 2.2\%$
- Small temperature coefficient: | aV_{REF}| = 46 ppm/°C
- Adjustable output voltage: $V_{REF} \le V_{OUT} \le 36 \text{ V}$
- Low dynamic output impedance: $|Z_{KA}| = 0.15 \Omega$ (Typ.)

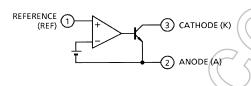
Pin Assignment

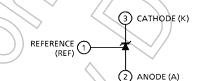




Weight: 0.36 g (typ.)

Functional Block Diagram



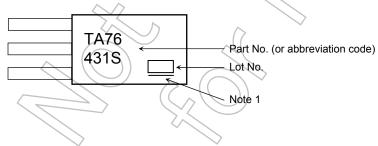


Circuit Symbol

This IC contains electrostatic sensitive elements.

Please handle with caution.

Marking



Note 1: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

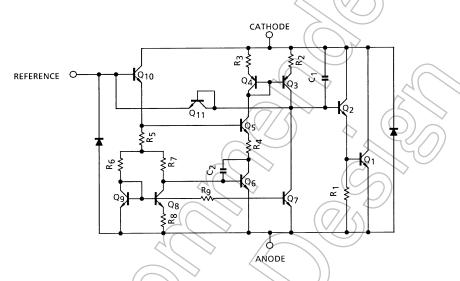
> Start of commercial production 1998-11

How to Order

Product No.	Package Type	Packing Type and Capacity
TA76431S (F)	LSTM	Loose in bag: 200 pcs/bag
TA76431S (TPE6,F)	(lead type)	Radial tape: 2000 pcs/reel

Note 2: The product supplied as TA76431S(TPE6,F) is different from TA76431S(F) in the lead pitch between the terminal.

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Cathode voltage	√VKA	37	V
Cathode current	/k	-100 to 150	mA
Reference voltage	V _{REF}	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	٧
Reference current	I _{REF}	50	μА
Reference-anode reverse current	-I _{REF}	10	mA
Power dissipation Ta = 25°C	PD	800	mW
Operating temperature	Topr	-40 to 85	°C
Storage temperature	Tstg	-55 to 150	°C

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges

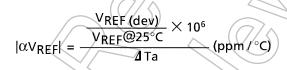
Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V_{KA}	V_{REF}	-	36	V
Cathode current	ΙK	1	-	100	mA
Operating temperature	T _{opr}	-40	-	85	°C

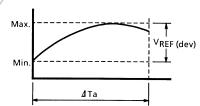
Electrical Characteristics (Unless otherwise specified, Ta = 25°C, I_K = 10 mA)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reference voltage	V_{REF}	V _{KA} = V _{REF}	2.440	2.495	2.550	V
Deviation of reference input voltage over temperature	V _{REF (dev)} (Note 4)	0°C ≤ Ta ≤ 70°C, V _{KA} = V _{REF}) -	8	17	mV
Ratio of change in reference input	^ \/ / ^ \/	V _{REF} ≤ V _{KA} ≤ 10 V	_	0.8	2.7	m)///
voltage to the change in cathode voltage	$\Delta V_{REF}/\Delta V$	10 V ≤ V _{KA} ≤ 36 V		0.5	2.0	mV/V
Reference input current	I _{REF}	V _{KA} = V _{REF}	-	(1.4	(4)	μА
Deviation of reference input current over temperature	I _{REF (dev)} (Note 4)	0°C ≤ Ta ≤ 70°C, V _{KA} = V _{REF} R ₁ = 10 kΩ, R ₂ = ∞	-((0.3	1.2	μА
Minimum cathode current for regulation	I _{Kmin}	V _{KA} = V _{REF}		0.4	1.0	mA
Off-state cathode current	I _{Koff}	V _{KA} = 36 V, V _{REF} = 0 V	(Y_)) –	1.0	μА
Dynamic impedance	IZKAI	$V_{KA} = V_{REF}, f \le 1 \text{ kHz}$ 1 mA $\le I_K \le 100 \text{ mA}$	_	0.15	0.5	Ω

Note 4: The deviation parameters V_{REF} (dev) and I_{REF} (dev) are defined as the maximum variation of the V_{REF} and I_{REF} over the rated temperature range.

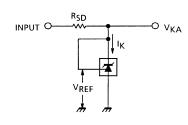
The average temperature coefficient of the V_{REF} is defined as:



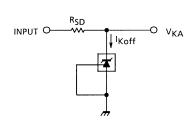


Test Parameter

(1) $V_{KA} = V_{REF}$ mode

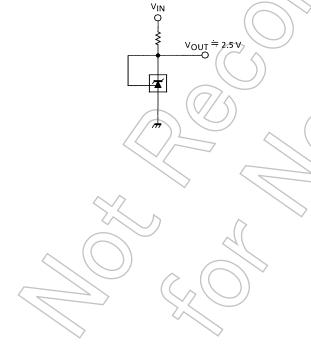


(3) Off-state mode

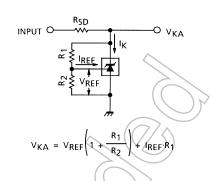


Typical Applications

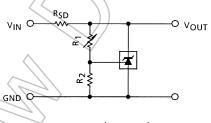
(1) 2.5 V reference



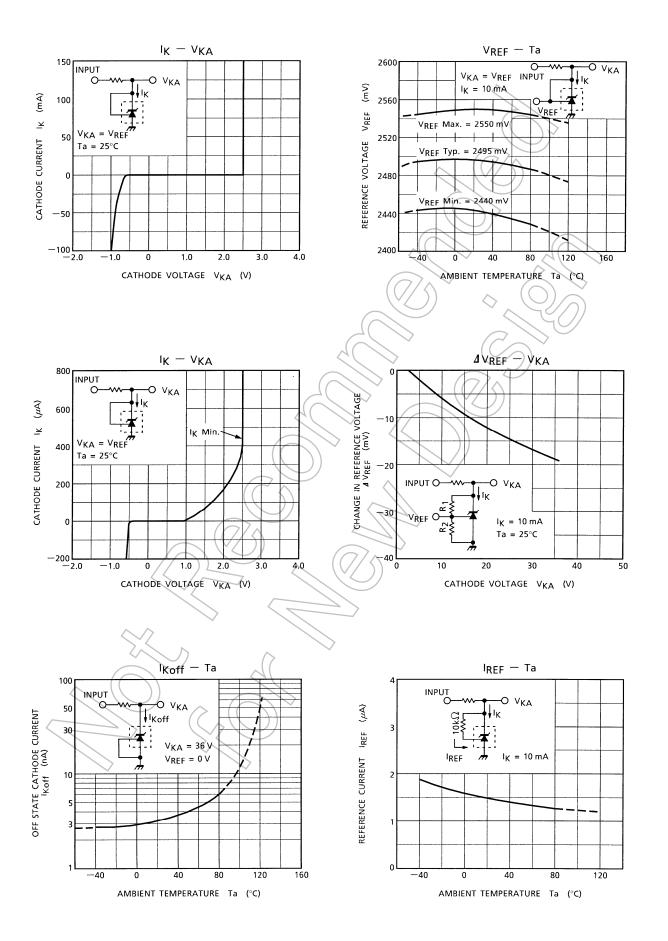
(2) V_{KA} > V_{REF} mode

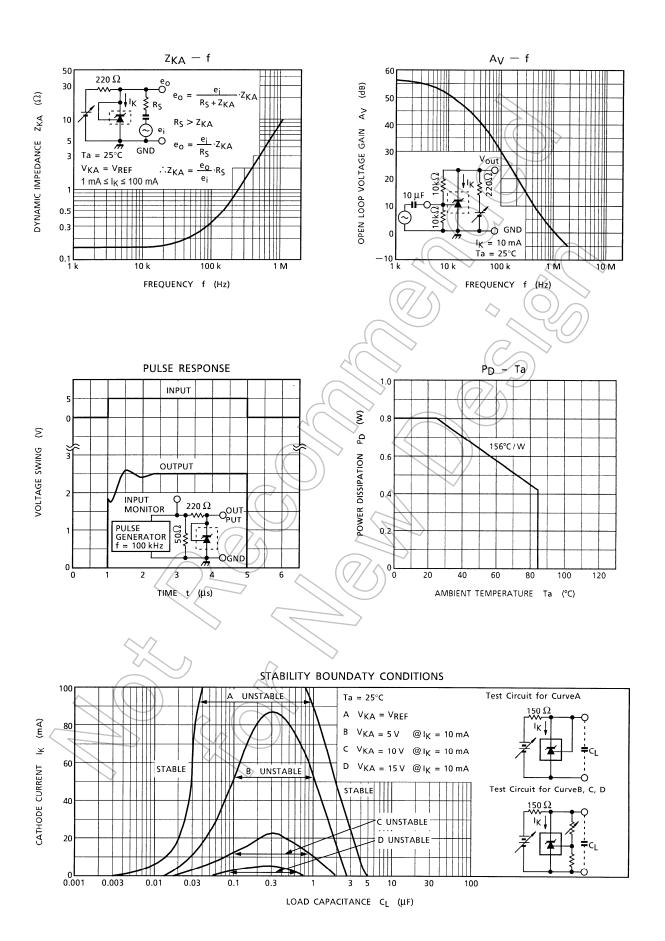






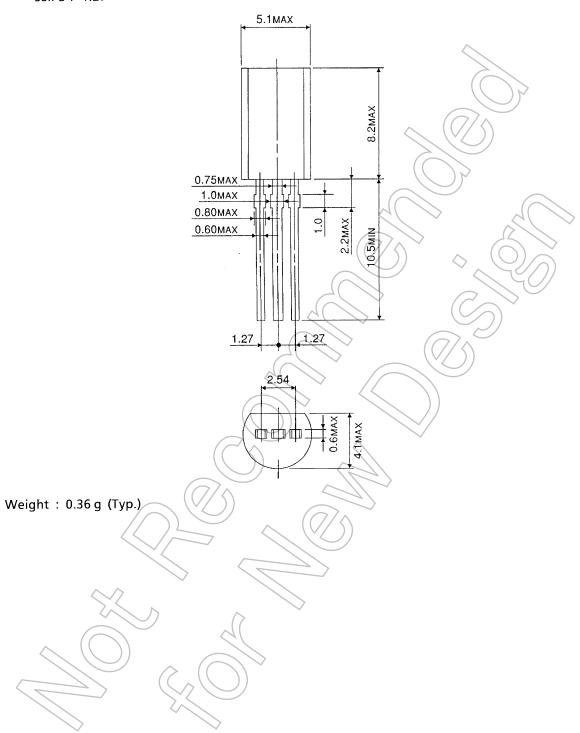
$$V_{OUT} = V_{REF} \left(1 + \frac{R_1}{R_2} \right) + I_{REF} \cdot R_2$$





Package Dimensions

SSIP3-P-1.27 Unit: mm



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