TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA4004F

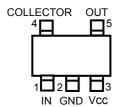
VHF~UHF Wide Band Amplifier

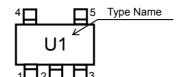
Features

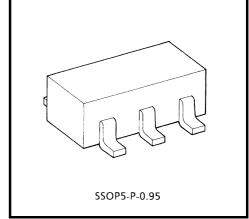
- Band width: 1.2 GHz (typ.) (3dB down, VCC = 2 V)
- High gain: $|S_{21}|^2 = 10.5 dB$ (typ.) (f = 500 MHz, $V_{CC} = 2 V$)
- Operating supply voltage: $V_{CC} = 2$ to 5 V
- Low current operation: ICC = 3.1 mA (typ.) (VCC = 2 V)
- Small package

Pin Assignment (top view)









Weight: 0.014 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	6	V
Total power dissipation	P _D (Note 1)	300	mW
Operating temperature	T _{opr}	−40 to 85	°C
Storage temperature	T _{stg}	−55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: When mounted glass epoxy of 2.5 cm² × 1.6 t



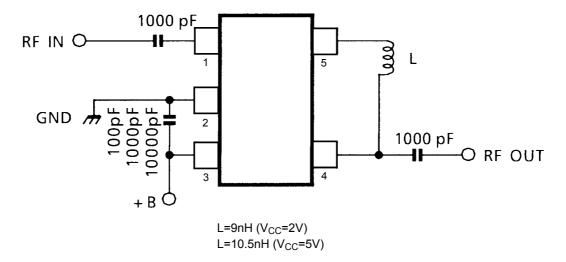
Electrical Characteristics (Ta = 25°C) (Note2)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Circuit current	Icc	_	V _{CC} = 2 V, non carrier	2.5	3.1	4	mA	
			V _{CC} = 5 V, non carrier	10	12.5	16		
Insertion gain	S ₂₁ ²	1	V _{CC} = 2 V, f = 500 MHz	8.5	10.5	13.5	dB	
			V _{CC} = 5 V, f = 500 MHz	13	15	18	ub	
Band width	BW	1	$V_{CC} = 2 V$ (Note 3)	0.9	1.2	_	- GHz	
			$V_{CC} = 5 V$ (Note 3)	0.7	1	_		
Noise figure	NF	1	V _{CC} = 2 V, f = 500 MHz	1	4.2	6	dB	
			V _{CC} = 5 V, f = 500 MHz	_	4.7	6.5	UD	
Input return loss	S ₁₁ ²	1	V _{CC} = 2 V, f = 500 MHz	_	-7	_	- dB	
			V _{CC} = 5 V, f = 500 MHz	_	-9	_		
Output return loss	S ₂₂ ²	1	V _{CC} = 2 V, f = 500 MHz	_	-7	_	dB	
			V _{CC} = 5 V, f = 500 MHz	_	-9	_	ub	
Isolation	S ₁₂ ²	1	V _{CC} = 2 V, f = 500 MHz	_	-23	_	dB	
			V _{CC} = 5 V, f = 500 MHz	_	-24		UD	
Maximum output level	Po	1	V _{CC} = 2 V, f = 500 MHz, P _{in} = 0dBmW	_	0	_	dBmW	
				V _{CC} = 5 V, f = 500 MHz, P _{in} = 0dBmW	_	8	_	UDIIIVV

Note 2: Have use for connect inductance between terminal 4 and 5 $\,$ 9 nH at V_{CC} = 2 V, 10.5 nH at V_{CC} = 5 V

Note 3: BW is frequency of 3dB down from $\left|S_{21}\right|^2$ at 500 MHz.

Test Circuit 1 (top view)



Notice

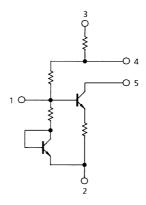
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

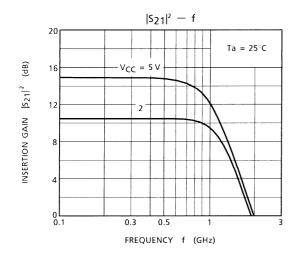
Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

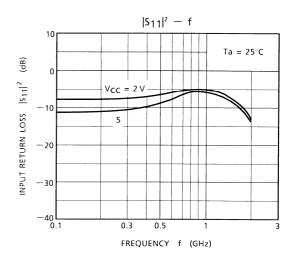
It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

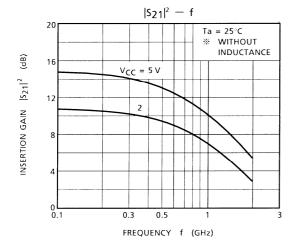
TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

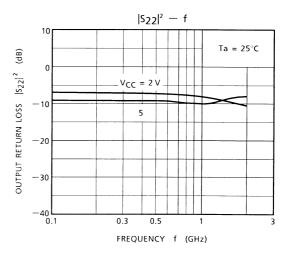
Equivalent Circuit

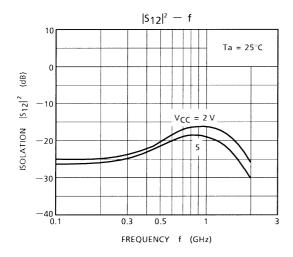


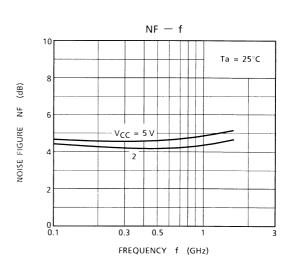


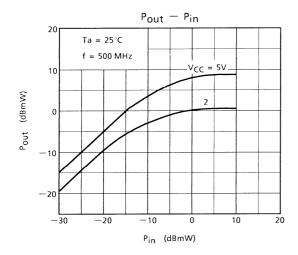


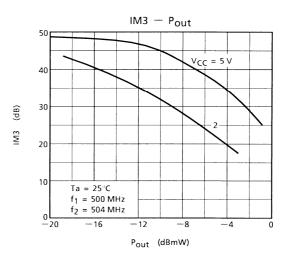


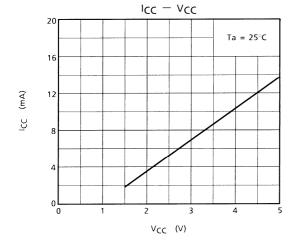










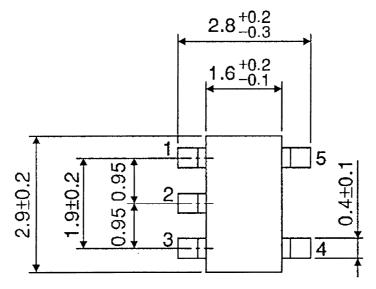


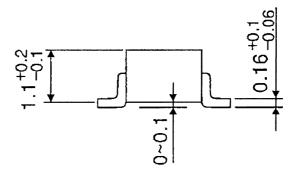
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Package Dimensions

SSOP5-P-0.95 Unit: mm





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Weight: 0.014 g (Typ.)

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