TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM1G43,SM1J43

AC POWER CONTROL APPLICATIONS

Repetitive Peak Off-State Voltage : VDRM = 400, 600V

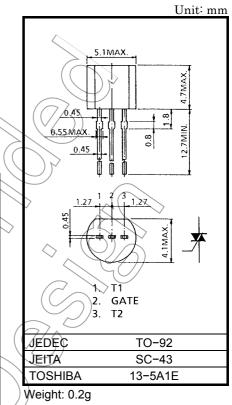
- R.M.S On-State Current

: IT (RMS) = 1A

Higt Commutating (dv / dt)

ABSOLUTE MAXIMUM RATINGS

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	//
Repetitive Peak	SM1G43	VDRM	400		
Off-State Voltage	SM1J43	VDRM	600	$(// \uparrow)$	\sim
R.M.S On-State Current (Full Sine Waveform Tc = 74°C)		I _{T (RMS)}	1.0		
Peak One Cycle Surge Current (Non-Repetitive		ITSM	8 (50Hz) 8.8 (60Hz)	A A	
I ² t Limit Value		l ² t	0.32	A ² s	
Peak Gate Power Dissi	pation	P _{GM}		W	/
Average Gate Power D	issipation	PG (AV)	0.1	/ (w	
Peak Gate Voltage		VGM	6	×	
Peak Gate Current		IGM	0.5	Α	\geq
Junction Temperature			-40~125	°C	
Storage Temperature R	Range	T _{stg}	-40~125)°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

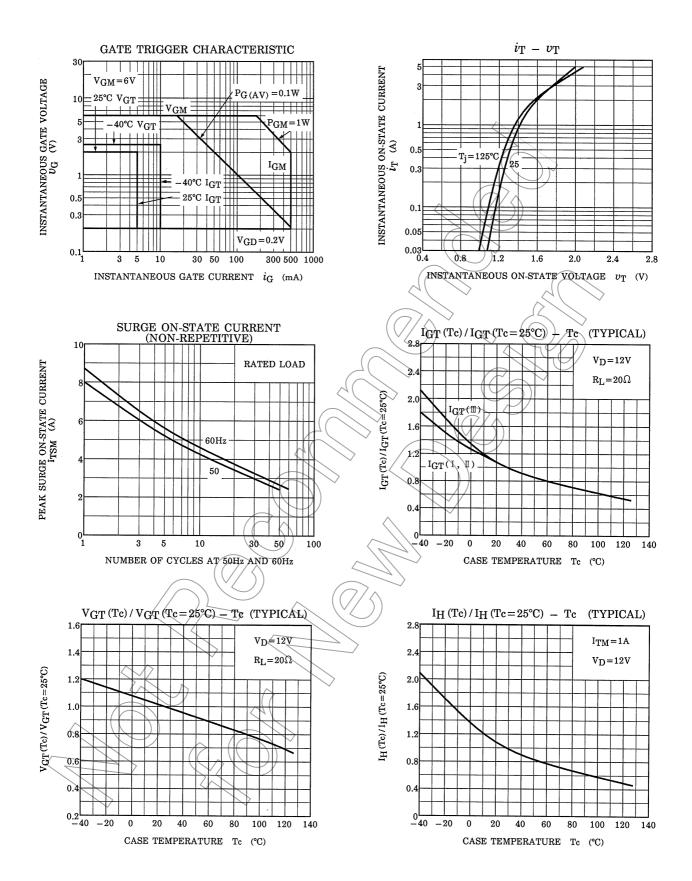


ELECTRICAL CHARACTERISTICS (Ta = 25°C)

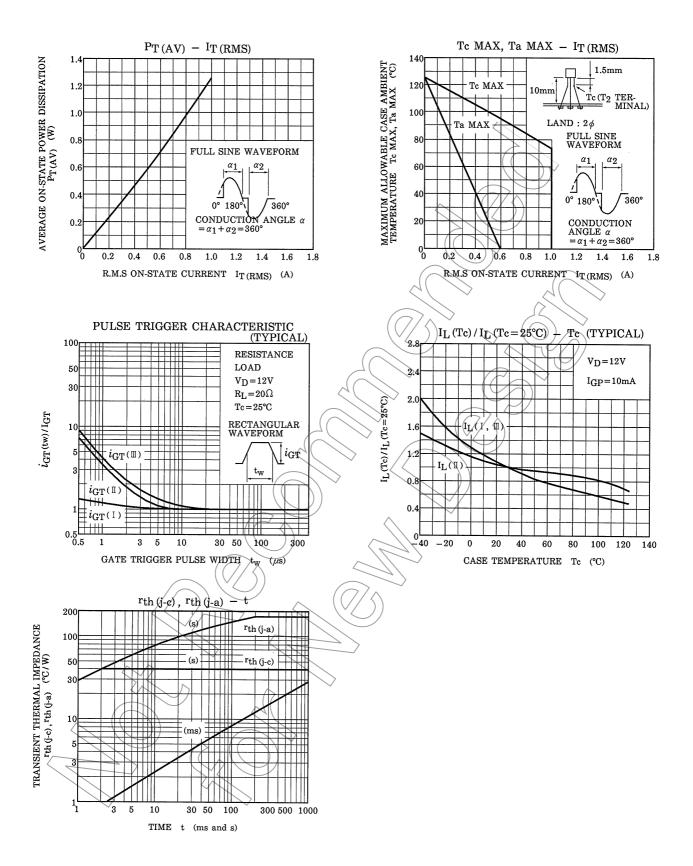
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		IDRM	V _{DRM} = Rated		_	_	10	μA
Gate Trigger Voltage	I	V _{GT}	V _D = 12V, R _L = 20Ω	T2 (+) , Gate (+)	_	_	2	V
	П			T2 (+) , Gate (−) <	$\overline{\langle}$	_	2	
				T2 (-) , Gate (-)	$\langle \rangle$	-	2	
	IV			T2 (-) , Gate (+)	Æ) >2	_	
Gate Trigger Current	I	- I _{GT}	V _D = 12V, R _L = 20Ω	T2 (+) , Gate (+)	\sum	_	5	mA
	П			T2 (+), Gate (-)	\bigcirc	_	5	
	Ш			T2 (-) , Gate (-)		_	5	
	IV			T2 (-) , Gate (+)	_	10		
Peak On-State Voltage		V _{TM}	I _{TM} = 1.5A		_	\square	1.5	V
Gate Non-Trigger Voltage		V _{GD}	V _D = Rated, Tc = 125°C		0.2	4	\searrow	V
Holding Current		Ι _Η	V _D = 12V, I _{TM} = 1A		-6	$\langle - \rangle$	> 10	mA
Thermal Resistance Rth		R _{th (j−c)}	Junction to Case, AC		$\sim c$	2A) 40	°C / W
Thermal Resistance R _{th (j-a)}		R _{th (j−a)}	Junction to Ambient, AC		\mathcal{A}	GC/	180	°C / W
ARKING				, (C	$\tilde{\langle}$	\checkmark		

MARKING

	-			77.	
	NUMBER		SYMBOL	\bigcirc	MARK
	*1	TYPE	SM1G43		M1G43
	I		SM1J43		M1J43
	*2	Year (1	(Starting from Alphabet A Cast Decimal Digit of the Current Year)	Example 8A : January 1998 8B : February 1998 8L : December 1998
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$\langle () \rangle$					



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RESTRICTIONS ON PRODUCT USE

• The information contained herein is subject to change without notice.

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