TOSHIBA Diode Silicon Epitaxial Planar Type

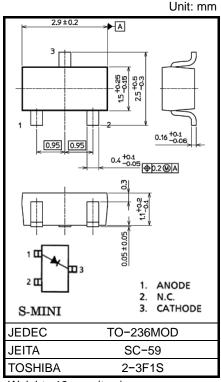
1SS250

Ultra High Speed Switching Application

- Low forward voltage $: V_F (2) = 0.90V (typ.)$
- Fast reverse recovery time: $t_{rr} = 60$ ns (max)
- $: C_{T} = 1.5 pF (typ.)$ Small total capacitance
- Small package : SC-59

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Peak reverse voltage	V _{RM}	250	V
Reverse voltage	VR	200	V
Peak forward current	IFM	300	mA
Average forward current	lo	100	mA
Surge current (10 ms)	IFSM	2	А
Power dissipation	Р	150	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55 to 125	°C



Weight: 12 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

> Start of commercial production 1984-05

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	IF = 10mA	_	0.72	1.00	V
	VF (2)	I _F = 100mA	_	0.90	1.20	
Reverse current	I _{R (1)}	V _R = 50V	_	_	0.1	μΑ
	IR (2)	V _R = 200V	_	—	1.0	
Total capacitance	Ст	$V_R = 0V$, f = 1MHz	_	1.5	3.0	pF
Reverse recovery time	t _{rr}	I _F = 10mA (Fig.1)	_	10	60	ns

Marking

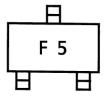
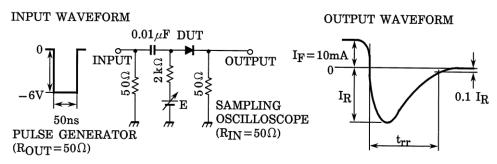
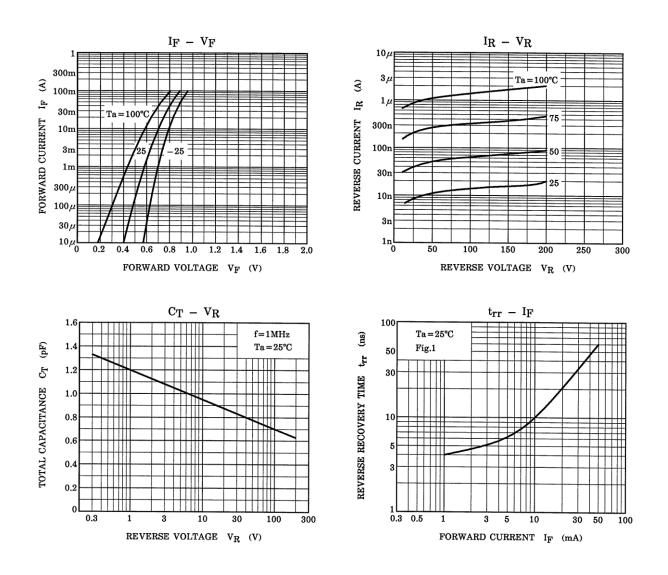


Fig.1 Reverse recovery time (trr) test circuit



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