

# High Speed ADC to HSMC (Altera) Header Adaptor

Card Status: ACTIVE

HSMC-ADC-BRIDGE



## Description

The HSMC-ADC-Bridge passive interconnect board enables the output of TI's LVDS output high speed ADCs to be directly connected to a standard HSMC interconnect header, a typical input on the latest Altera FPGA EVMs. This enables users of TI's high speed data converter EVMs to directly interface to Altera FPGA's for prototyping purposes, saving the time and cost of producing a custom prototyping board.

## Features

- Enables direct connection from TI high speed ADC EVMs with LVDS outputs to the HSMC standard header

## What's Included

- SLOR100, HSMC-ADC-Bridge Physical Design Database. (Rev. A)