

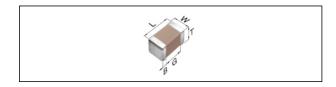
## **☆TDK** Multilayer Ceramic Chip Capacitors

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Creation Date: March 17, 2013

### C1608X7R1A105K080AC

**Applications** Commercial Grade Feature General General (Up to 50V) C1608 [EIA CC0603] Series







Size	
Length(L)	1.60mm +0.15/-0.10mm
Width(W)	0.80mm +0.15/-0.10mm
Thickness(T)	0.80mm +0.15/-0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.

Electrical Characteristics	
Capacitance	1uF +/-10%
Rated Voltage	10Vdc
Temperature Characteristic	X7R(+/-15%)
Dissipation Factor	5% Max.
Insulation Resistance	100MΩ Min.

Other	
Soldering Method	Reflow, Flow
AEC Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000 Pcs Min.

<sup>•</sup>This PDF document was created based on the data listed on the TDK Corporation website.

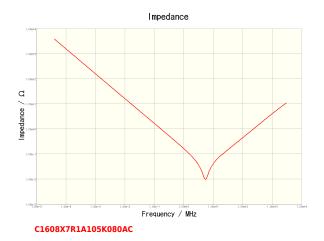
<sup>•</sup>All specifications are subject to change without notice.

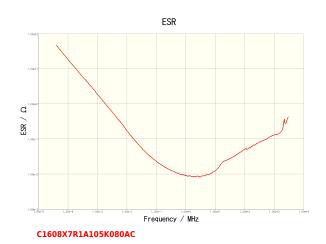
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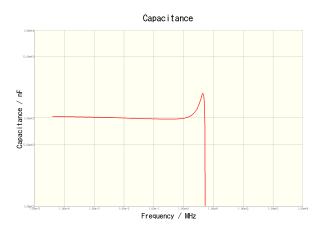
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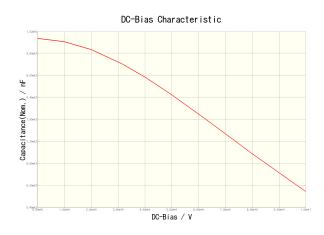
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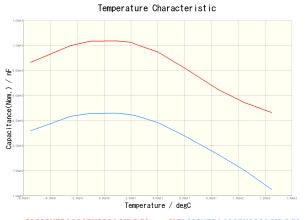
### **Characteristic Graphs**

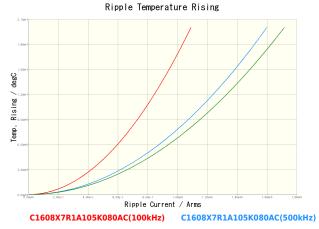












C1608X7R1A105K080AC(DC Bias = 0V)1608X7R1A105K080AC(DC Bias = 5V)

C1608X7R1A105K080AC(1MHz)

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