

# **Surface Mount Super Fast Rectifiers**

#### **FEATURES**

- Low forward voltage drop
- Ideal for automated placement
- High current capability
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

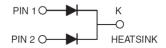
# MECHANICAL DATA

Case: TO-263AB (D<sup>2</sup>PAK)

Molding compound, UL flammability classification rating 94V-0 Base P/N with suffix "G" on packing code - green compound (halogen-free) Base P/N with prefix "H" on packing code - AEC-Q101 qualified **Terminal:** Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test with prefix "H" on packing code meet JESD 201 class 2 whisker test **Polarity:** As marked **Weight:** 1.37 g (approximately)



TO-263AB	(D <sup>2</sup> PAK)
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		SFS	SFS	SFS	SFS	SFS	SFS	SFS	SFS	
PARAMETER	SYMBOL	1001	1002	1003	1004	1005	1006	1007	1008	Unit
		G	G	G	G	G	G	G	G	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I <sub>F(AV)</sub>				1	0				А
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>				12	25				А
Maximum instantaneous forward voltage (Note 1) $I_F$ = 5 A	V <sub>F</sub>		0.9	975		1	.3	1	.7	V
Maximum reverse current @ rated VR T <sub>J</sub> =25 $^{\circ}$ C T <sub>J</sub> =125 $^{\circ}$ C	I <sub>R</sub>	1 200		μA						
Maximum reverse recovery time (Note 2)	Trr				3	5				ns
Typical junction capacitance (Note 3)	Cj		7	0			5	60		pF
Typical thermal resistance	$R_{ extsf{ heta}JC}$					2				<sup>o</sup> C/W
Operating junction temperature range	TJ				- 55 to	o +150	_	_		О <sup>о</sup>
Storage temperature range	T <sub>STG</sub>				- 55 to	o +150				ос

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.



# SFS1001G thru SFS1008G

**Taiwan Semiconductor** 

## **ORDERING INFORMATION**

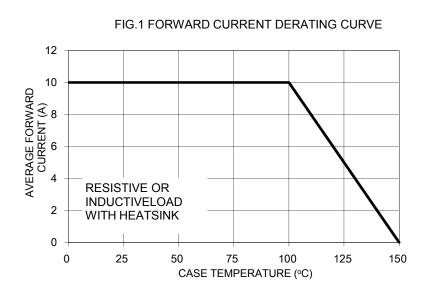
PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	PACKAGE	PACKING
SFS100xG	Prefix "H"	RN	Suffix "G"	D <sup>2</sup> PAK	800 / 13" Paper reel
(Note 1)		C0	Sullix G	D <sup>2</sup> PAK	50 / Tube

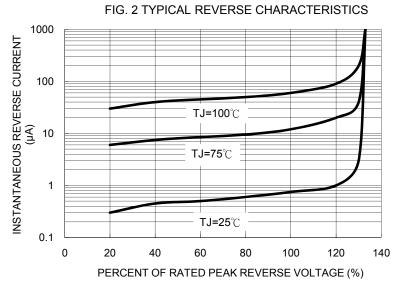
Note 1: "x" defines voltage from 50V (SFS1001G) to 600V (SFS1008G)

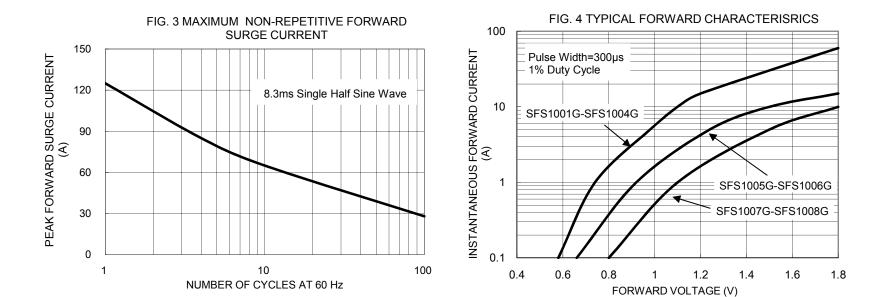
EXAMPLE						
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION	
SFS1008G RN	SFS1008G		RN			
SFS1008G RNG	SFS1008G		RN	G	Green compound	
SFS1008GHRN	SFS1008G	Н	RN		AEC-Q101 qualified	

# **RATINGS AND CHARACTERISTICS CURVES**

(TA=25 $^{\circ}$ C unless otherwise noted)







#### Document Number: DS\_D1405076



# SFS1001G thru SFS1008G

+0.5A

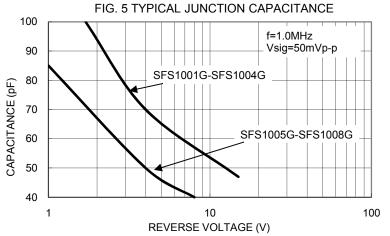
0

-0.25A

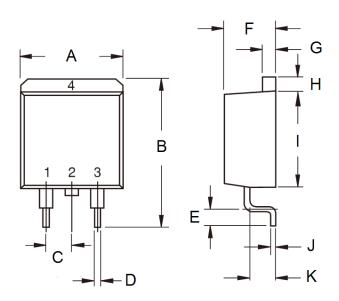
-1.0A

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## PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min	Min Max		Max	
А	-	10.5	-	0.413	
В	14.60	15.88	0.575	0.625	
С	2.41	2.67	0.095	0.105	
D	0.68	0.94	0.027	0.037	
E	2.29	2.79	0.090	0.110	
F	4.44	4.70	0.175	0.185	
G	1.14	1.40	0.045	0.055	
Н	1.14	1.40	0.045	0.055	
I	8.25	9.25	0.325	0.364	
J	0.36	0.53	0.014	0.021	
К	2.03	2.79	0.080	0.110	

FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

(-)

(+)

PULSE GENERATOR (NOTE 2)

1

50Ω NONINDUCTIVE

W

(+) 50Vdc (approx) (-)

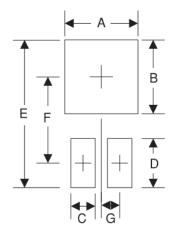
DUT

10Ω NONINDUCTIVE

NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf 2. Rise Time=10ns max. Sourse Impedance= 50 ohms

(NOTE 1)

## SUGGESTED PAD LAYOUT



P/N

YWW

G

F

Symbol	Unit (mm)	Unit (inch)
А	10.8	0.425
В	8.3	0.327
С	1.1	0.043
D	3.5	0.138
E	16.9	0.665
F	9.5	0.374
G	2.5	0.098

## **MARKING DIAGRAM**



- = Specific Device Code
- = Green Compound
- = Date Code
- = Factory Code

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