

3A, 50V - 600V Glass Passivated Super Fast Rectifier

FEATURES

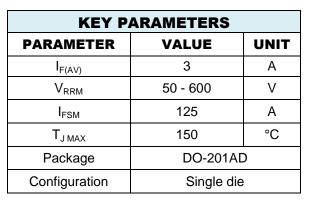
- Glass passivated chip junction
- High efficiency, Low V_F
- High reliability
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- TV
- Monitor

MECHANICAL DATA

- Case: DO-201AD
- Molding compound meets UL 94V-0 flammability rating
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: As marked
- Weight: 1.1 g (approximately)











ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	SF 31G-A	SF 32G-A	SF 33G-A	SF 34G-A	SF 35G-A	SF 36G-A	SF 37G-A	SF 38G-A	UNIT
Marking code on the device		SF31G	SF32G	SF33G	SF34G	SF35G	SF36G	SF37G	SF38G	
Repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	105	140	210	280	350	420	V
Forward current	I _{F(AV)}		3					А		
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load per diode	I _{FSM}		125				A			
Junction temperature	TJ	- 55 to +150				°C				
Storage temperature	T _{STG}	- 55 to +150			°C					



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THERMAL PERFORMANCE						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-lead thermal resistance per diode	R _{ƏJL}	10	°C/W			
Junction-to- ambient thermal resistance per diode	R _{OJA}	35	°C/W			
Junction-to-case thermal resistance per diode	R _{eJC}	9	°C/W			

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
	SF31G-A					
	SF32G-A		VF	_	0.95	v
	SF33G-A			-	0.95	v
Forward voltage per diode (1)	SF34G-A					
Forward voltage per diode	SF35G-A	I _F = 3A,T _J = 25°C		_	1.30	v
	SF36G-A			_	1.50	v
	SF37G-A			_	1.70	v
	SF38G-A			-	1.70	v
		$T_J = 25^{\circ}C$		-	5	μA
Reverse current @ rated V_R per di	ode	T _J = 125°C	I _R	-	100	μA
	SF31G-A		0	80	-	pF
	SF32G-A					
	SF33G-A					
	SF34G-A					
Junction capacitance	SF35G-A	1 MHz, V _R =4.0V	CJ			
	SF36G-A			60	-	pF
	SF37G-A			60		
	SF38G-A					
Reverse recovery time		I _F =0.5A , I _R =1.0A	t _{rr}	-	35	ns
		I _{RR} =0.25A				

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms

ORDERING INFORMATION					
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING	
SF3xG-A (Note 1, 2)	A0		DO-201AD	500 / Ammo box	
	R0	G	DO-201AD	1,250 / 13" Paper reel	
	В0		DO-201AD	500 / Bulk packing	

Notes:

1. "x" defines voltage from 50V (SF31G-A) to 600V (SF38G-A)

2. Whole series with green compound (halogen-free)

EXAMPLE P/N				
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
SF31G-A A0G	SF31G-A	AO	G	Green compound



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

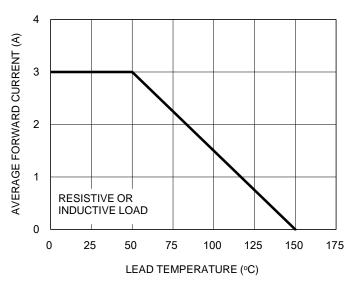


Fig.1 Forward Current Derating Curve

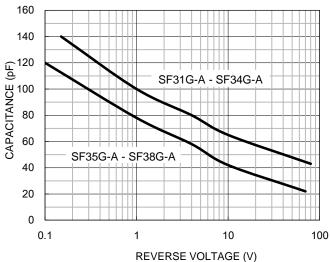
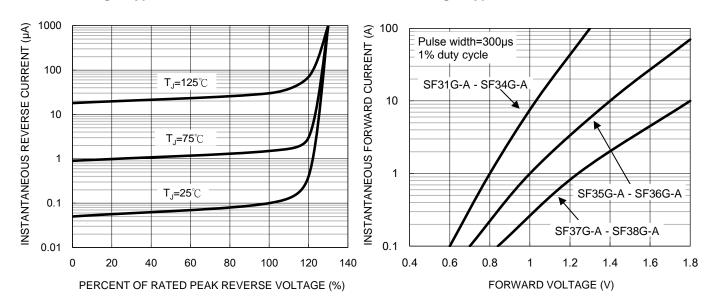


Fig.2 Typical Junction Capacitance

Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics





CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.5 Maximum Non-repetitive Forward Surge Current

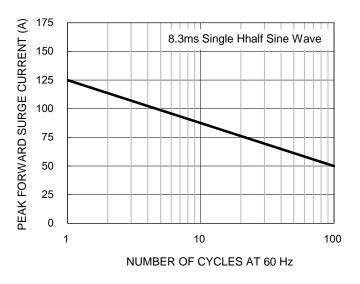
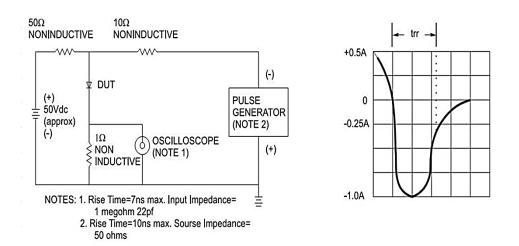


Fig.6 Reverse Recovery Time Characteristic And Test Circuit Diagram

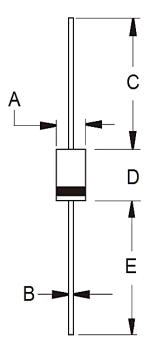






PACKAGE OUTLINE DIMENSIONS

DO-201AD



DIM.	Unit (ı	nm)	Unit (inch)		
Dilvi.	Min	Max	Min	Max	
А	5.00	5.60	0.197	0.220	
В	1.20	1.30	0.048	0.052	
С	25.40	-	1.000	-	
D	8.50	9.50	0.335	0.375	
Е	25.40	-	1.000	-	

MARKING DIAGRAM



P/N	= Marking Code
G	= Green Compound

- YWW = Date Code
- F = Factory Code



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