I _{FSM}	2	А
V _F at I _F =100mA	1.00	V
T _{J MAX}	175	°C
Package	LS34	
Configuration	Single dice	

KEY PARAMETERS

PARAMETER

I_{F(AV)}

V_{RRM}

Taiwan Semiconductor

VALUE

450

75

HALOGEN

UNIT

mΑ

V

450mA, 75V Switching Diode

FEATURES

- Low power loss, high efficiency
- Ideal for automated placement
- High surge current capability
- Compliance to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- On-board DC/DC converter

MECHANICAL DATA

- Case: QUADRO Mini-MELF(LS34)
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Polarity: Indicated by cathode band
- Weight: 0.06 (approximately)



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)				
PARAMETER SYMBOL PART NUMBER				
Repetitive peak reverse voltage	V _{RRM}	75	V	
Forward current	I _{F(AV)}	450	mA	
Peak Forward Surge Current	I _{FSM}	2	А	
Junction temperature range	TJ	-65 ~ 175	°C	
Storage temperature range	T _{STG}	-65 ~ 175	°C	

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient thermal resistance	R _{eja}	300	°C/W	



ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS SYME		ТҮР	MAX	UNIT
	LS4448, LS914B I _F = 5.0mA, T _J = 25°C			0.72	V
Forward voltage per diode (1)	LS4148 I _F = 10.0mA, T _J = 25°C	V _F		1.0	
	LS4448, LS914B I _F = 100mA, T _J = 25°C	-		1.0	
Reverse current @ rated V _R per	V_{R} =20V T_{J} = 25°C			25	nA
diode ⁽²⁾	V_{R} =75V T_{J} = 25°C	I _R		5	μA
Junction capacitance	1 MHz, V _R =0V	CJ		4	ρF

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms

ORDERING INFORMATION				
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING
LS4148	LO	C C	QUADRO	10K / 13" Reel
(Note 1)	L1	G	Mini-MELF	2.5K / 7" Reel

Notes:

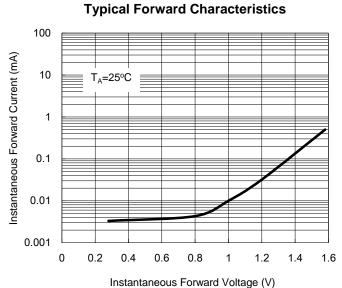
1. Whole series with green compound

EXAMPLE				
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
LS4148 L0G	LS4148	LO	G	Green compound



CHARACTERISTICS CURVES

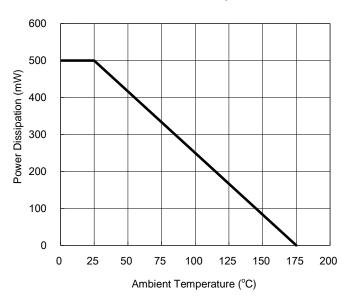
(T_A = 25°C unless otherwise noted)



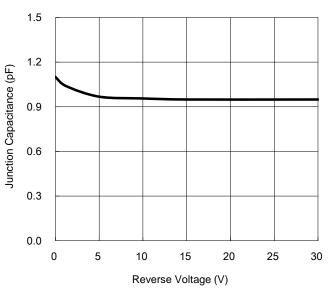
100 T_A=25°C 10 Reverse Current (uA) 1.0 1 0.01 40 120 0 20 60 80 100 Reverse Voltage (V)

Reverse Current VS. Reverse Voltage

Admissible Power Dissipation Curve



Typical Junction Capacitance

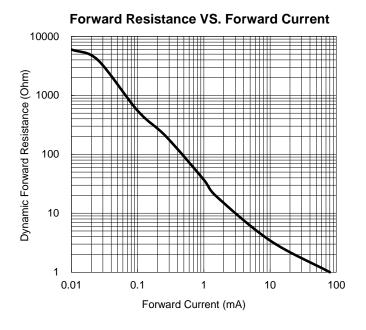




Taiwan Semiconductor

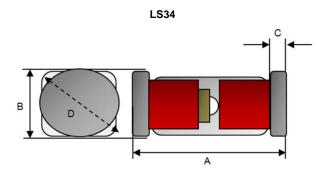
CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$



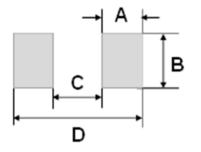


PACKAGE OUTLINE DIMENSION



DIM.	Unit(mm)		Unit(inch)	
DIN.	Min	Max	Min	Max
A	3.30	3.70	0.130	0.146
В	1.40	1.60	0.055	0.063
С	0.20	0.45	0.008	0.018
D	Тур. 1.80		Тур. (0.071

SUGGEST PAD LAYOUT



DIM.	Unit(mm)	Unit(inch)	
DIM.	Тур.	Тур.	
А	1.25	0.049	
В	2.00	0.079	
С	2.50	0.098	
D	5.00	0.197	



LS4148/LS4448/LS914B

Taiwan Semiconductor

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.