

Taiwan Semiconductor

2A, 400V - 1000V Glass Passivated Bridge Rectifier

FEATURES

- Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application

MECHANICAL DATA

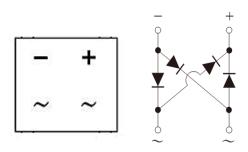
- Case: DBL
- Molding compound :meets UL 94V-0 flammability rating
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: As marked
- Weight: 0.36 g (approximately)

KEY PA	RAMETER	S
PARAMETER	VALUE	UNIT
I _{F(AV)}	2	А
V _{RRM}	400 - 1000	V
I _{FSM}	50	А
T _{J MAX}	150	°C
Package	DBL	
Configuration	Quad	ł









ABSOLUTE MAXIMUM RATING	S (T _A = 25°C u	Inless other	vise noted)			
PARAMETER	SYMBOL	DBL204	DBL205	DBL206	DBL207	UNIT
	STMBOL	G-T	G-T	G-T	G-T	
Marking code on the device		DBL204G	DBL205G	DBL206G	DBL207G	
Repetitive peak reverse voltage	V _{RRM}	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	400	600	800	1000	V
Forward current	I _{F(AV)}		2	2		А
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}		5	0		А
I ² t value (of a surge on-state current)	l ² t		10).3		A ² s
Junction temperature	TJ		-55 to	+150		°C
Storage temperature	T _{STG}		-55 to	+150		°C



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THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-lead thermal resistance	R _{⊖JL}	15	°C/W
Junction-to-ambient thermal resistance	R _{eja}	40	°C/W

ELECTRICAL SPECIFICATIO	NS (T _A = 25°C unless of	herwise noted)			
PARAMETER	CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Forward voltage ⁽¹⁾	$I_F = 2A, T_J = 25^{\circ}C$	V _F	-	1.15	V
Reverse current @ rated V _R ⁽²⁾	$T_J = 25^{\circ}C$		-	2	μA
Reverse current @ fated V _R	T _J = 125°C	- I _R	-	500	μA

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms.

ORDERING INF	ORMATION			
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING
DBL20xG-T (Note 1, 2)	СВ	G	DBL	50 / TUBE

Notes:

1. "x" defines voltage from 400V (DBL204G-T) to 1000V (DBL207G-T)

2. Whole series with green compound (halogen-free)

EXAMPLE P/N				
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
DBL204G-T CBG	DBL204G-T	СВ	G	Green compound



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CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

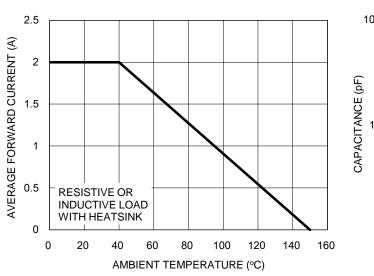


Fig.1 Forward Current Derating Curve Fig.2 Typical Junction Capacitance

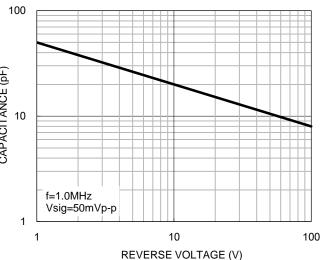
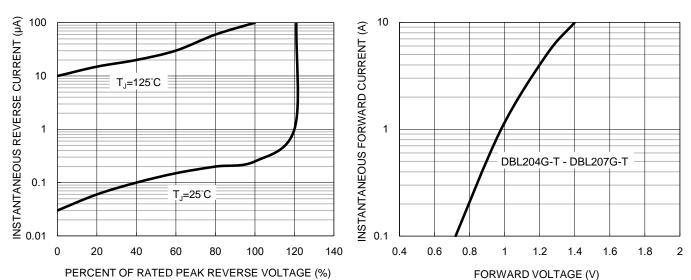


Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics

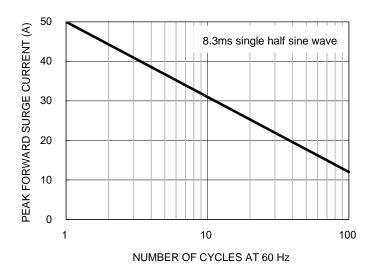




CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.5 Maximum Non-repetitive Forward Surge Current

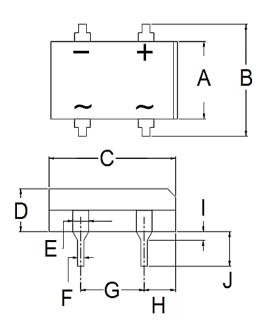


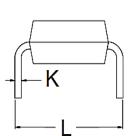


DBL204G-T - DBL207G-T Taiwan Semiconductor

PACKAGE OUTLINE DIMENSIONS







DIM.	Unit (mm) Unit (inc		inch)	
DIM.	Min.	Max.	Min.	Max.
А	6.20	6.50	0.244	0.256
В	7.24	8.00	0.285	0.315
С	8.12	8.51	0.320	0.335
D	2.35	2.60	0.093	0.102
Е	0.89	1.14	0.035	0.045
F	0.46	0.58	0.018	0.023
G	5.00	5.20	0.197	0.205
Н	1.39	1.90	0.055	0.075
I	1.27	2.03	0.050	0.080
J	3.81	4.69	0.150	0.185
К	0.22	0.33	0.009	0.013
L	7.60	8.90	0.299	0.350

MARKING DIAGRAM



P/N = Marking Code

G = Green Compound

YW = Date Code

= Factory Code F



DBL204G-T - DBL207G-T

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