-200mA / -30V Low Vce (sat) Digital transistors (with built-in resistors)

DTB743XE / DTB743XM

Applications

Inverter, Interface, Driver

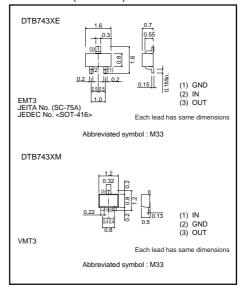
● Feature

- 1) VCE(sat) is lower than the conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

● Dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits DTB743XE DTB743XM	Unit
Supply voltage	Vcc	-30	V
Input voltage	Vin	-20 to +7	V
Collector current *1	IC (max)	-200	mA
Power dissipation *2	Po	150	mW
Junction temperature	Tj	150	ဗ
Storage temperature	Tstg	-55 to +150	င

*1 Characteristics of built-in transistor. *2 Each terminal mounted on a recom

Packaging specifications

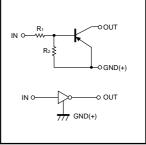
	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code		T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTB743XE		0	-
DTB743XM		-	0

●Electrical characteristics (Ta=25°C)

	(-,			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-	_	-0.3	٧	Vcc= -5V, Io= -100μA
	V _{I(on)}	-2.5	-	-		Vo=-0.3V, Io=-20mA
Output voltage	Vo(on)	-	-70	-300	mV	Io/I=-50mA / -2.5mA
Input current	lı	-	-	-1.4	mA	VI= −5V
Output current	IO(off)	-	-	-0.5	μА	Vcc=-30V, Vi=0V
DC current gain	Gı	140	_	-	-	Vo=-2V, Io=-100mA
Transition frequency *	f⊤	-	260	-	MHz	Vce=-10V, Ie=5mA, f=100MHz
Input resistance	R ₁	3.29	4.7	6.11	kΩ	-
Resistance ratio	R2/R1	1.7	2.1	2.6	-	

^{*} Characteristics of built-in transistor.

●Equivalent circuit



 $R_1=4.7k\Omega / R_2=10k\Omega$

^{*2} Each terminal mounted on a recommended land

•Electrical characteristics curves

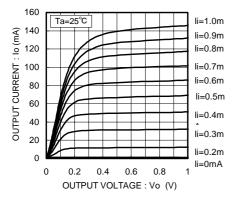


Fig.1 Output Current vs. Output Voltage

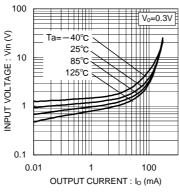


Fig.2 Input Voltage vs. Output Current

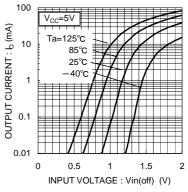


Fig.3 Output Current vs. Input Voltage

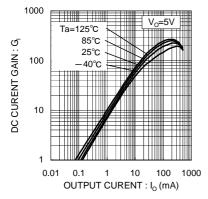


Fig.4 DC Current Gain vs. Output Current

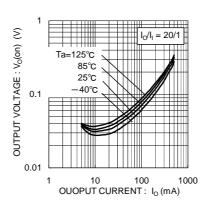


Fig.5 Output Voltage vs. Output Current

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