

RKR0503AKJ

Silicon Schottky Barrier Diode for Rectifying

REJ03G1890-0100 Rev.1.00 Jan 20, 2010

Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- Ultra small Flat Lead Package (UFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)				
RKR0503AKJ P	C8	UFP	PWSF0002ZA-A	P (4,000 pcs / reel)				
RKR0503AKJ R	0		FW3F0002ZA-A	R (8,000 pcs / reel)				
Pin Arrangement	t		Cel di					
Cathode mark Mark 1 C 8 2 1. Cathode 2. Anode								
•	20°C	ne						

Absolute Maximum Ratings

$(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	30	V
Average rectified current	lo * ¹	0.5	А
Non-Repetitive peak forward surge current	I _{FSM} * ²	2	А
Junction temperature	Тј	125	°C
Storage temperature	Tstg	–55 to +125	°C

Notes: 1. See from Fig.4 to Fig.7.

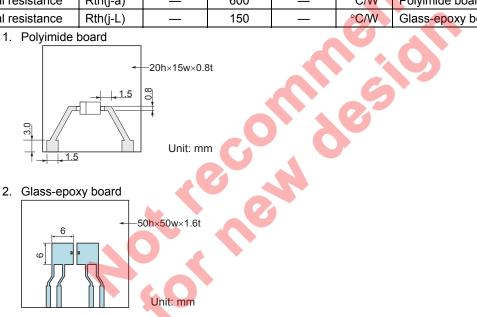
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

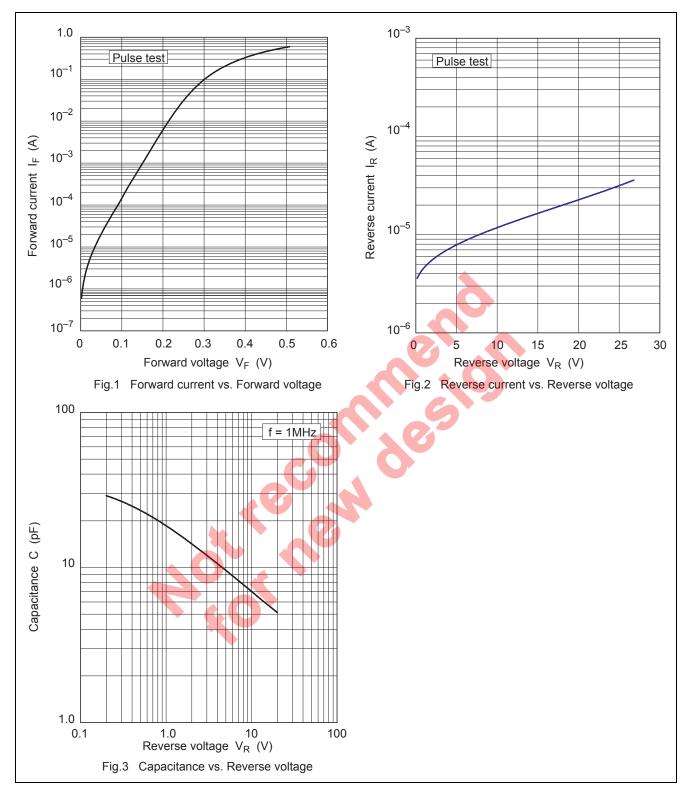
ltem	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _F		—	0.54	V	1 _F = 500 mA
Reverse current	I _R	_	_	200	μΑ	$V_R = 30 V$
Thermal resistance	Rth(j-a)	_	600		°C/W	Polyimide board * ¹
Thermal resistance	Rth(j-L)	_	150		°C/W	Glass-epoxy board * ²

Notes: 1. Polyimide board



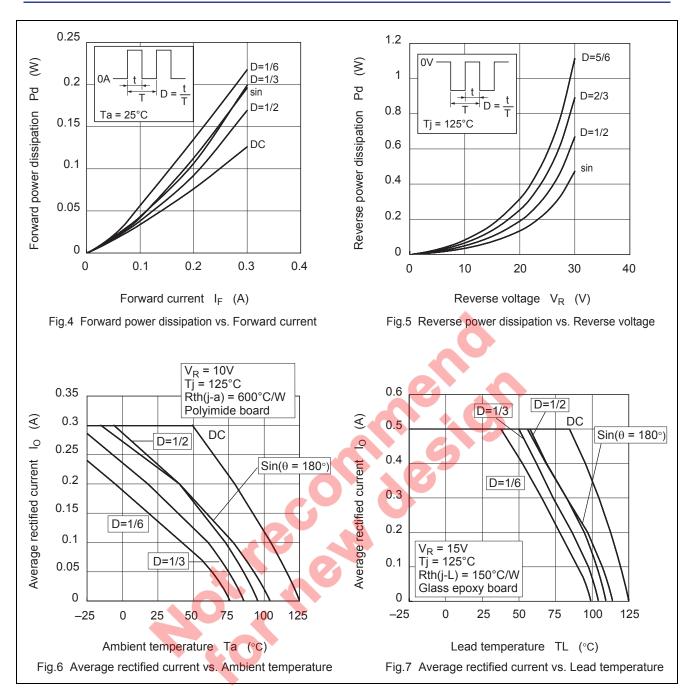
In the UFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, Note: the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristics

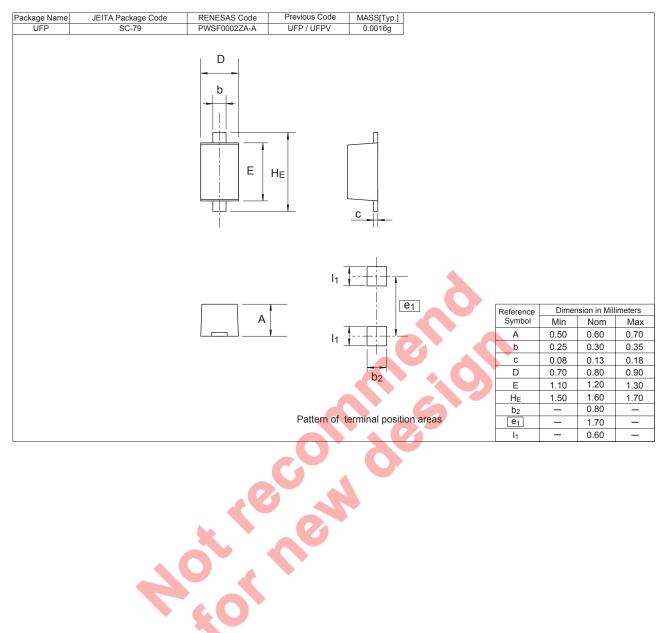


RENESAS

RKR0503AKJ



Package Dimensions



RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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 Image: State State



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April 1st, 2010 Renesas Electronics Corporation

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