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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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1S2076

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0559-0300

(Previous: ADE-208-145B)

Rev.3.00 Mar 16, 2005

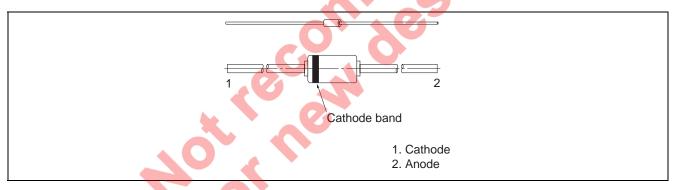
Features

- Low capacitance. (C = 3.0 pF max)
- Short reverse recovery time. $(t_{rr} = 8.0 \text{ ns max})$
- High reliability with glass seal.

Ordering Information

| Type No. | Cathode band | Package Name | Package Code (Previous Code) |
|----------|--------------|--------------|---------------------------------|
| 1S2076 | Light Blue | DO-35 | GRZZ0002ZB-A (DO-35) |

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Value | Unit |
|---|--------------------|-------------|------|
| Peak reverse voltage | V_{RM} | 35 | V |
| Reverse voltage | V _R | 30 | V |
| Peak forward current | I _{FM} | 450 | mA |
| Non-Repetitive peak forward surge current | I _{FSM} * | 1 | Α |
| Average rectified current | Io | 150 | mA |
| Power dissipation | Pd | 250 | mW |
| Junction temperature | Tj | 175 | °C |
| Storage temperature | Tstg | -65 to +175 | °C |

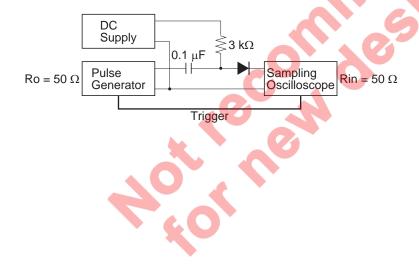
Note: Within 1s forward surge current.

Electrical Characteristics

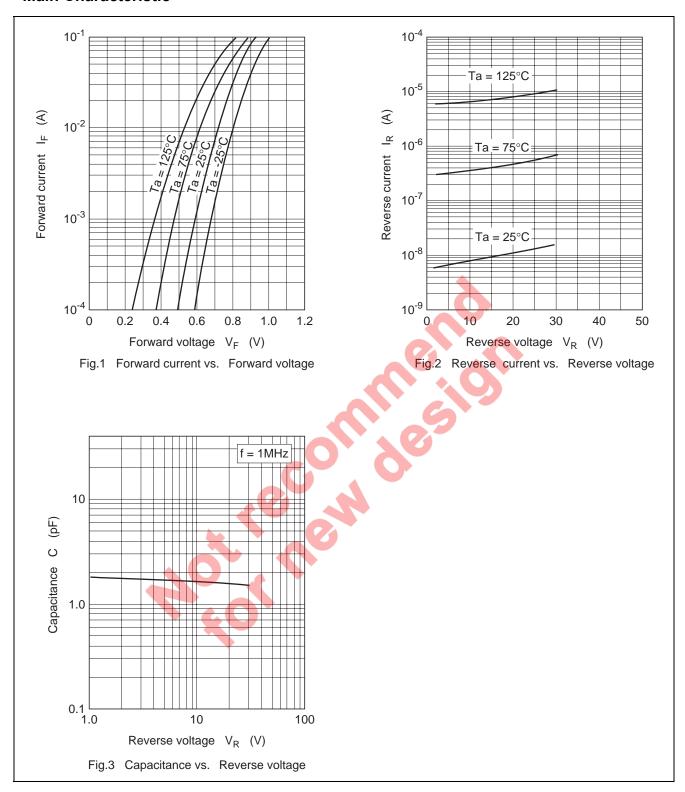
 $(Ta = 25^{\circ}C)$

| Item | Symbol | Min | Тур | Max | Unit | Test Condition |
|-----------------------|-------------------|------|-----|-----|------|---|
| Forward voltage | V_F | 0.64 | _ | 0.8 | V | I _F = 10 mA |
| Reverse current | I _R | _ | _ | 100 | nA | V _R = 30 V |
| Capacitance | С | _ | _ | 3.0 | pF | V _R = 1 V, f = 1 MHz |
| Reverse recovery time | t _{rr} * | _ | _ | 8.0 | ns | $I_F = I_R = 10 \text{ mA}, Irr = 1 \text{ mA}$ |

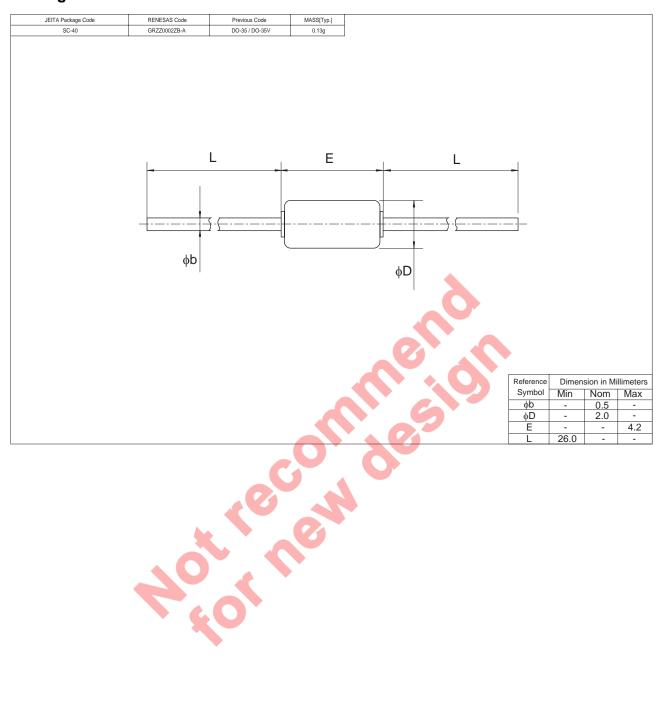
Note: Reverse recovery time test circuit



Main Characteristic



Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

- (ii) use of nonframmable material of (iii) prevention against any maintunction or misnap.

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