Zener Voltage Regulators

200 mW Micro Packaged

NZD5V1MU Series

This Zener diode is designed to provide voltage regulation protection and is especially attractive in situations where space is at a premium. Because of its small size, it is suited for use in mobile applications.

Specification Features:

- Standard Zener Breakdown Voltage Range: 2.4 V to 9.1 V
- Steady State Power Rating of 200 mW
- Small Body Outline Dimensions: 0.60 mm x 0.30 mm
- Low Body Height: 0.30 mm
- ESD Rating of Class 3 (>8 kV) per Human Body Model
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics: MOUNTING POSITION: Any

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

Device Meets MSL 1 Requirements

MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) @ T_A = 25°C	P _D	200	mW
Thermal Resistance from Junction-to-Ambient	$R_{\theta JA}$	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \times 0.75 \times 0.62$ in.



ON Semiconductor®

www.onsemi.com









X = Specific Device Code

M = Month Code

ORDERING INFORMATION

Device	Package	Shipping†
NZDxxxMUT5G	X3DFN (Pb-Free)	10000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

DEVICE MARKING INFORMATION

See specific marking information in the device marking column of the Electrical Characteristics tables starting on page 3 of this data sheet.

1

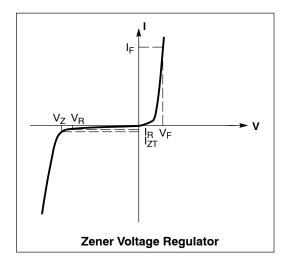
NZD5V1MU Series

ELECTRICAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise noted,}$

 $V_F = 1.1 \text{ V Max.} @ I_F = 10 \text{ mA for all types})$

Symbol	Parameter				
VZ	Reverse Zener Voltage @ I _{ZT}				
I _{ZT}	Reverse Current				
Z _{ZT}	Maximum Zener Impedance @ I _{ZT}				
I _{ZK}	I _{ZK} Reverse Current				
Z _{ZK} Maximum Zener Impedance @ I _{ZK}					
I _R	Reverse Leakage Current @ V _R				
V_{R}	Reverse Voltage				
I _F	Forward Current				
V _F	Forward Voltage @ I _F				
ΘV _Z	Maximum Temperature Coefficient of V _Z				
С	Max. Capacitance @V _R = 0 and f = 1 MHz				



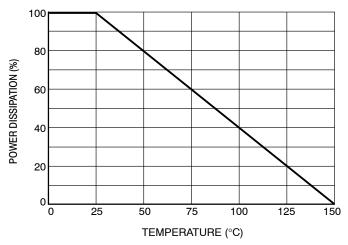


Figure 1. Steady State Power Derating

NZD5V1MU Series

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}C \ unless \ otherwise \ noted, \ V_F = 1.1 \ V \ Max. \ @ \ I_F = 10 \ mA \ for \ all \ types)$

			ner Volta (Note 1)	•	Zener Impedance			Leakage	Current			С
	Device	V _Z (V	olts)	@ I _{ZT}	Z _{ZT} @ I _{ZT}	Z _{ZK} (@ I _{ZK}	I _R @	V _R	Θ\ (mV/k)	_	@ V _R = 0 f = 1 MHz
Device	Marking	Min	Max	mA	Ω	Ω	mA	μΑ	Volts	Min	Max	рF
NZD2V4MUT5G	D***	2.28	2.52	5	100	1000	1	50	1	-3.5	0	210
NZD2V7MUT5G	E***	2.57	2.84	5	100	1000	1	20	1	-3.5	0	210
NZD3V0MUT5G	Q*	2.85	3.15	5	100	1000	1	10	1	-3.5	0	210
NZD3V3MUT5G	F***	3.14	3.47	5	100	1000	1	10	1	-3.5	0	210
NZD3V6MUT5G	J***	3.42	3.78	5	100	1000	1	10	1	-3.5	0	210
NZD3V9MUT5G	L	3.71	4.10	5	100	1000	1	5	1	-3.5	-2.5	210
NZD4V3MUT5G	D**	4.09	4.52	5	100	1000	1	5	1	-3.5	0	210
NZD4V7MUT5G	Р	4.47	4.94	5	100	800	0.5	2	1	-3.5	0.2	150
NZD5V1MUT5G	Q	4.85	5.36	5	80	500	0.5	2	1.5	-2.7	1.2	130
NZD5V6MUT5G	R	5.32	5.88	5	60	200	0.5	1	2.5	-2.0	2.5	115
NZD6V2MUT5G	Т	5.89	6.51	5	60	100	0.5	1	3.0	0.4	3.7	110
NZD6V8MUT5G	K***	6.46	7.14	5	40	60	0.5	0.5	3.5	1.2	4.5	105
NZD7V5MUT5G	L***	7.13	7.88	5	30	60	0.5	0.5	4.0	2.5	5.3	100
NZD8V2MUT5G	2	7.79	8.61	5	30	60	0.5	0.5	5.0	3.2	6.2	90
NZD9V1MUT5G	E**	8.65	9.56	5	30	60	0.5	0.5	6.0	3.8	7.0	80

^{*}Rotated 90°.

**Rotated 180°.

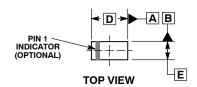
***Rotated 270°.

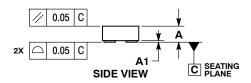
^{1.} Zener voltage is measured with a pulse test current I_Z at an ambient temperature of 25°C.

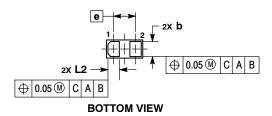
NZD5V1MU Series

PACKAGE DIMENSIONS

X3DFN2, 0.62x0.32, 0.355P, (0201) CASE 152AF ISSUE A





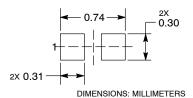


NOTES:

- DIMENSIONING AND TOLERANCING PER
 ASME V14 5M 1994
- ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS.

	MILLIMETERS					
DIM	MIN	MAX				
Α	0.25	0.33				
A1		0.05				
b	0.22	0.28				
D	0.58	0.66				
Е	0.28	0.36				
е	0.355 BSC					
L2	0.17	0.23				

RECOMMENDED MOUNTING FOOTPRINT*



See Application Note AND8398/D for more mounting details

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and the are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative