# **Schottky Barrier Rectifier**

5 A, 40 V Low VF

## **FSV540**

#### **Features**

- Low Forward Voltage Drop
- Low Thermal Resistance
- Very Low Profile: Typical Height of 1.1 mm
- Green Molding Compound as per IEC61249 Standard
- Non-DAP Option Only
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

#### **Specifications**

### **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub> = 25°C unless otherwise noted)

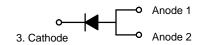
Symbol	Parameter	Value	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage	40	V
V <sub>RMS</sub>	RMS Reverse Voltage	28	V
V <sub>R</sub>	DC Blocking Voltage	40	V
I <sub>F(AV)</sub>	Average Rectified Peak Forward Surge Current at T <sub>A</sub> = 75°C	5	Α
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	150	А
TJ	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

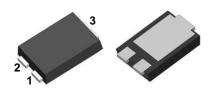


## ON Semiconductor®

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**Schottky Barrier Rectifier** 



TO-277-3LD CASE 340BQ

## **MARKING DIAGRAM**

\$Y&Z&3 FSV540

\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Date Code (Year & Week) FSV540 = Specific Device Code

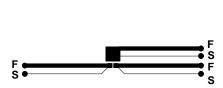
#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

### THERMAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (Note 1)

Symbol	Characteristic	Minimum Land Pattern	Maximum Land Pattern	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance	100	40	°C/W
$\Psi_{JL}$	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Anode	15	12	°C/W
	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Cathode	6	5	

<sup>1.</sup> The thermal resistances ( $R_{\theta JA} \& \Psi_{JL}$ ) are characterized with device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm. Minimum land pattern size: 4.9 x 4.8 mm (big pattern, x1), 1.4 x 1.52 mm (small pattern, x2). Maximum land pattern size: 30 x 30 mm (pattern, x2). Force line trace size = 55 mils, sense line trace size = 4 mils.



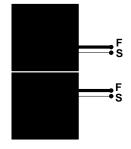


Figure 1. Minimum Land Pattern of 2 oz Copper

Figure 2. Maximum Land Pattern of 2 oz Copper

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BV <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 0.5 mA	40	-	_	V
V <sub>F</sub>	Forward Voltage Drop	I <sub>F</sub> = 5 A	-	474	520	mV
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 40 V	-	25	250	μΑ
CJ	Junction Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	-	730	_	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## **ORDERING INFORMATION**

Part Number	Top Mark	Package	Shipping <sup>†</sup>
FSV540	FSV540	TO-277-3LD (Pb-Free/Halogen Free)	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## TYPICAL PERFORMANCE CHARACTERISTICS

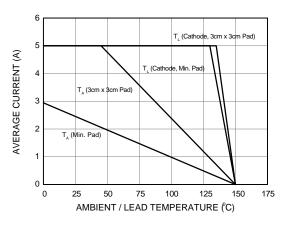


Figure 3. Forward Current Derating Curve

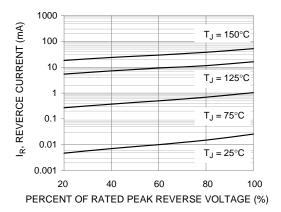


Figure 5. Typical Reverse Characteristic

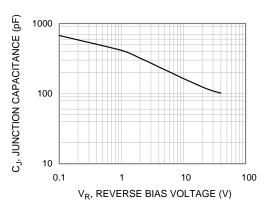


Figure 4. Typical Junction Capacitance

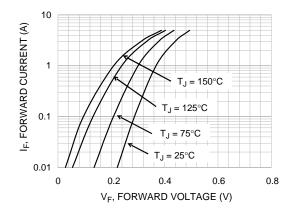
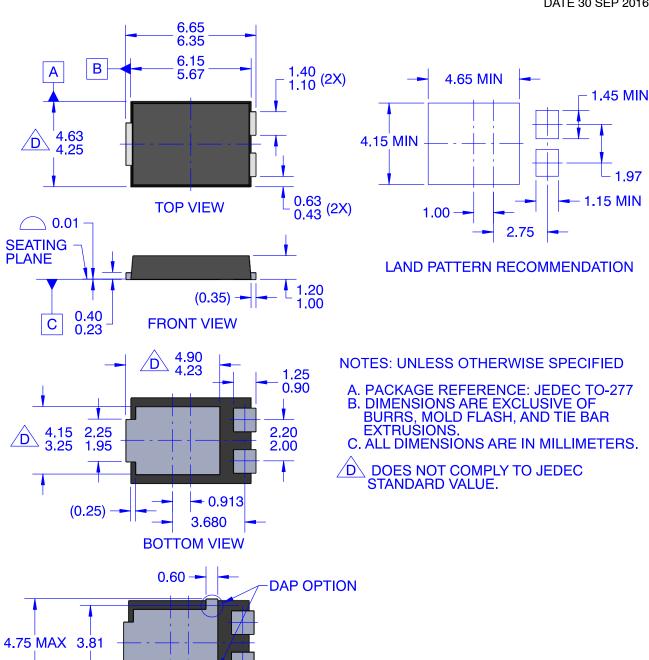


Figure 6. Typical Reverse Characteristic



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**BOTTOM VIEW - DAP OPTION** 

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