# 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 60 V
- · Low forward voltage
- · High power capability due to clip-bonding technology
- · Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T<sub>i</sub> ≤ 175 °C
- · Capable for reflow and wave soldering

## 3. Applications

- · Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- · Reverse polarity protection

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F</sub>	forward current	T <sub>sp</sub> = 165 °C		-	-	2.8	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; $T_{amb} \le 120 ^{\circ}\text{C}$ ; square wave	[1]	-	-	2	А
		$\delta$ = 0.5; f = 20 kHz; $T_{sp} \le 170 ^{\circ}\text{C}$ ; square wave		-	-	2	А
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	60	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 2 A; T <sub>j</sub> = 25 °C		-	460	530	mV
I <sub>R</sub>	reverse current	$V_R = 60 \text{ V; } t_p \le 300  \mu\text{s; } \delta \le 0.02;$ $T_j = 25 \text{ °C; pulsed}$		-	60	150	μΑ
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$		-	8.6	-	ns

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		к <del>_[(-)</del> А
2	А	anode	1	sym001
			CFP5 (SOD 126)	

<sup>[1]</sup> The marking bar indicates the cathode.

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMEG6020ETP	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128			

# 7. Marking

### Table 4. Marking codes

Type number	Marking code
PMEG6020ETP	D9

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	60	V
l <sub>F</sub>	forward current	T <sub>sp</sub> = 165 °C		-	2.8	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; $T_{amb} \le 120$ °C; square wave	[1]	-	2	А
		$\delta$ = 0.5; f = 20 kHz; $T_{sp} \le 170$ °C; square wave		-	2	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	50	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	750	mW
			[3]	-	1250	mW
			[1]	-	2500	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
from j	thermal resistance		[1] [2]	-	-	200	K/W
	from junction to ambient		[1] [3]	-	-	120	K/W
			[1] [4]	-	-	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [5] Soldering point of cathode tab.

### High-temperature 60 V, 2 A Schottky barrier rectifier

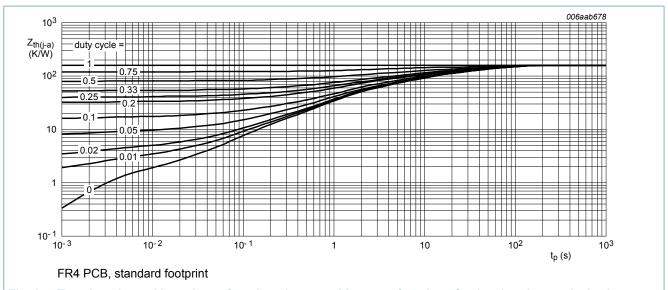


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

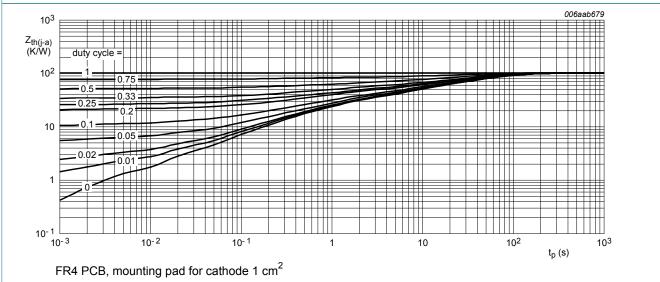
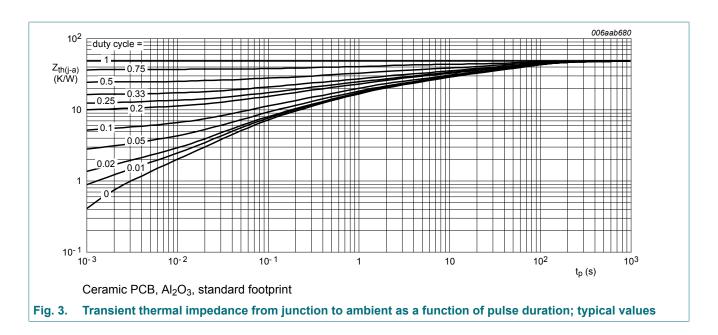


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### High-temperature 60 V, 2 A Schottky barrier rectifier



### 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	300	340	mV	
		I <sub>F</sub> = 0.5 A; T <sub>j</sub> = 25 °C	-	360	420	mV	
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	400	460	mV	
		I <sub>F</sub> = 1.5 A; T <sub>j</sub> = 25 °C	-	430	500	mV	
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 25 °C	-	460	530	mV	
		I <sub>F</sub> = 2 A; T <sub>j</sub> = -40 °C	-	500	590	mV	
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 125 °C	-	395	480	mV	
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 150 °C	-	380	460	mV	
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 175 °C	-	360	450	mV	
I <sub>R</sub>	reverse current	$V_R$ = 5 V; $t_p \le 300 \text{ μs}$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C; pulsed	-	2.5	-	μA	
		$V_R$ = 10 V; $t_p \le 300  \mu s$ ; δ ≤ 0.02; $T_j$ = 25 °C; pulsed	-	3.5	-	μA	
			$V_R$ = 60 V; $t_p \le 300  \mu s$ ; δ ≤ 0.02; $T_j$ = 25 °C; pulsed	-	60	150	μΑ
		$V_R$ = 60 V; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = -40 °C; pulsed	-	0.9	15	μA	
		$V_R$ = 60 V; $t_p \le 300 \mu s$ ; δ ≤ 0.02; $T_j$ = 125 °C; pulsed	-	27	100	mA	
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	240	-	pF	
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	80	-	pF	
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	8.6	-	ns	
$V_{FRM}$	peak forward recovery voltage	$I_F = 1 \text{ A}; \text{ d}I_F/\text{d}t = 40 \text{ A}/\mu\text{s}; T_j = 25 °C$	-	401	-	mV	

#### High-temperature 60 V, 2 A Schottky barrier rectifier

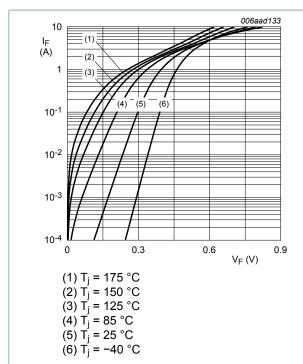


Fig. 4. Forward current as a function of forward voltage; typical values

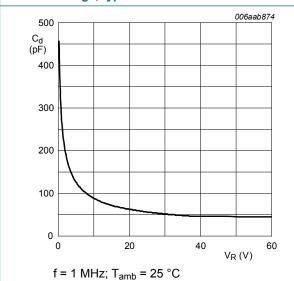


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

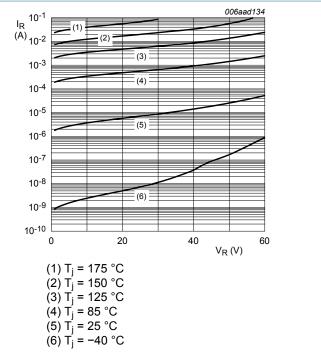
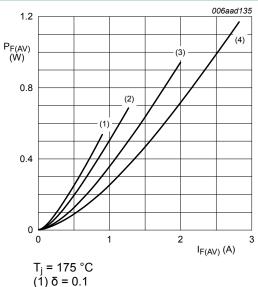


Fig. 5. Reverse current as a function of reverse voltage; typical values



 $I_j = 175 \,^{\circ}\text{C}$   $(1) \, \delta = 0.1$   $(2) \, \delta = 0.2$   $(3) \, \delta = 0.5$  $(4) \, \delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

#### High-temperature 60 V, 2 A Schottky barrier rectifier

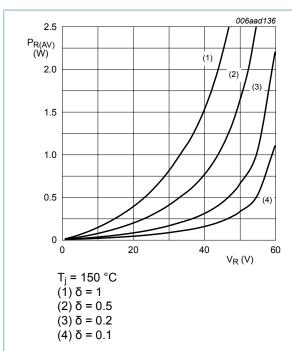


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values

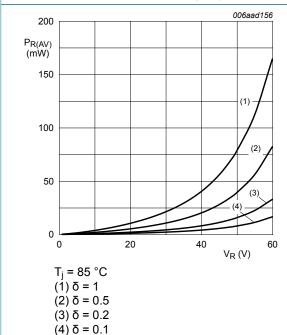


Fig. 10. Average reverse power dissipation as a function of reverse voltage; typical values

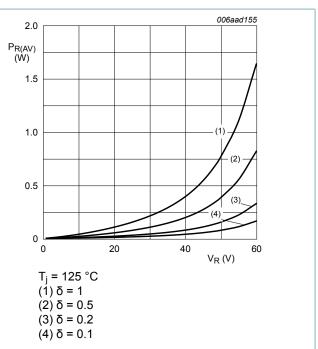


Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values

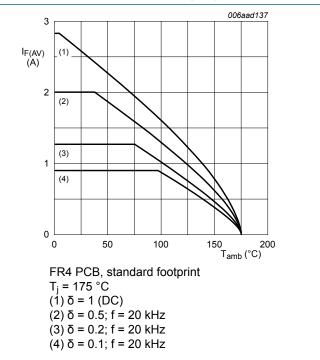
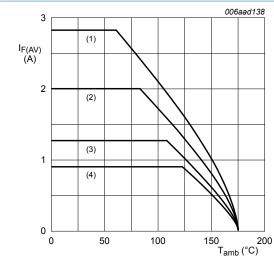


Fig. 11. Average forward current as a function of ambient temperature; typical values

#### High-temperature 60 V, 2 A Schottky barrier rectifier



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

T<sub>i</sub> = 175 °C

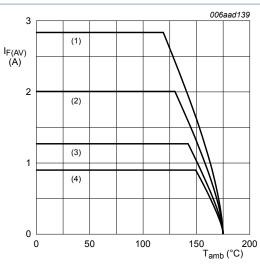
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

 $T_i = 175$  °C

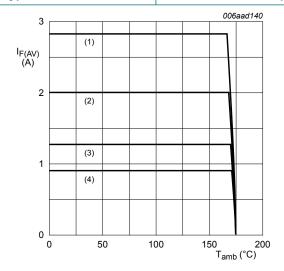
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 13. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 175 °C

 $(1) \delta = 1 (DC)$ 

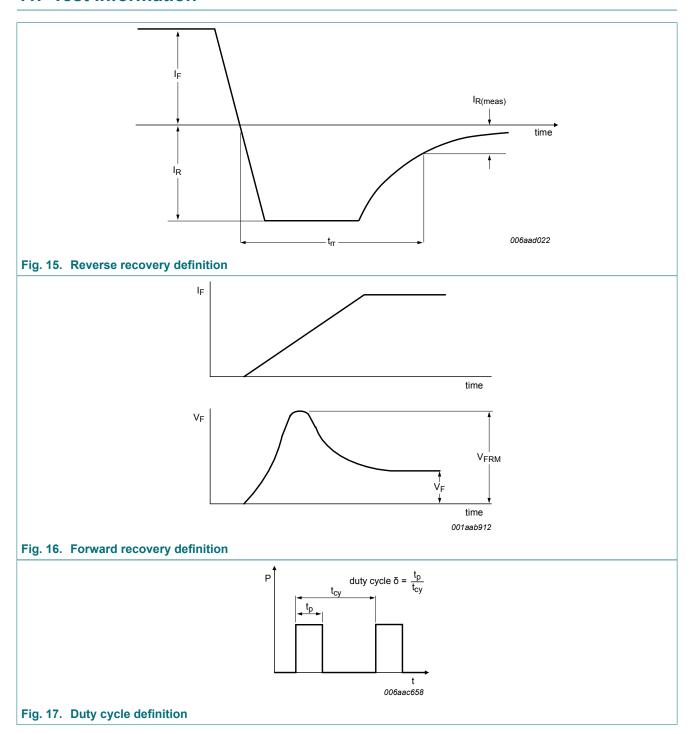
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 14. Average forward current as a function of solder point temperature; typical values

### 11. Test information



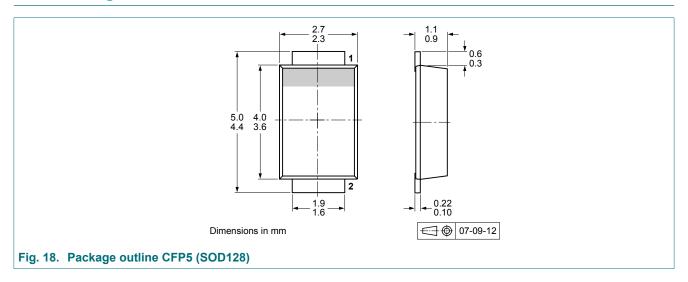
The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

### High-temperature 60 V, 2 A Schottky barrier rectifier

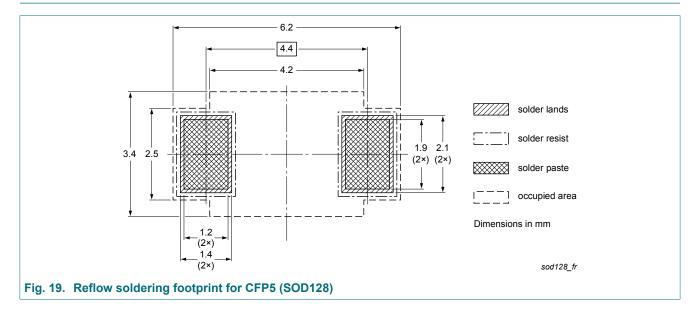
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

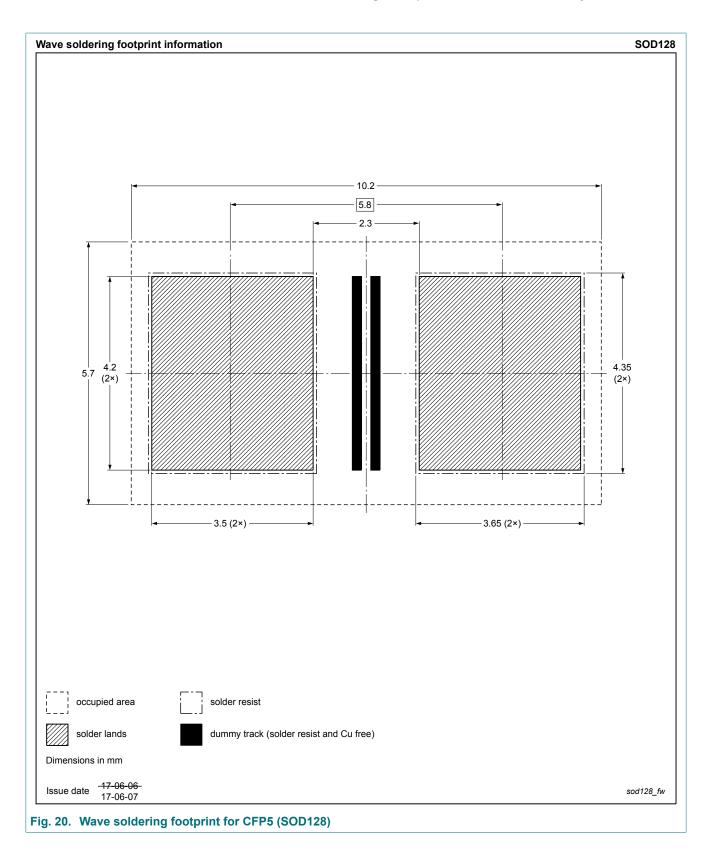
# 12. Package outline



# 13. Soldering



### High-temperature 60 V, 2 A Schottky barrier rectifier



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# 14. Revision history

#### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG6020ETP v.2	20180528	Product data sheet	-	PMEG6010EP v.1			
Modifications:	<ul> <li>Features and benefits: Capable for reflow and wave soldering added</li> <li>Soldering: Wave soldering footprint added</li> </ul>						
PMEG6020ETP v.1	20121011	Product data sheet	-	-			

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# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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